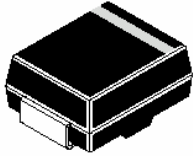


**SURFACE MOUNT SCHOTTKY BARRIER RECTIFIERS**

**SS12 - SS110**



**DO-214AC (SMA)  
Surface Mount Package**

For Use in Low Voltage High Frequency Inverters, Free Wheeling and Polarity Protection Application

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**

(Rating at 25°C Ambient Temperature unless specified otherwise. Single Phase, half wave Resistive or Inductive Load. For Capacitive Load, Derate by 20%)

DESCRIPTION	SYMBOL	SS12	SS13	SS14	SS15	SS16	SS18	SS110	UNIT
Maximum Peak Repetitive Reverse Voltage	$V_{RRM}$	20	30	40	50	60	80	100	V
Maximum RMS Voltage	$V_{RMS}$	14	21	28	35	42	56	70	V
Maximum DC Blocking Voltage	$V_{DC}$	20	30	40	50	60	80	100	V
Maximum Average Forward Rectified Current at $T_L=100^\circ\text{C}$	$I_{(AV)}$	1.0							A
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	$I_{FSM}$	40							A
Maximum Voltage at $I_F=1.0\text{A}$	$V_F$	0.45	0.55	0.6	0.70		0.85		V
Maximum DC Reverse Current $T_j=25^\circ\text{C}$	$I_R$	1.0							mA
at Rated DC Blocking Voltage $T_j=100^\circ\text{C}$		10							mA
Junction Capacitance	$*C_J$	TYP110							pF
Thermal Resistance Junction to Lead	$R_{th(J-L)}$	TYP20							$^\circ\text{C/W}$
Operating Junction Temperature Range	$T_j$	- 55 to +150							$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to +150							$^\circ\text{C}$

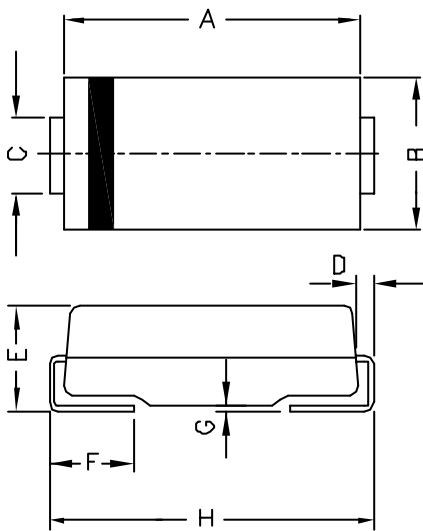
\*Measured at 1MHz and applied reverse voltage of 4.0 V DC

SS12\_SS110Rev\_1 280907E

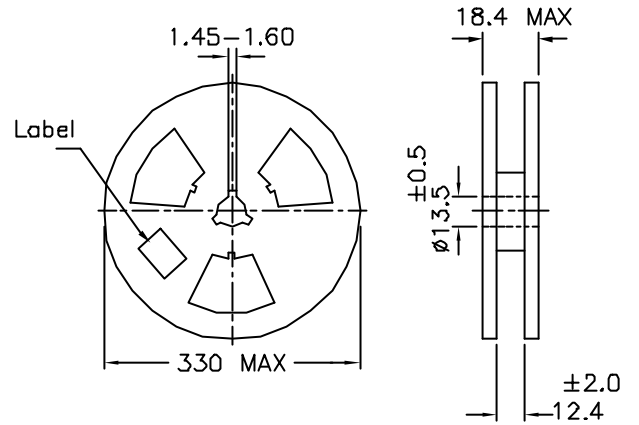
**SS12 - SS110**

**DO-214AC (SMA)  
Surface Mount Package**

DO-214AC (SMA) Package & Reel Taping Specification



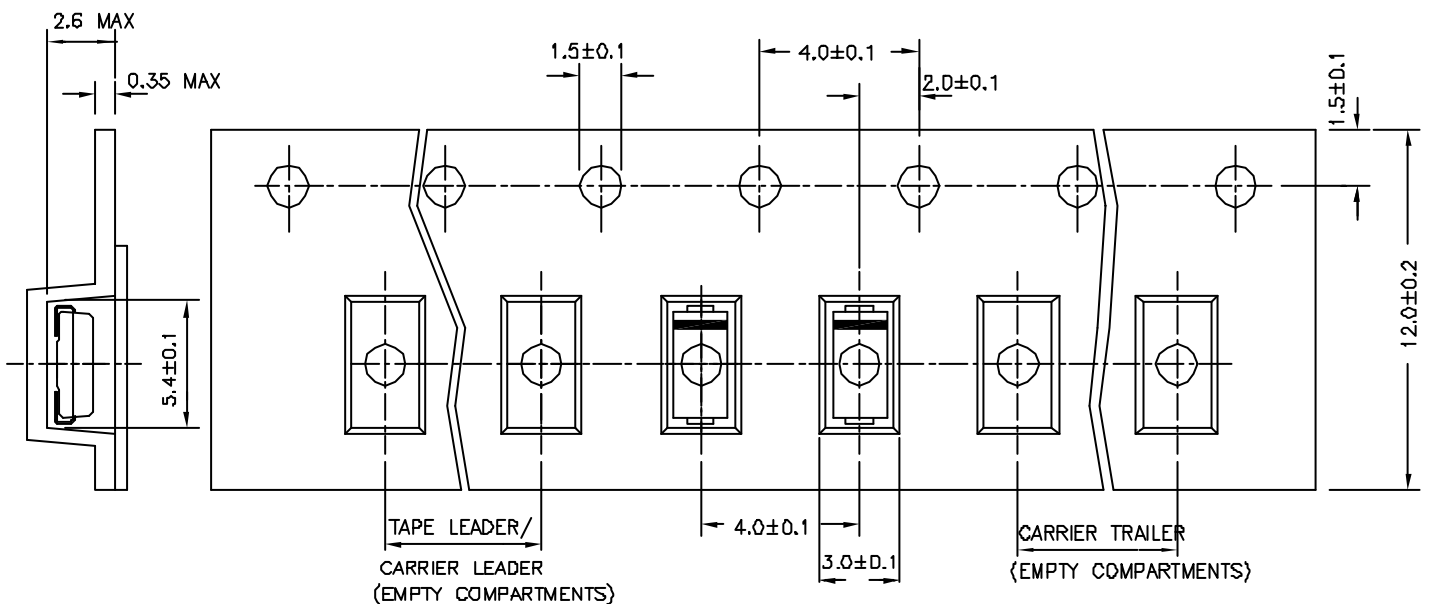
DIM	MIN.	MAX.
A	3.95	4.60
B	2.18	2.95
C	1.25	1.70
D	0.15	0.31
E	1.70	2.90
F	0.75	1.60
G	0.050	0.203
H	4.70	5.65



ALL DIMENSIONS ARE IN mm

Packing:- 5K/Reel

➔ De-reeling direction



ALL DIMENSIONS ARE IN mm

**Component Disposal Instructions**

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

**Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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