

SSCE7V012L1

Bi-directional 7V Normal Capacitance ESD Protector

Description

The SSCE7V012L1 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.

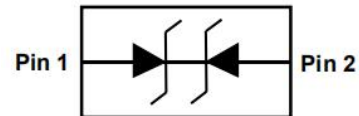
Feature

- ✧ DFN0603-2L package
- ✧ Replacement for MLV(0201)
- ✧ Bidirectional configurations
- ✧ Response time is typically < 1 ns
- ✧ Low clamping voltage
- ✧ RoHS compliant
- ✧ Transient protection for data lines to IEC 61000-4-2(ESD) $\pm 20\text{KV}(\text{air})$, $\pm 20\text{KV}(\text{contact})$; IEC 61000-4-4 (EFT) 40A (5/50ns)

Electronic Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_{R}	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_{T}
I_{T}	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_{C}	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_{J}	Junction Capacitance
I_{F}	Forward Current
V_{F}	Forward Voltage @ I_{F}

PIN configuration

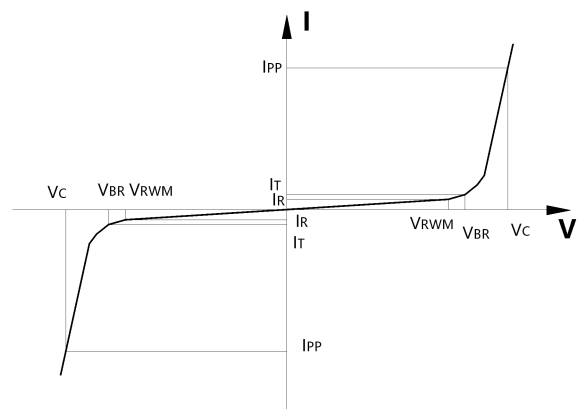


Applications

- ✧ Cellular phones
- ✧ Portable devices
- ✧ Digital cameras
- ✧ Power supplies

Mechanical data

- ✧ Lead finish: 100% matte Sn(Tin)
- ✧ Mounting position: Any
- ✧ Qualified max reflow temperature: 260°C
- ✧ Device meets MSL 1 requirements
- ✧ Pure tin plating: 7 ~ 17 μm
- ✧ Pin flatness: $\leq 3\text{mil}$





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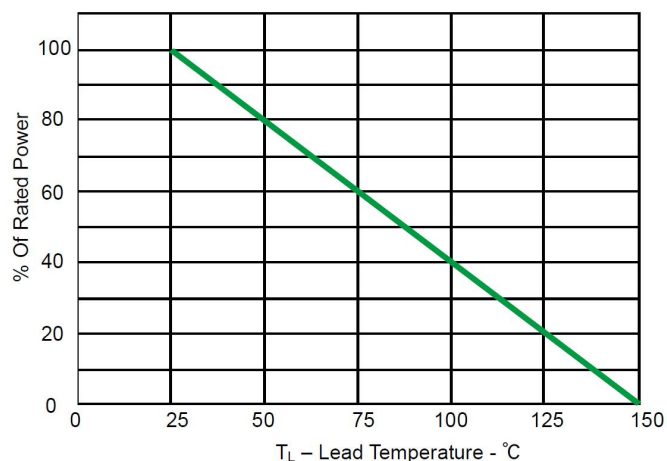
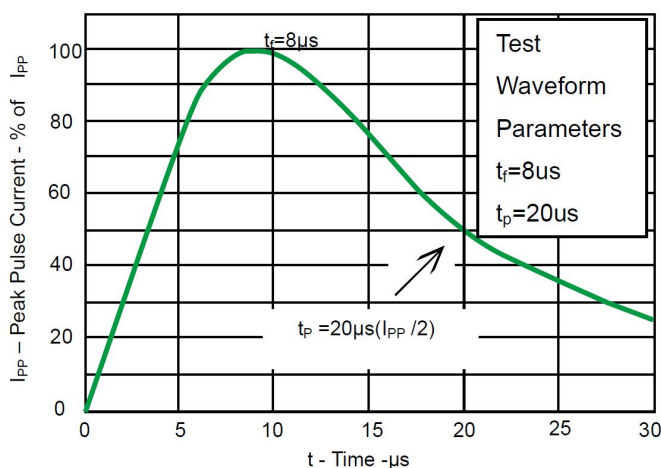
- **Absolute maximum rating @TA=25°C**

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (8/20μS)	110	W
I _{PP}	Peak Pulse Current (8/20μS)	6	A
T _{STG}	Storage Temperature	-55/+150	°C
T _J	Operating Temperature	-55/+150	°C

- **Electrical Characteristics @TA=25°C**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}				7.0	V
Breakdown Voltage	V _{BR}	I _T = 1mA	7.5	9.0	10.0	V
Reverse Leakage Current	I _R	V _{RWM} = 7.0V, T = 25°C			1.0	μA
Clamping Voltage	V _C	I _{PP} = 2A, T _P = 8/20μs		13.0	14.0	V
		I _{PP} = 6A, T _P = 8/20μs		19.0	21.0	V
Junction Capacitance	C _J	V _R = 0V, f = 1MHz	11.0	13.0	15.0	pF

- **Typical Performance Characteristics**



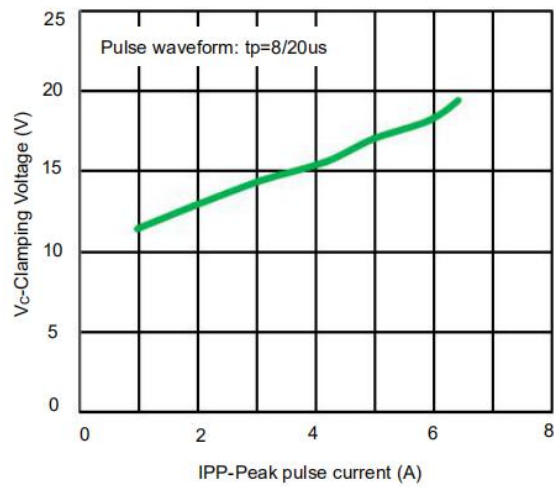


Fig 3. Clamping voltage vs. Peak pulse current

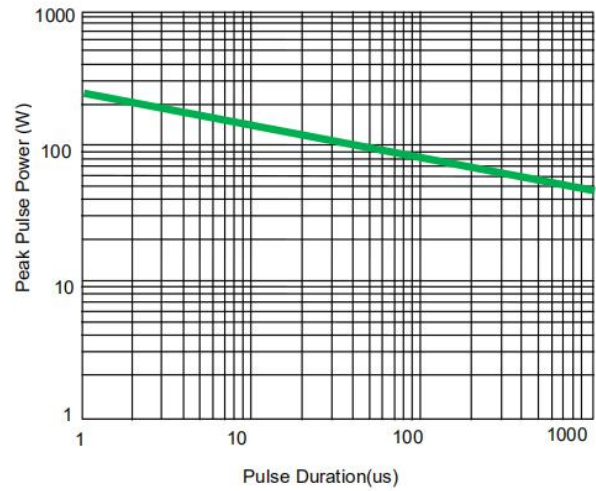


Fig 4. Non-Repetitive Peak Pulse Power vs. Pulse time

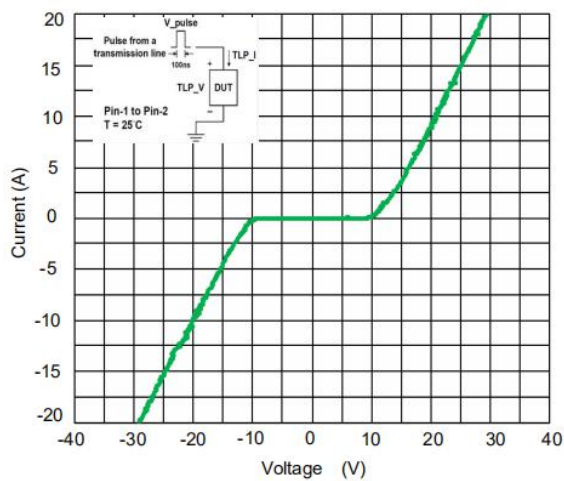


Fig 5. TLP Measurement

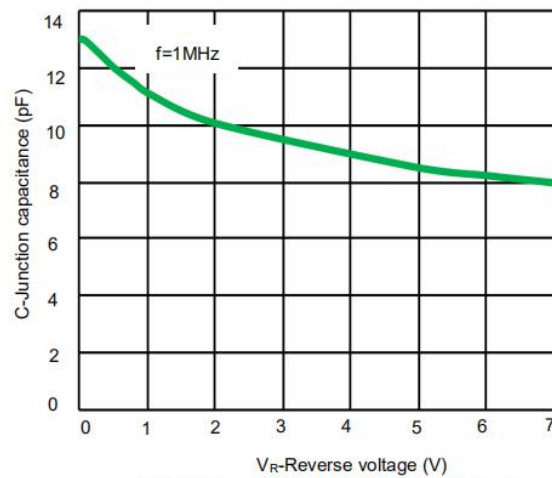


Fig 6. Capacitance vs. Reverse voltage

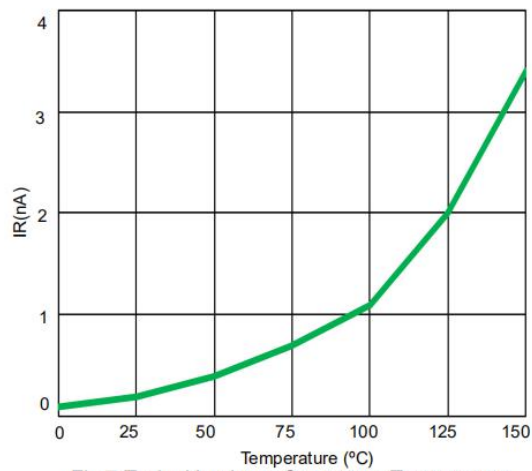


Fig 7. Typical Leakage Current vs. Temperature

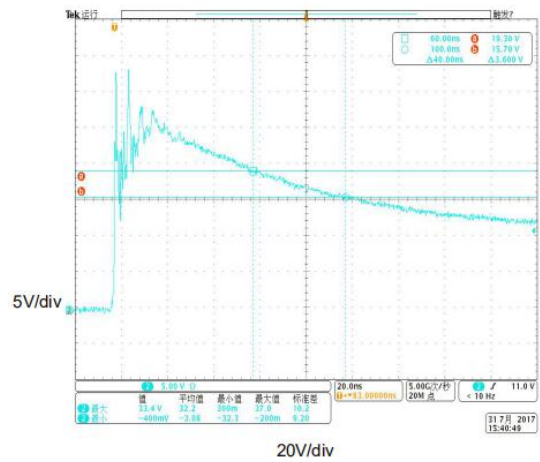


Fig 8. ESD clamping voltage
(IEC61000-4-2 +8KV contact)

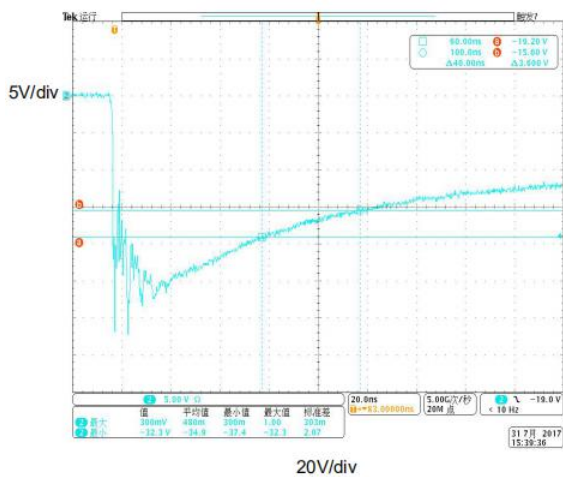


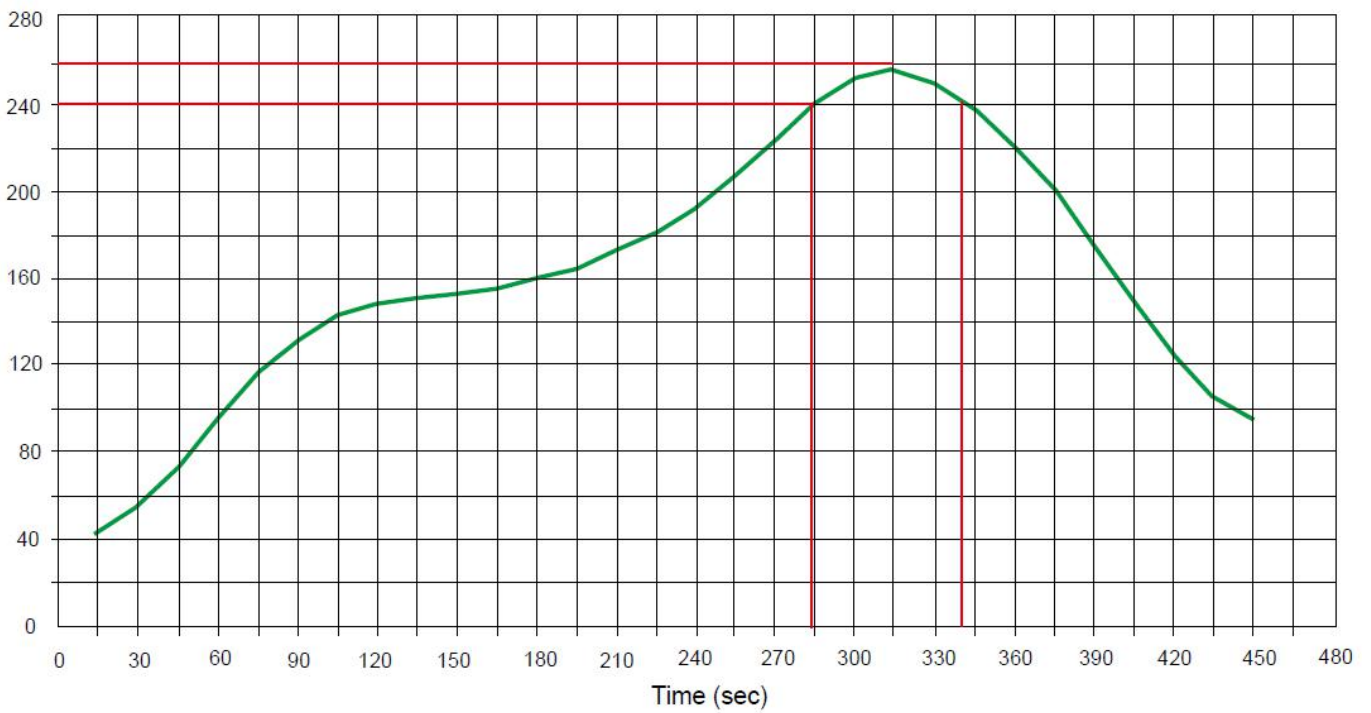
Fig 9. ESD clamping voltage
(IEC61000-4-2-8KV contact)



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● Solder Reflow Recommendation

Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec



● Package Information

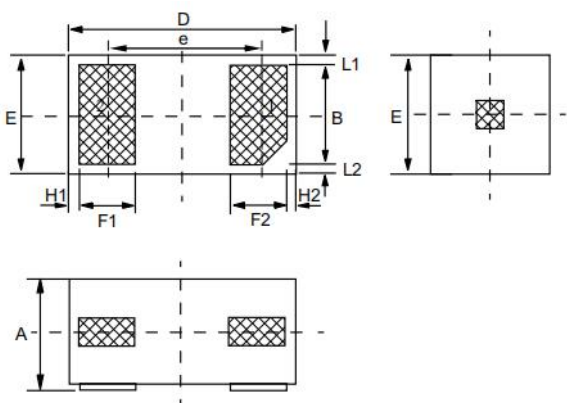
Ordering Information

Device	Package	Qty per Reel	Reel Size
SSCE7V012L1	DFN0603-2L(Pb-Free)	10000/Tape&Reel	7"

Mechanical Data

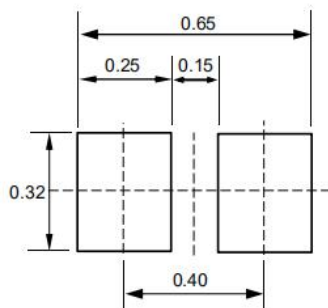
Case:DFN0603-2L

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters		
	Min	Typ	Max
A	0.270	0.300	0.340
B	0.200	0.250	0.300
D	0.550	0.600	0.650
E	0.250	0.300	0.350
e	-	0.350	-
F1	0.130	0.180	0.230
F2	0.130	0.180	0.230
L1	0.015	0.030	0.045
L2	0.015	0.030	0.045
H1	0.030	0.045	0.060
H2	0.030	0.045	0.060

Recommended Pad outline



Suggested PCB Layout



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