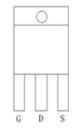
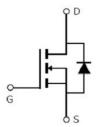


Main Product Characteristics:

V _{DSS}	900V
R _{DS} (on)	0.85Ω(typ.)
I _D	10A ①







TO-3P

Marking and pin
Assignment

Schematic diagram

Features and Benefits:

- Advanced MOSFET process technology
- Low On Resistance
- Low Gate Charge
- Fast switching and reverse body recovery



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute max Rating:

Symbol	Parameter	Max.	Units
I _D @ TC = 25℃	Continuous Drain Current, V _{GS} @ 10V ①	10	
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	7	Α
I _{DM}	Pulsed Drain Current ②	40	
D @TC = 25°C	Power Dissipation ③	170	W
P _D @TC = 25°C	Linear Derating Factor	1.36	W/℃
V _{DS}	Drain-Source Voltage	900	V
V _{GS}	Gate-to-Source Voltage	± 30	V
Eas	Single Pulse Avalanche Energy @ L=20mH	300	mJ
I _{AS}	Avalanche Current @ L=20mH	5.5	Α
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	$^{\circ}$



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	0.73	°C/W
$R_{\theta JA}$	Junction-to-ambient (t $\leq 10s)$ (4)	_	40	°C/W

Electrical Characterizes $@T_A=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	900	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	0.85	1.3	Ω	V _{GS} =10V,I _D = 4.5A
V _{GS(th)}	Gate threshold voltage	2	_	4	V	V _{DS} = V _{GS} , I _D = 250μA
	Drain to Source leakage current	_	_	1		V _{DS} =900V,V _{GS} = 0V
I _{DSS}	Drain-to-Source leakage current	_	_	500	μA	T _J = 125℃
	Cata to Source forward lookage	_	_	100	- Α	V _{GS} =30V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -30V
Qg	Total gate charge	_	66	_		I _D = 10A,
Qgs	Gate-to-Source charge	_	13	_	nC	V _{DS} =450V,
Q _{gd}	Gate-to-Drain("Miller") charge	_	25	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time	_	18	_		V _{GS} =10V, V _{DS} =450V,
tr	Rise time	_	28	_		R _L =45Ω,
t _{d(off)}	Turn-Off delay time	_	49	_	nS	R _{GEN} =4.7Ω
t _f	Fall time	_	34	_		I _D =10A
C _{iss}	Input capacitance	_	2956	_		V _{GS} = 0V
Coss	Output capacitance	_	106	_	pF	V _{DS} = 25V
C _{rss}	Reverse transfer capacitance	_	18	_]	f = 1MHz

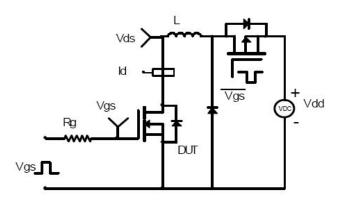
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current (Body Diode)	_	_	10 ①	А	MOSFET symb
I _{SM}	Pulsed Source Current (Body Diode)	_	_	40	А	integral reverse p-n junction diode.
V_{SD}	Diode Forward Voltage	_	_	1.5	V	I _S =10A, V _{GS} =0V
trr	Reverse Recovery Time	_	265	_	nS	$T_J = 25 ^{\circ}\mathrm{C}$, $I_F = 10A$, di/dt =
Q _{rr}	Reverse Recovery Charge	_	1.7	_	nC	100A/µs

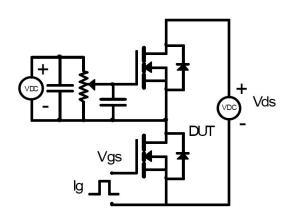


Test circuits and Waveforms

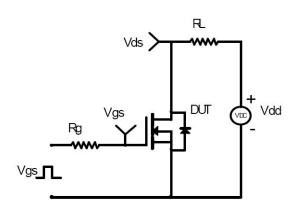
EAS Test Circuit:



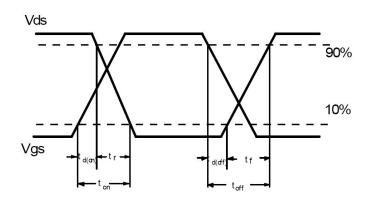
Gate charge test circuit:



Switching Time Test Circuit:



Switching Waveforms:

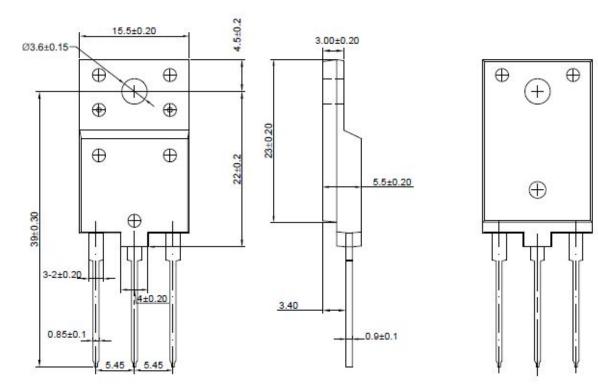


Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- ⓐThe value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25 °C



Mechanical Data:







Ordering and Marking Information

Device Marking: SSF10N90F1

Package (Available)
TO-3P
Operating Temperature Range
C: -55 to 150 ℃

Devices per Unit

Package	Units/	Tubes/Inner	Units/Inner	Inner	Units/Carton
Type	Tube	Box	Box	Boxes/Carton	Box
				Box	

Reliability Test Program

Test Item	Conditions	Duration	Sample Size
High	T _j =125℃ to 150℃ @	168 hours	3 lots x 77 devices
Temperature	80% of Max	500 hours	
Reverse	V _{DSS} /V _{CES} /VR	1000 hours	
Bias(HTRB)			
High	T _j =150℃ or 150℃ @	168 hours	3 lots x 77 devices
Temperature	100% of Max V _{GSS}	500 hours	
Gate		1000 hours	
Bias(HTGB)			





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Preliminary Version: 1.0

page 6 of 6