

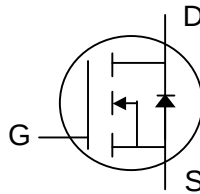
N-channel Enhancement-mode Power MOSFET

Low gate-charge

Simple drive requirement

Fast switching

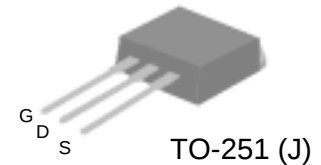
 **Pb-free; RoHS compliant.**



BV_{DSS}	30V
$R_{DS(ON)}$	4mΩ
I_D	75A

DESCRIPTION

The SSM90T03GH is in a TO-252 package, which is widely used for commercial and industrial surface mount applications, and is well suited for low voltage applications such as DC/DC converters. The through-hole version, the SSM90T03GJ in TO-251, is available for low-footprint vertical mounting. These devices are manufactured with an advanced process, providing improved on-resistance and switching performance.



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D @ T_C=25^{\circ}C$	Continuous Drain Current, $V_{GS} @ 4.5V$	75	A
$I_D @ T_C=100^{\circ}C$	Continuous Drain Current, $V_{GS} @ 4.5V$	63	A
I_{DM}	Pulsed Drain Current ¹	350	A
$P_D @ T_C=25^{\circ}C$	Total Power Dissipation	96	W
	Linear Derating Factor	0.7	W/°C
E_{AS}	Single Pulse Avalanche Energy ³	29	mJ
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

THERMAL DATA

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case	Max. 1.3	°C/W
Rthj-a	Thermal Resistance Junction-ambient	Max. 110	°C/W

ELECTRICAL CHARACTERISTICS @ T_j=25°C (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	-	-	V
ΔBV _{DSS} / ΔT _j	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =1mA	-	0.015	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =45A	-	-	4	mΩ
		V _{GS} =4.5V, I _D =30A	-	-	6	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.8	-	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =30A	-	55	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
	Drain-Source Leakage Current (T _j =150°C)	V _{DS} =24V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =40A	-	60	96	nC
Q _{gs}	Gate-Source Charge	V _{DS} =24V	-	8.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	38	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =15V	-	14	-	ns
t _r	Rise Time	I _D =30A	-	83	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =10V	-	66	-	ns
t _f	Fall Time	R _D =0.5Ω	-	120	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	4090	6540	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	1010	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	890	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =45A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =30A, V _{GS} =0V,	-	51	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	63	-	nC

Notes:

- 1.Pulse width limited by safe operating area.
- 2.Pulse width ≤300us , duty cycle ≤2%.
- 3.V_{DD}=25V , L=100uH , R_G=25Ω , I_{AS}=24A.

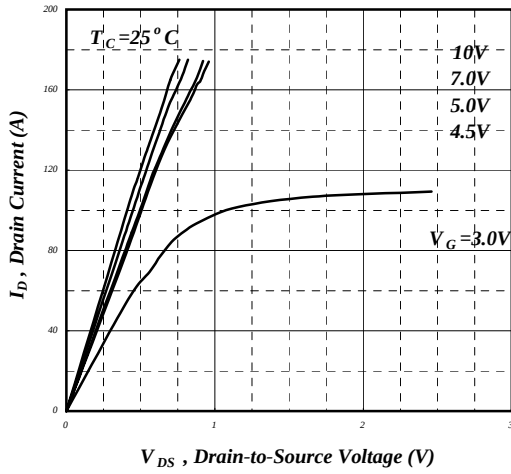


Fig 1. Typical Output Characteristics

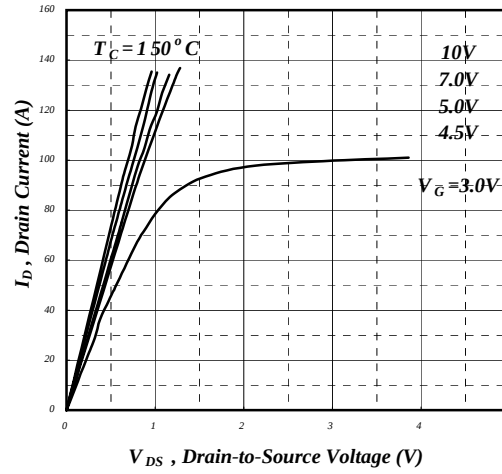


Fig 2. Typical Output Characteristics

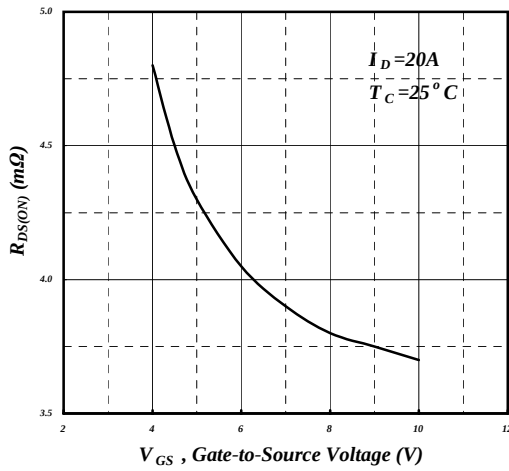


Fig 3. On-Resistance vs. Gate Voltage

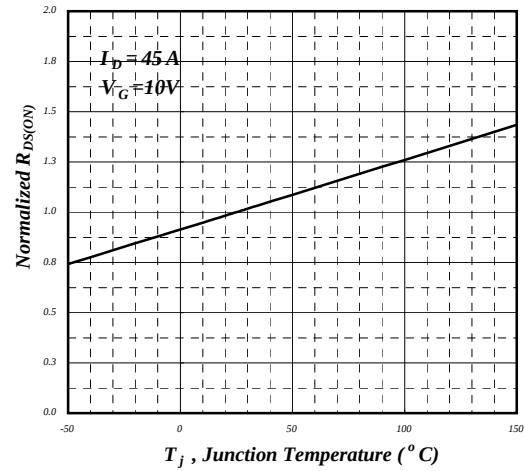


Fig 4. Normalized On-Resistance vs. Junction Temperature

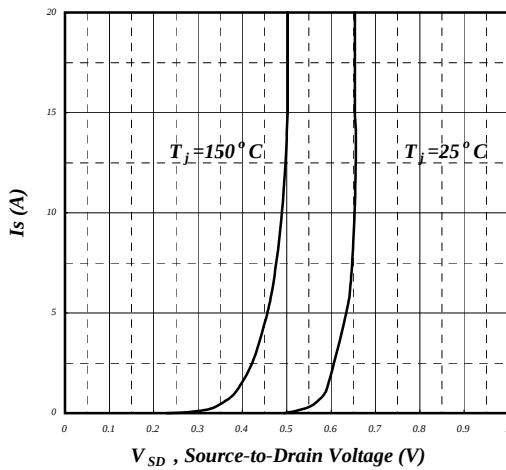


Fig 5. Forward Characteristic of Reverse Diode

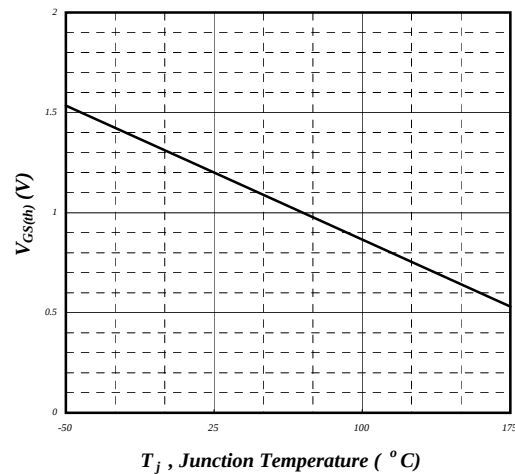


Fig 6. Gate Threshold Voltage vs. Junction Temperature

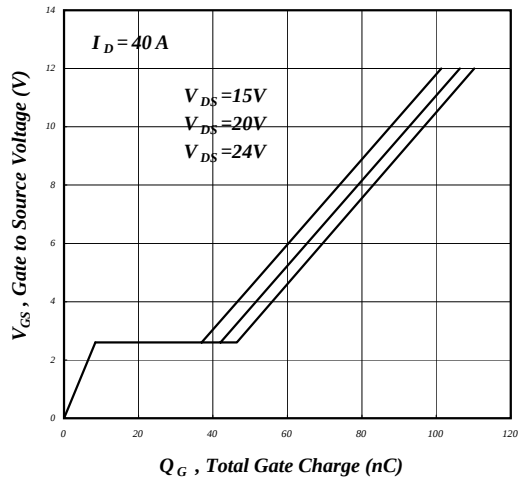


Fig 7. Gate Charge Characteristics

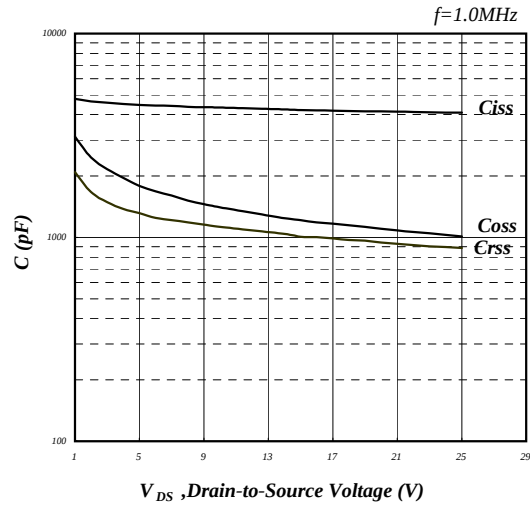


Fig 8. Typical Capacitance Characteristics

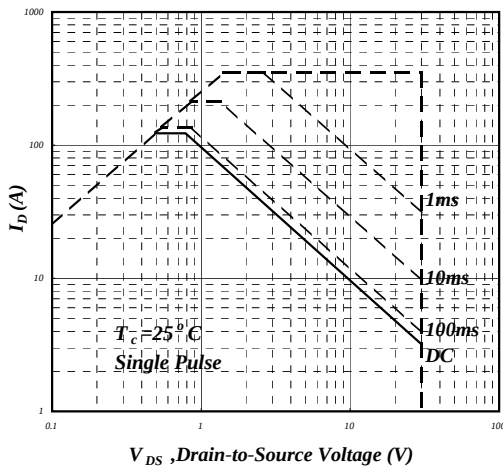


Fig 9. Maximum Safe Operating Area

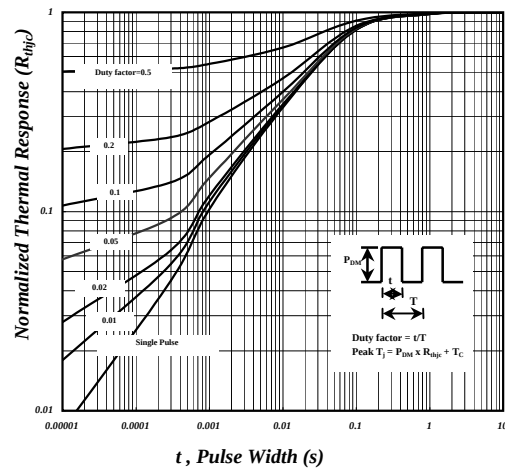


Fig 10. Effective Transient Thermal Impedance

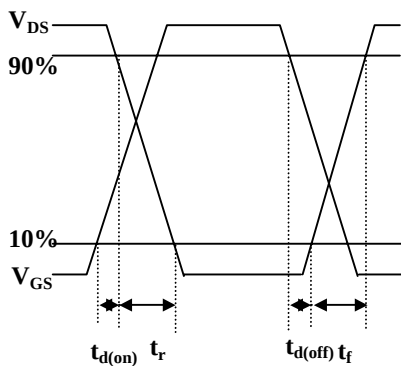


Fig 11. Switching Time Waveform

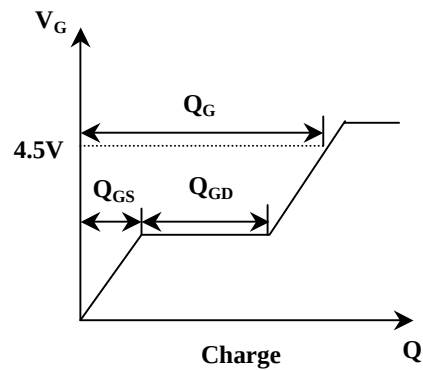


Fig 12. Gate Charge Waveform

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