

ST13003D-K

High voltage fast-switching NPN power transistor

Features

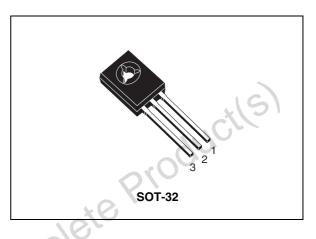
- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Integrated antiparallel collector-emitter diode

Applications

Electronic ballast for fluorescent lighting

Description

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.





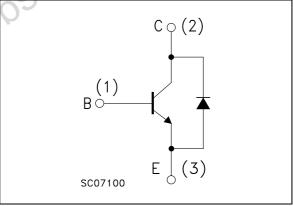


Table 1.Device summary

Order code	Marking	Package	Packaging
ST13003D-K	13003D	SOT-32	Bag

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1 Electrical ratings

Table 2.	Absolute maximum ratings
	Absolute maximum ratings

	Parameter	Value	Unit
V_{CES} Collector-emitter voltage ($V_{BE} = 0$)		700	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	400	V
V_{EBO} Emitter-base voltage (I _C = 0, I _B = 0.75 A, t _P < 10 µs)		V _{(BR)EBO}	۷
Ι _C	Collector current	1.5	Α
I _{CM}	Collector peak current (t _P < 5 ms)	3	А
I _B	Base current	0.75	Α
I _{BM}	Base peak current (t _P < 5 ms)	1.5	Α
P _{TOT}	Total dissipation at $T_c = 25 \text{ °C}$	40	W
T _{STG}	Storage temperature	-55 to 150	°C
ТJ	Max. operating junction temperature	150	°C
	Max. operating junction temperature		



Electrical characteristics 2

 $T_{case} = 25 \ ^{\circ}C$ unless otherwise specified

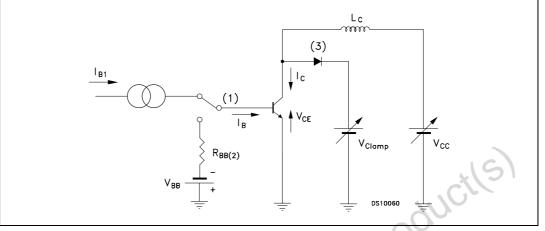
Table 3.	Electrical characteristics	
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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current $(V_{BE} = 0)$	V _{CE} = 700 V V _{CE} = 700 V T _c = 125 °C	;		1 5	mA mA
V _{(BR)EBO}	Emitter-Base breakdown voltage (I _C = 0)	I _E = 10 mA	9		18	v
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 10 mA	400	. (v
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage			90.	0.5 1 3	V V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$I_{C} = 0.5 A$ $I_{B} = 0.1 A$ $I_{C} = 1 A$ $I_{B} = 0.25 A$			1 1.2	V V
h _{FE}	DC current gain				20 25	
t _r t _s t _f	Resistive load Rise time Storage time Fall time	$V_{CC} = 125 V \qquad I_C = 1 A \\ I_{B1} = 0.2 A \qquad I_{B2} = -0.2 \\ T_p = 25 \ \mu s$	A		1 4 0.7	μs μs μs
ts	Inductive load Storage time	$ I_C = 1 A \qquad I_{B1} = 0.2 , \\ V_{BE} = -5 V \qquad L = 50 \text{ mH} \\ V_{Clamp} = 300 \text{ V} $		0.8		μs
V _F	Diode forward voltage	I _F = 0.5 A			1.5	V
V _F	Storage time Diode forward voltage pulse duration $300 \le \mu s$, duty c	V _{Clamp} = 300 V I _F = 0.5 A		0.8	1.5	



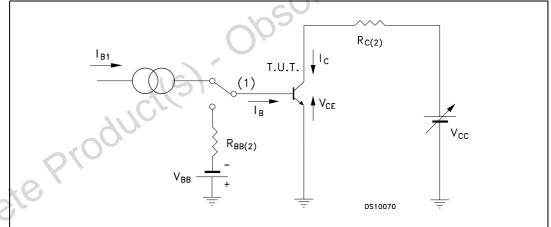
2.1 Test circuits





- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

Figure 3. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor



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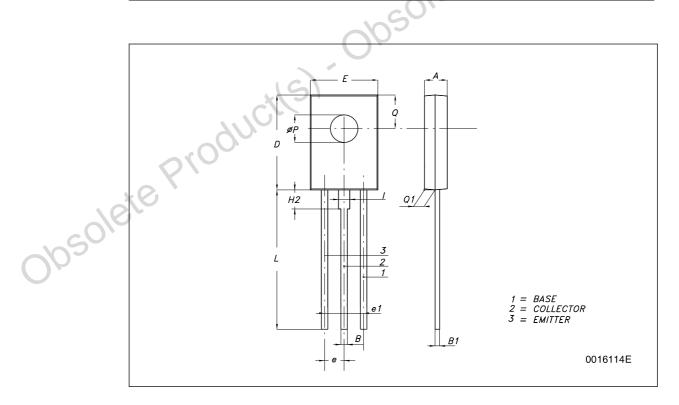
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.



obsolete Product(s). Obsolete Product(s)

DIM.	mm.			
	MIN.	ТҮР	MAX.	
А	2.4		2.9	
В	0.64		0.88	
B1	0.39		0.63	
D	10.5		11.05	
E	7.4		7.8	
е	2.04	2.29	2.54	
e1	4.07	4.58	5.08	
L	15.3		16	
Р	2.9		3.2	
Q		3.8	×0v	
Q1	1	Q	1.52	
H2		2.15		
1		1.27		





4 Revision history

Table 4.Document revision history

Date	Revision	Changes
15-Nov-2007	1	Initial release.
08-Sep-2009	2	Updated packaging information Table 1 on page 1.

obsolete Product(s). Obsolete Product(s)



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