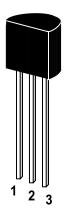


NPN Silicon Epitaxial Planar Transistor

High frequency amplifier applications.

The transistor is subdivided into three groups, R, O and Y, according to its DC current gain.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Collector 3. Base TO-92 Plastic Package Weight approx. 0.19g

Absolute Maximum Ratings (Ta=25°C)

	Symbol	Value	Unit
Collector Base Voltage	V_{CBO}	40	V
Collector Emitter Voltage	V_{CEO}	30	V
Emitter Base Voltage	V _{EBO}	4	V
Collector Current	I _C	20	mA
Base Current	I _B	4	mA
Power Dissipation	P _{tot}	100	mW
Junction Temperature Range	T _j	125	°C
Storage Temperature Range	Ts	-55 to +125	°C









Characteristics at T_{amb}=25 °C

	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain					
at V _{CE} =6V, I _C =1mA					
Current Gain Group R	h _{FE}	40	-	80	-
0	h _{FE}	70	-	140	-
Y	h _{FE}	100	-	200	-
Collector Cutoff Current					
at V _{CB} =40V	I _{CBO}	-	-	0.5	μΑ
Emitter Cutoff Current					
at V _{EB} =4V	I _{EBO}	-	-	0.5	μΑ
Reverse Transfer Capacitance					
at V _{CE} =6V, f=1MHz	Cre	-	0.70	-	pF
Transition Frequency					
at V _{CE} =6V, I _C =1mA	f _T	-	550	-	MHz
Collector Base Time Constant					
at V _{CE} =6V, I _E =-1mA,f=30MHz	C _C 'rbb'	-	-	30	ps
Noise Figure					
at V _{CC} =6V, f=100MHz, I _E =-1mA	NF	-	2.5	5.0	dB
Power Gain					
at V _{CC} =6V, f=100MHz, I _E =-1mA	G _{pe}	-	18	-	dB







