MODEL: ST4601B02-3

Ver. 2.2

Date: 16.Nov.2012

Customer's Approval	CSOT
Signature Date	Approved By Product Director Date Name: Richard Lung Signature:
	Reviewed By PM Manager Date Name: Aaron Tu Signature:
	Reviewed By Project Leader Date Name: Joe Kuo Signature:
	Reviewed By PM Date Name: Chenguo Wu Signature:

Contents

Revision History

Version	Date	Page (New)	Section	Description	Revision by
Ver. 0.1	18.June.2011	33	All	Tentative Specification was First Issued.	Wu Chenguo
Ver. 1.1	3.Sep.2012	33	All	Preliminary Specification was First Issued	Wu Chenguo
Ver. 2.1	19.Oct.2012	33	All	Approval Specification was First Issued	Wu Chenguo
Ver. 2.2	16.Nov.2012	8-9 25-26	4.1 8.2-8.3	Add Note（3）for L/R _O pin. Modify Carton Label and Pallet Label Serial Number Naming Rule.	Wu Chenguo

1. General Description

1.1 Product Features

- **FHD Resolution (1920 x 1080)**
- **Very High Contrast Ratio: 4000:1**
- **Fast Response Time**
- **Ultra Wide Viewing Angle: 178° (H)/178° (V) (CR ≥ 10)**
- **DE (Data Enable) Mode**
- **LVDS (Low Voltage Differential Signaling) Interface**

1.2 Overview

ST4601B02-3 is a diagonal 46.0" color active matrix LCD open cell with 2ch-LVDS interface. This open cell is a transmissive type display operating in the normally black mode. It supports 1920 x 1080 FHD resolution and can display up to 16.7M colors (8bit). Each pixel is divided into Red, Green and Blue sub-pixels which are arranged in vertical stripe.

This open cell dedicates for LCD TV products and provides excellent performance which includes high transparency, ultra wide viewing angle and high color depth. CSOT open cell comply with RoHS for identification.

1.3 General Information

Item	Specification	Unit	Note
Active Area	1018.08 (H) x 572.67 (V)	mm	
Cell Size	1038.480(H) x 593.634 (V) x 1.740 (D)	mm	
Weight	2.35	kg	Max.
Driving Scheme	a-Si TFT Active Matrix	-	
Number of Pixels	1920 x 1080	pixel	
Pixel Pitch (Sub Pixel)	0.17675 (H) x 0.53025 (V)	mm	
Pixel Arrangement	RGB Vertical Stripe	-	
Display Colors	16.7 M	color	8bit
Display Mode	Transmissive Mode, Normally Black	-	
Glass Thickness (Array/CF)	0.7/0.7	mm	
Color Chromaticity	R=(0.640,0.336) G= (0.323,0.626) B= (0.155,0.055) W=(0.280, 0.290)		Typical value measured at CSOT's module: MT4601B02-3
Contrast Ratio	4000:1 (Typ.)		
Cell Transmittance	5.8% (Typ.)	%	
View Angle(CR>10)	+89/-89 (H),+89/-89 (V) (Typ.)		
Polarizer(CF side)	Anti-glare, Haze 2%, Hard Coating (3H)		
Polarizer(TFT side)	Hard Coating (3H)		

2. Absolute Maximum Ratings

2.1 Absolute Maximum Ratings (T_A = 25 ± 2 °C)

The followings are maximum values which, if exceeded, may cause damage to the unit.

Item	Symbol	Value		Unit
		Min.	Max.	
Power Supply Voltage	V_{CC}	- 0.3	13.8	V
Input Signal Voltage	V_{IN}	- 0.3	3.6	V

2.2 Environment Requirement (Based on CSOT Module MT4601B02-3)

(1) Temperature and relative humidity range are shown as below.

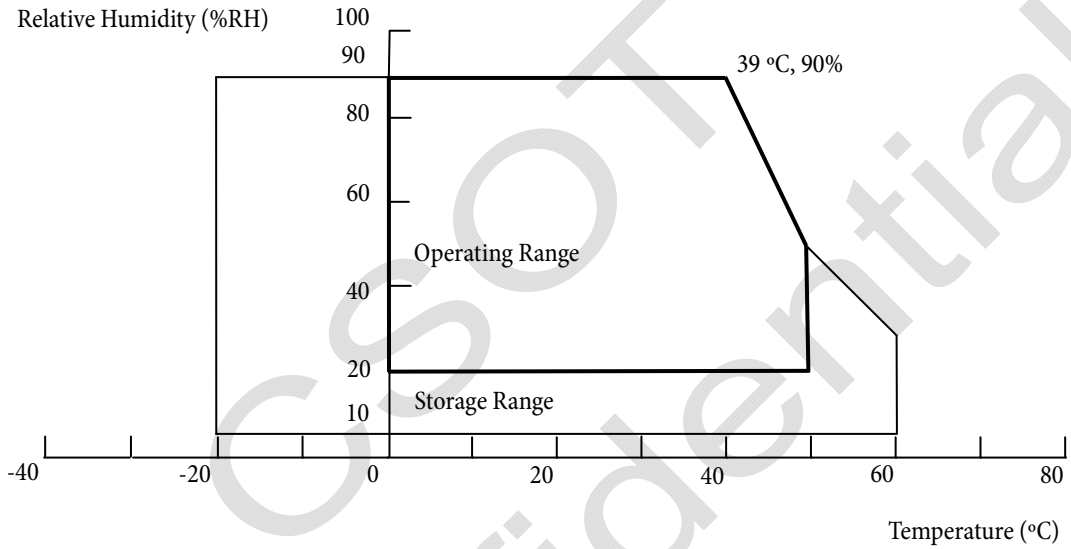


Fig. 2.1 Operating and storage environment

(a) 90%RH maximum ($T_A \leq 39$ °C).

(b) Wet-bulb temperature should be 39 °C maximum ($T_A > 39$ °C).

(c) No condensation.

(2) The storage temperature is between - 20 °C to 60 °C, and the operating ambient temperature is between 0 °C to 50 °C.

The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 65 °C with LCD module in a temperature controlled chamber alone. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 65 °C. The range of operating temperature may degrade in case of improper thermal management in the end product design.

(3) The rating of environment is based on LCD module. Leave LCD cell alone, this environment condition can't be guaranteed. Except LCD cell, the customer has to consider the ability of other parts of LCD module and LCD module process.

2.3 Absolute Ratings of Environment (Open Cell)

When storing open cell as spares for a long time, please follow the precaution instructions:

(1) Do not store the module in high temperature and high humidity for a long time. It is highly recommended to store the module with temperature from 20 °C to 30 °C in normal humidity (50 ± 10% RH) with shipping package.

(2) The open cell should be keep within one month shelf life

3. Electrical Specification

3.1 Open Cell Power Consumption (TA = 25 ± 2 °C)

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V_{CC}	10.8	12.0	13.2	V	(1)
Rush Current		I_{RUSH}	-	-	2.31	A	(2)
Power Supply Current	White Pattern	I_{CC}	-	0.45	0.63	A	(3)
	Horizontal Stripe	I_{CC}	-	0.87	1.27	A	
	Black Pattern	I_{CC}	-	0.42	0.62	A	

Note:

(1) The ripple voltage should be controlled less than 10% of V_{CC}.

(2) Measurement condition: V_{CC} rising time = 470 μs.

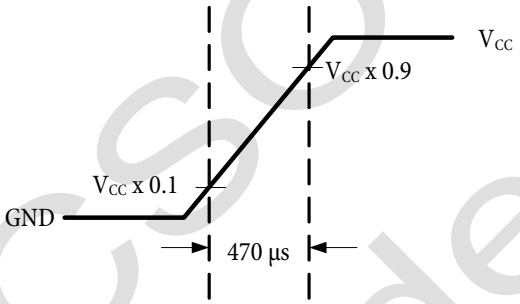
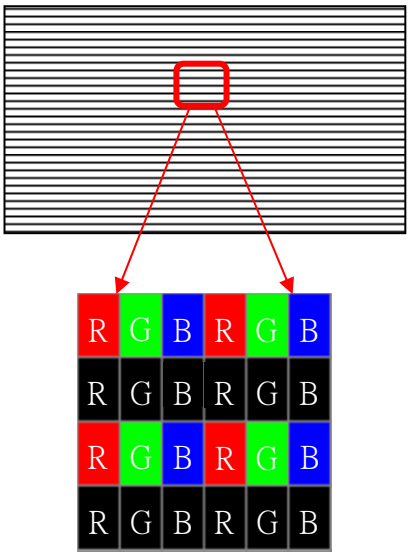


Fig. 3.1 V_{CC} rising time condition

(3) Measurement condition: V_{CC} = 12 V, Ta = 25 ± 2 °C, F = 60 Hz. The test patterns are shown as below.

A. White Pattern

B. Horizontal Pattern



C. Black Pattern

Fig. 3.2 Test patterns

3.2 LVDS Characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
LVDS Interface	Differential Input High Threshold Voltage	V_{TH}	+ 100	-	-	mV	(1)
	Differential Input Low Threshold Voltage	V_{TL}	-	-	- 100	mV	
	Common Input Voltage	V_{CM}	1.0	1.2	2.1	V	
	Differential Input Voltage	$\|V_{ID}\|$	200	400	600	mV	
	Terminating Resistor	R_T	87.5	100	112.5	ohm	
CMOS Interface	Input High Threshold Voltage	V_{IH}	2.0	-	3.3	V	
	Input Low Threshold Voltage	V_{IL}	0	-	0.7	V	

Note:

(1) The LVDS input signal has been defined as follows:

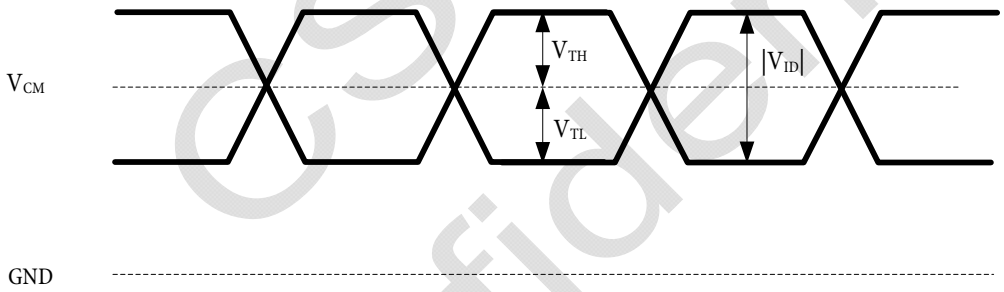


Fig. 3.3 LVDS input signal

4. Input Terminal Pin Assignment

4.1 Interface Pin Assignment

CN1: 300C51-0000RA-G(STARCONN)or equivalent (see Note (1))

Pin No.	Symbol	Description	Note
1	N.C.	No Connection	(2)
2	N.C.	No Connection	(2)
3	N.C.	No Connection	(2)
4	N.C.	No Connection	(2)
5	L/R_O	Output signal for Left Right Glasses control (High:Left glass turn on,Low:Right glass turn on)	(3)
6	N.C.	No Connection	
7	SELLVDS	Input signal for LVDS Data Format Selection	(4)
8	N.C.	No Connection	(2)
9	N.C.	No Connection	(2)
10	N.C.	No Connection	(2)
11	GND	Ground	
12	CH1-0-	LVDS Channel 1,Sign0-	
13	CH1-0+	LVDS Channel 1,Sign0+	
14	CH1-1-	LVDS Channel 1,Sign1-	
15	CH1-1+	LVDS Channel 1,Sign1+	
16	CH1-2-	LVDS Channel 1,Sign2-	
17	CH1-2+	LVDS Channel 1,Sign2+	
18	GND	Ground	
19	CH1-CLK-	LVDS Channel 1,CLK-	
20	CH1-CLK+	LVDS Channel 1,CLK+	
21	GND	Ground	
22	CH1-3-	LVDS Channel 1,Sign3-	
23	CH1-3+	LVDS Channel 1,Sign3+	
24	N.C.	No Connection	(2)
25	N.C.	No Connection	(2)
26	2D/3D	Input signal for 2D/3D Mode Selection	
27	NC	No Connection	
28	CH2-0-	LVDS Channel 2,Sign0-	
29	CH2-0+	LVDS Channel 2,Sign0+	
30	CH2-1-	LVDS Channel 2,Sign1-	

31	CH2-1+	LVDS Channel 2,Sign1+	
32	CH2-2-	LVDS Channel 2,Sign2-	
33	CH2-2+	LVDS Channel 2,Sign2+	
34	GND	Ground	
35	CH2-CLK-	LVDS Channel 2,CLK-	
36	CH2-CLK+	LVDS Channel 2,CLK+	
37	GND	Ground	
38	CH2-3-	LVDS Channel 2,Sign3-	
39	CH2-3+	LVDS Channel 2,Sign3+	
40	N.C.	No Connection	(2)
41	N.C.	No Connection	(2)
42	N.C.	No Connection	(2)
43	N.C.	No Connection	(2)
44	GND	Ground	
45	GND	Ground	
46	GND	Ground	
47	N.C.	No Connection	(2)
48	VCC	Power Supply, + 12 V DC Regulated	
49	VCC	Power Supply, + 12 V DC Regulated	
50	VCC	Power Supply, + 12 V DC Regulated	
51	VCC	Power Supply, + 12 V DC Regulated	

Note:

(1)The direction of pin assignment is shown as below:

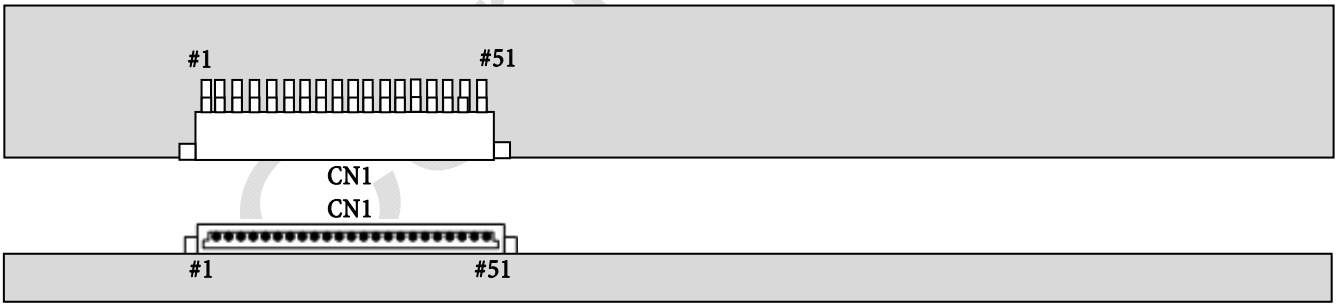


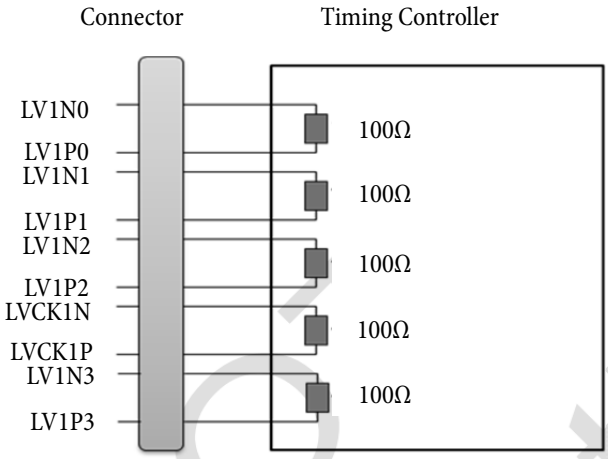
Fig. 4.1 LVDS connector direction sketch map

(2) For CSOT internal only, please let it open.

(3)The signal of Left Right Glass control has be synchronized with CSOT's Backlight Converter. If the signal is used to control the Glass without CSOT' s Backlight Converter, the 3D performance may be affected.

(4) High: connect to + 3.3 V → NS format; Low: connect to GND or Open → JEIDA format.

4.2 Block Diagram of Interface



Attention:

(1) This open cell uses a 100 ohms (Ω) resistor between positive and negative lines of each receiver input.

(2) LVDS cable impedance shall be 50 ohms per signal line or about 100 ohms per twist-pair line respectively.

4.3 LVDS Interface

4.3.1 NS Format (SELLVDS = H)

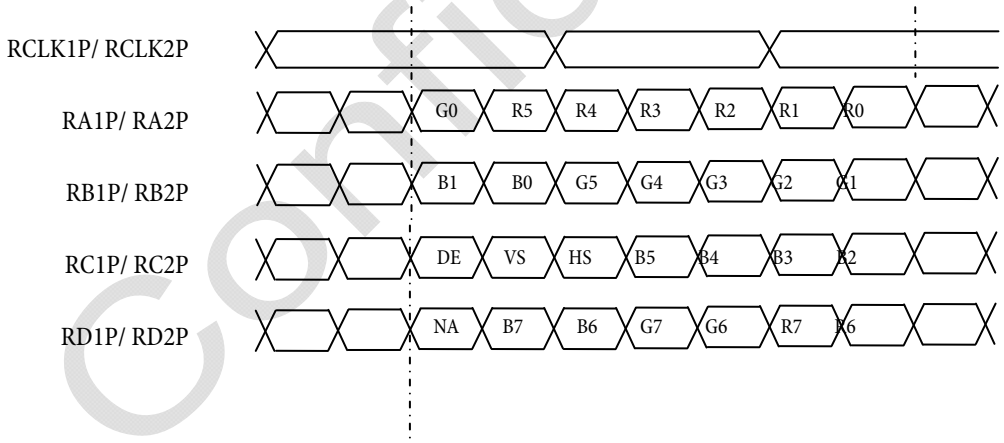


Fig. 4.2 NS format

4.3.2 JEIDA Format (SELLVDS = L or Open)

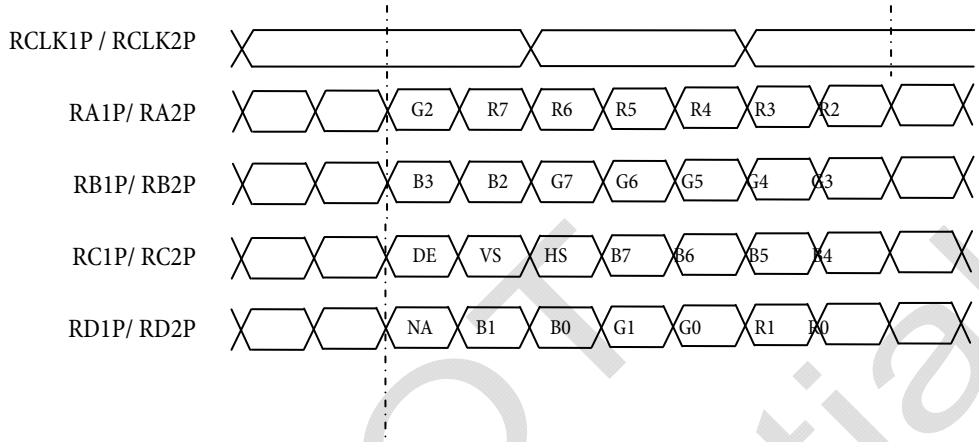
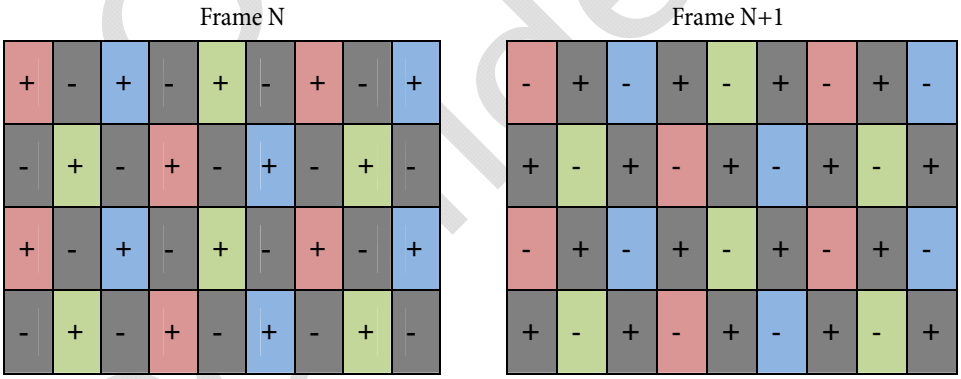


Fig. 4.3 JEIDA format

4.4 Pattern FOR Vcom Adjustment

Dot - inversion pattern



5. Interface Timing

5.1 Timing Table (DE Only Mode)

Signal		Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Receiver Clock		Frequency	F_{CLK} (= 1 / T_{CLK})	60	74.25	76	MHz	(2)
		Input cycle to cycle jitter	T_{rcl}	—	—	200	ps	(3)
		Spread spectrum modulation range	F_{clkin_mod}	F_{clkin}-2%	—	F_{clkin}+2%	MHz	(4)
		Spread spectrum modulation frequency	F_{SSM}			200	KHz	
LVDS Receiver Data		Receiver Skew Margin	T_{RSM}	-400	—	400	ps	(5)
Frame Rate		2D Mode	Fr5	47	50	53	Hz	
			Fr6	57	60	63	Hz	
		3D Mode	Fr6	60	60	60	Hz	(7)
Vertical Term	2D	Total	T_V	1115	1125	1380	T_H	$T_V = T_{VD} + T_{VB}$
		Display	T_{VD}	1080			T_H	
		Blank	T_{VB}	35	45	300	T_H	
	3D	Total	T_V	1125			T_H	(6) , (8)
		Display	T_{VD}	1080			T_H	
		Blank	T_{VB}	45			T_H	
Horizontal Term	2D	Total	T_H	1080	1100	1150	T_{CLK}	$T_H = T_{HD} + T_{HB}$
		Display	T_{HD}	960			T_{CLK}	
		Blank	T_{HB}	90	140	190	T_{CLK}	
	3D	Total	T_H	1080	1100	1150	T_{CLK}	$T_H = T_{HD} + T_{HB}$
		Display	T_{HD}	960			T_{CLK}	
		Blank	T_{HB}	90	140	190	T_{CLK}	

Attention:

(1) The module is operated in DE only mode, H sync and V sync input signal have no effect on normal operation.

(2) Please make sure the range of pixel clock follows the following equations:

Fclkin(max) ≥ Fmax×Tv×Th

Fmin×Tv×Th≥Fclkin(min)

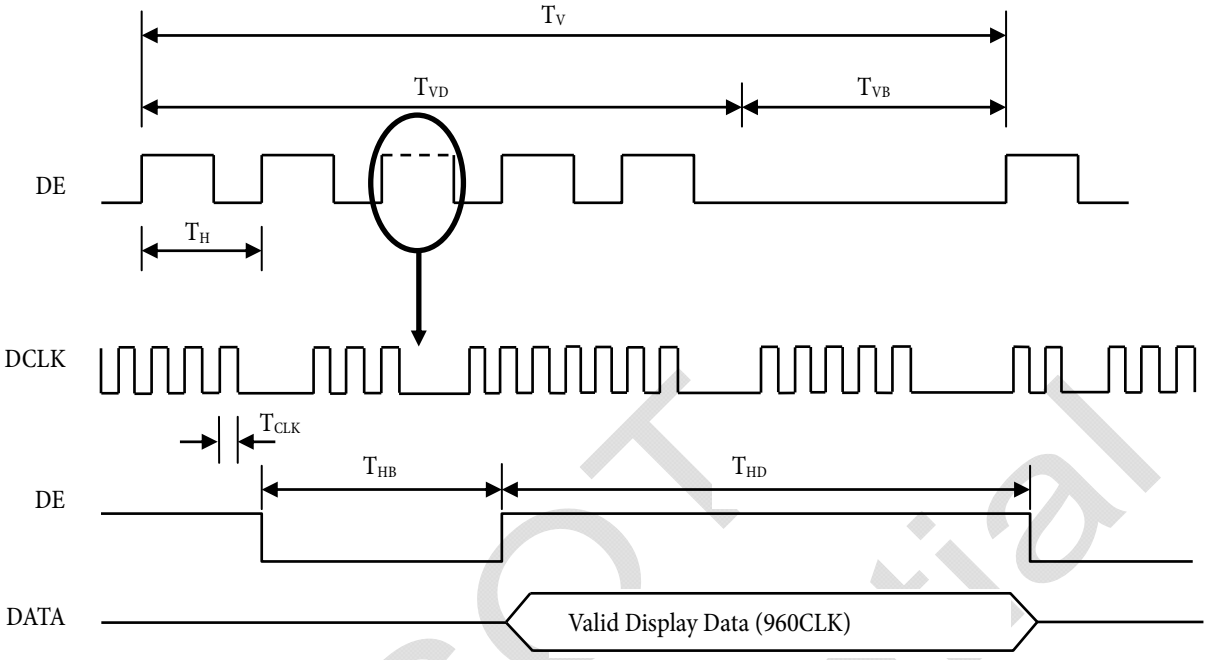
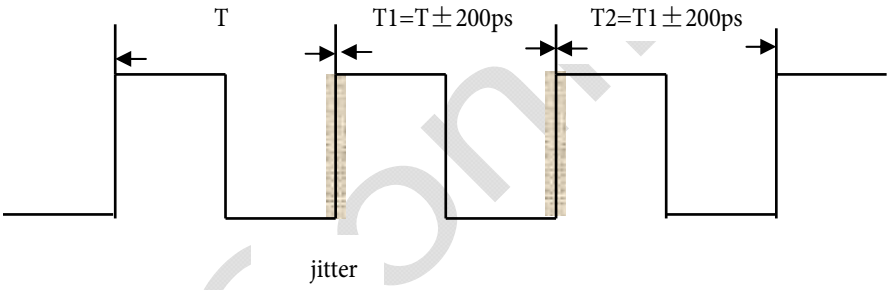
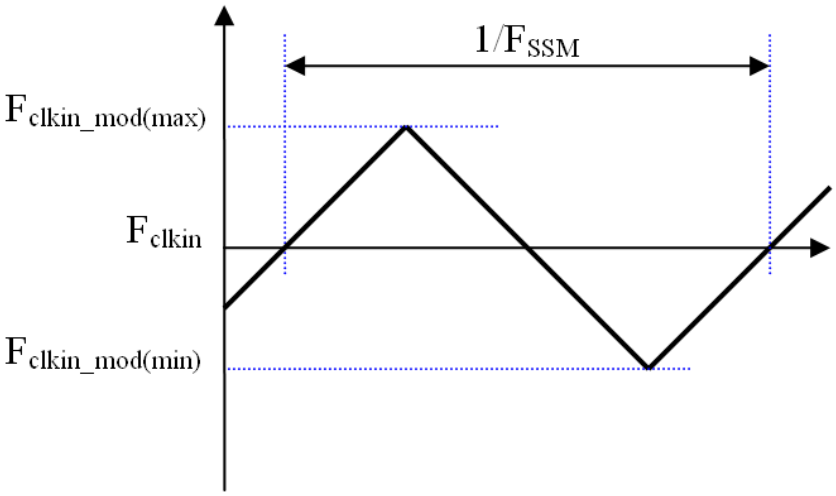


Fig. 5.1 Interface signal timing diagram

(3) The input clock cycle-to-cycle jitter is defined as the following figure. Trcl = I T1– TI

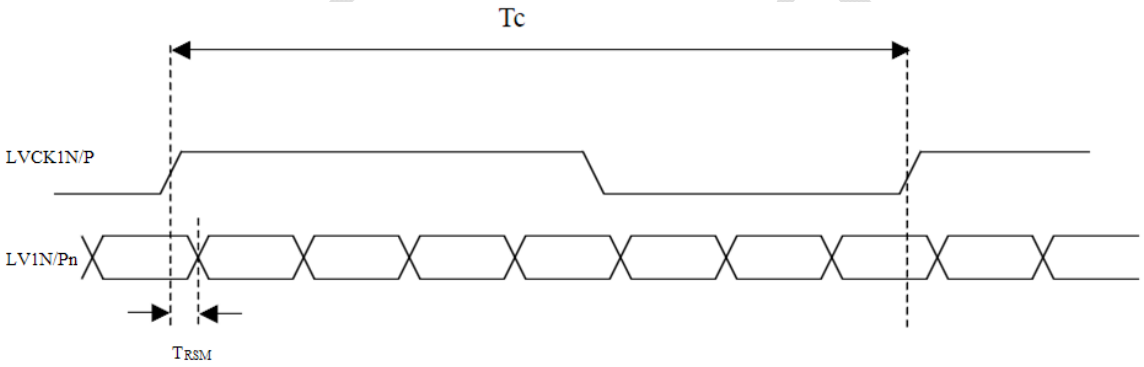


(4) The SSCG (Spread Spectrum Clock Generator) is defined as the following figure.



(5) The LVDS timing diagram and setup/hold time is defined and showed as the following figure.

LVDS Receiver Interface Timing Diagram



(6) Please fix the vertical timing in 3D mode.(Vertical Total =1125/Diplay=1080/Blank=45)

(7) In 3D mode,the setup Fr6 should be in Typ. In order to ensure that the electic function performance to avoid no display symptom.(Except picture quality symptom)

(8) In 3D mode, the setup Tv and Tvb should be in Typ.In order to ensure that the electric function performance to avoid no display symptom.(Except picture quality symtom)

5.2 Power On/Off Sequence

5.2.1 Power On/Off Sequence

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.

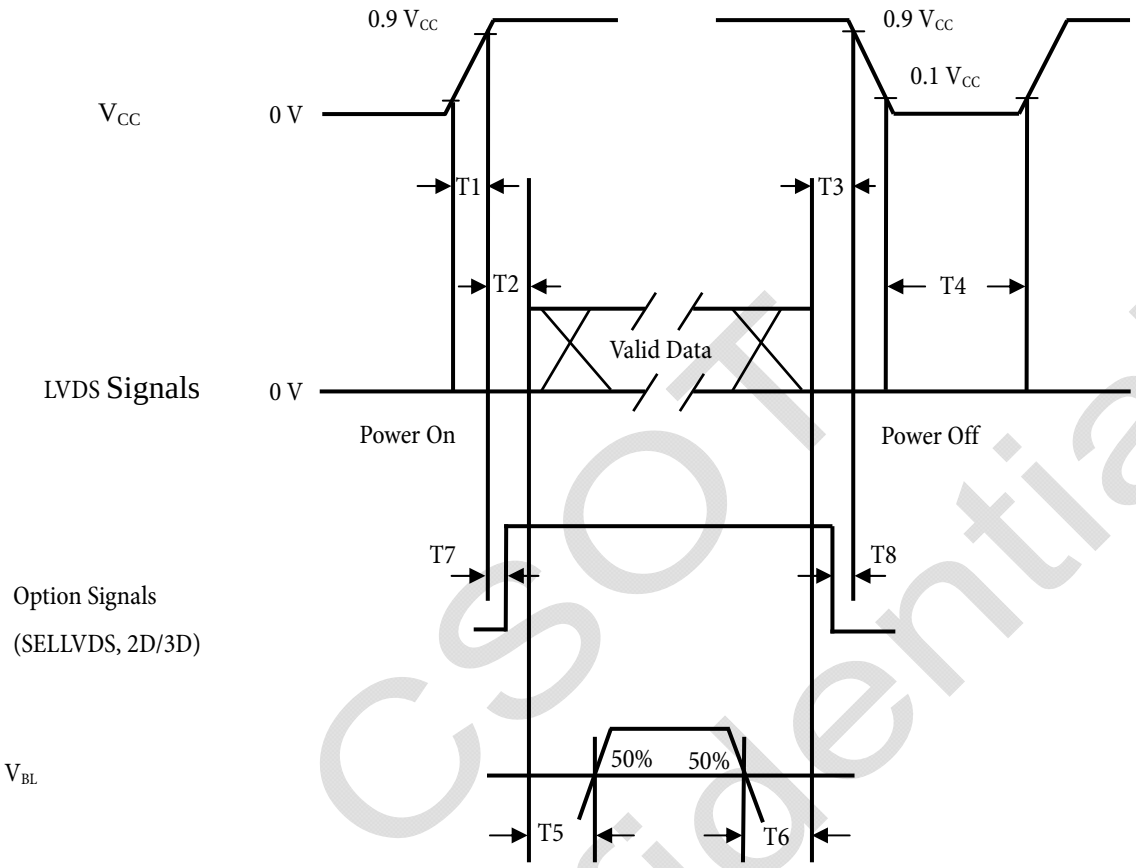
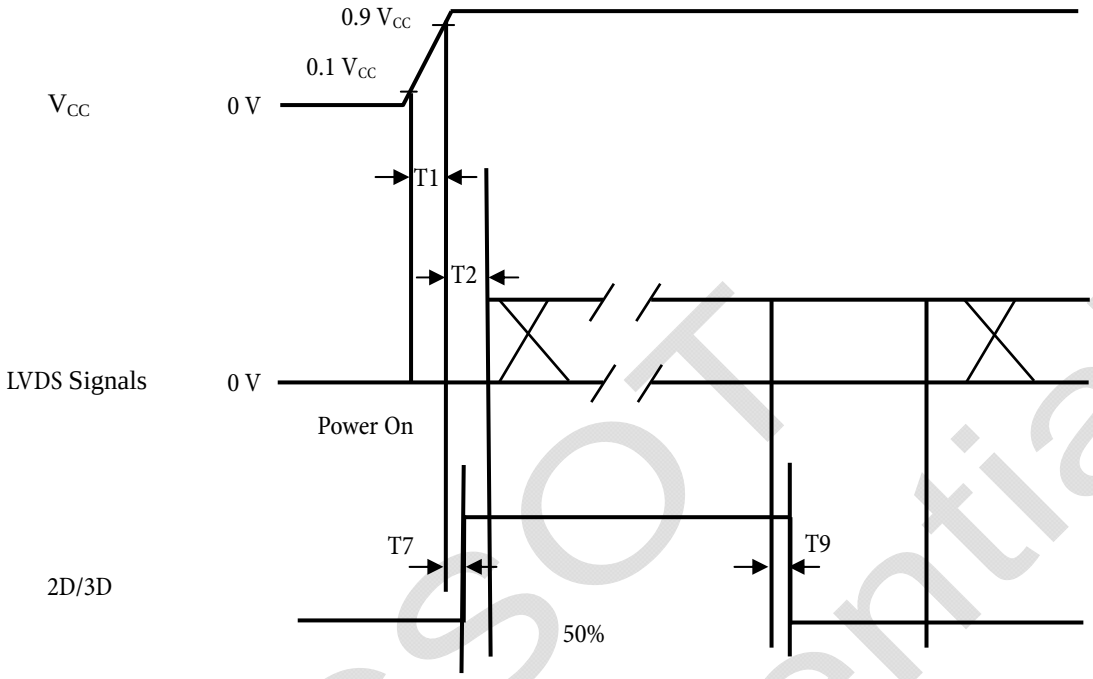


Fig. 5.2 Power on/off sequence

5.2.2 2D/3D Change Signal Sequence without Vcc Turn off and Turn on



Parameter	Values			Unit
	Min.	Typ.	Max.	
T1	0.5	-	10.0	ms
T2	0.0	-	-	ms
T3	0.0	-	-	ms
T4	1000.0	-	-	ms
T5	500.0	-	-	ms
T6	100.0	-	-	ms
T7	-	-	T2	ms
T8	-	-	T3	ms
T9	0.0	-	10.0	ms

Attention:

(1) While system turn from 2D/3D function to 3D/2D function,the V_{BL} signal should be always high .

(2) The supply voltage of the external system for the module input should follow the definition of V_{CC}.

(3) Apply the lightbar voltage within the LCD operation range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.

(4) In case that V_{CC} is in off level, please keep the level of input signals on the low or high impedance. If T2 < 0, that may cause electrical overstress.

(5) T4 should be measured after the module has been fully discharged between power off and on period.

(6) Interface signal shall not be kept at high impedance when the power is on.

6. Optical Characteristics

6.1 Measurement Conditions

The table below is the test condition of optical measurement.

Item	Symbol	Value	Unit
Ambient Temperature	T_A	25 ± 2	°C
Ambient Humidity	H_A	50 ± 10	% RH
Supply Voltage	V_{CC}	12	V
Driving Signal	Refer to the typical value in Chapter 3: Electrical Specification		
Vertical Refresh Rate	F_R	60	Hz

To avoid abrupt temperature change during optical measurement, it's suggested to warm up the LCD module more than 60 minutes after lighting the backlight and in the windless environment.

To measure the LCD cell, it is suggested to set up the standard measurement system as Fig. 6.1. The measuring area S should contain at least 500 pixels of the LCD cell as illustrated in Fig.6.2 (A means the area allocated to one pixel). In this model, for example, the minimum measuring distance Z is 370 mm when θ is 2 degree. Hence, 500 mm is the typical measuring distance. This measuring condition is referred to 301-2H of VESA FPDM 2.0 about viewing distance, angle, and angular field of view definition.

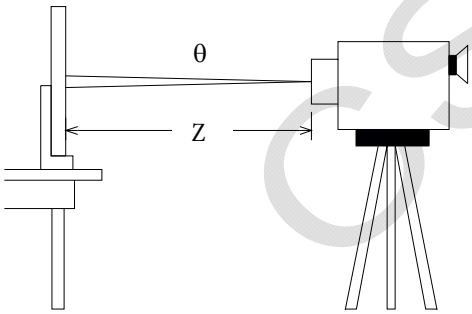


Fig. 6.1 The standard set-up system of measurement

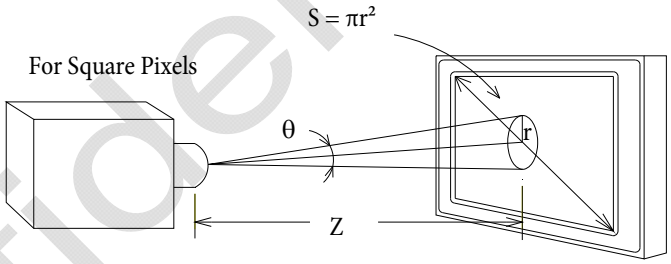


Fig. 6.2 The area S contains at least 500 pixels to be measured

$$N = \frac{S}{A} \geqslant 500\text{pixels}$$

N means the actual number of the pixels in the area S.

6.2 Optical Specifications

The table below of optical characteristics is measured by MINOLTA CS2000, MINOLTA CA310, ELDIM OPTI Scope-SA and ELDIM EZ Contrast in dark room.

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Static Contrast Ratio		CR	$\theta_H = 0°$, $\theta_V = 0°$ Normal direction at center point with CSOT's module: MT4601B02-3	-	4000	-	-	(1) (2)
Response Time		T_L		-	6.5	12	ms	(3)
Center Transmittance		T%		-	5.8		%	(2) (4)
Color Chromaticity (CIE1931)	Red	R_X		Typ. - 0.03	0.640	Typ. + 0.03	-	(2) (5)
		R_Y			0.336		-	
	Green	G_X			0.323		-	
		G_Y			0.626		-	
	Blue	B_X			0.155		-	
		B_Y			0.055		-	
	White	W_X			0.280		-	
		W_Y			0.290		-	
	Color Gamut	CG		-	72	-	% NTSC	
Viewing Angle	Horizontal	θ_{H+}	CR ≥ 10	-	89	-	Deg.	(6)
		θ_{H-}		-	89	-		
	Vertical	θ_{V+}		-	89	-		
		θ_{V-}		-	89	-		

Note:

(1) Definition of static contrast ratio (CR):

It's necessary to switch off all the dynamic and dimming function when measuring the static contrast ratio.

$$\text{Static Contrast Ratio (CR)} = \frac{\text{CR-W}}{\text{CR-D}}$$

CR-W is the luminance measured by LMD (light-measuring device) at the center point of the LCD module with full-screen displaying white. The standard setup of measurement is illustrated in Fig. 6.3; CR-D is the luminance measured by LMD at the center point of the LCD module with full-screen displaying black. The LMD in this item is CS2000.

(2) The LMD in the item could be a spectroradiometer such as (KONICA MINOLTA) CS2000, CS1000(TOPCON), SR-UL2 or the same level spectroradiometer. Other display color analyzer (KONICA MINOLTA) CA210, CA310 or (TOPCON) BM-7 could be involved after being calibrated with a spectroradiometer on each stage of a product.

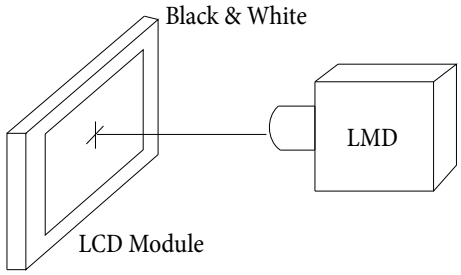


Fig. 6.3 The standard setup of CR measurement

(3) Response time T_L is defined as the average transition time in the response time matrix. The table below is the response time matrix in which each element $t_{X\ to\ Y}$ is the transition time from luminance ratio X to Y. X and Y are two different luminance ratios among 0%, 25%, 50%, 75%, and 100% luminance. The transition time $t_{X\ to\ Y}$ is defined as the time taken from 10% to 90% of the luminance difference between X and Y (X < Y) as illustrated in Fig.6.4. When X > Y, the definition of $t_{X\ to\ Y}$ is the time taken from 90% to 10% of the luminance difference between X and Y. The response time is optimized on refresh rate F_r = 60Hz.

Measured Transition Time		Luminance Ratio of Previous Frame				
		0%	25%	50%	75%	100%
Luminance Ratio of Current Frame	0%		$t_{25\%\ to\ 0\%}$	$t_{50\%\ to\ 0\%}$	$t_{75\%\ to\ 0\%}$	$t_{100\%\ to\ 0\%}$
	25%	$t_{0\%\ to\ 25\%}$		$t_{50\%\ to\ 25\%}$	$t_{75\%\ to\ 25\%}$	$t_{100\%\ to\ 25\%}$
	50%	$t_{0\%\ to\ 50\%}$	$t_{25\%\ to\ 50\%}$		$t_{75\%\ to\ 50\%}$	$t_{100\%\ to\ 50\%}$
	75%	$t_{0\%\ to\ 75\%}$	$t_{25\%\ to\ 75\%}$	$t_{50\%\ to\ 75\%}$		$t_{100\%\ to\ 75\%}$
	100%	$t_{0\%\ to\ 100\%}$	$t_{25\%\ to\ 100\%}$	$t_{50\%\ to\ 100\%}$	$t_{75\%\ to\ 100\%}$	

$t_{X\ to\ Y}$ means the transition time from luminance ratio X to Y.

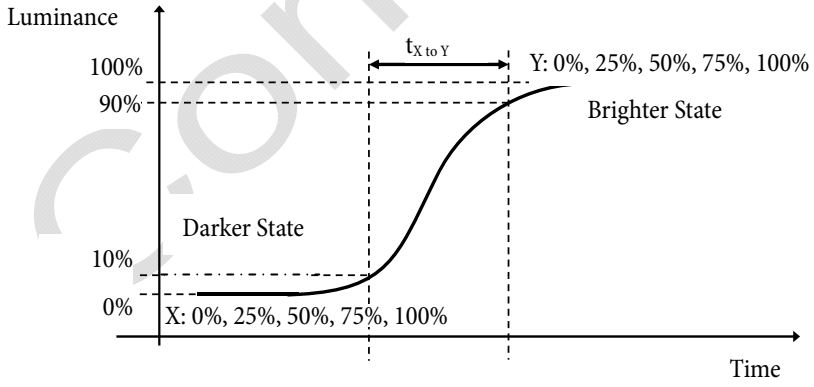


Fig. 6.4 The definition of $t_{X\ to\ Y}$

All the transition time is measured at the center point of the LCD module by ELDIM OPTI Scope-SA.

(4) Definition of center Transmittance (T%):

The transmittance is measured with full white pattern (Gray 255)

$$\text{Static Contrast Ratio (CR)} = \frac{\text{Luminance of LCD module}}{\text{Luminance of Backlight}}$$

(5) Definition of color chromaticity:

Each chromaticity coordinates (x, y) are measured in CIE1931 color space when full-screen displaying primary color R, G, B and white. The color gamut is defined as the fraction in percent of the area of the triangle bounded by R, G, B coordinates and the area is defined by NTSC 1953 color standard in the CIE color space. Chromaticity coordinates are measured by CS2000 and the standard setup of measurement is shown in Fig. 6.5.

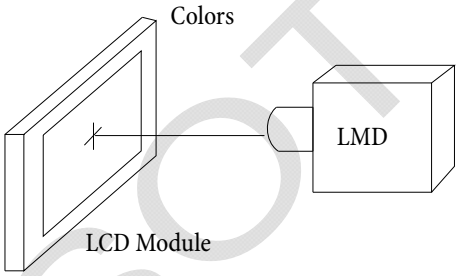


Fig. 6.5 The standard setup of color chromaticity measurement

(6) Definition of viewing angle coordinate system (θ_H, θ_V):

The contrast ratio is measured at the center point of the LCD module. The viewing angles are defined at the angle that the contrast ratio is larger than 10 at four directions relative to the perpendicular direction of the LCD module (two vertical angles: up θ_{V+} and down θ_{V-}; and two horizontal angles: right θ_{H+} and left θ_{H-}) as illustrated in Fig. 6.6. The contrast ratio is measured by ELDIM EZ Contrast.

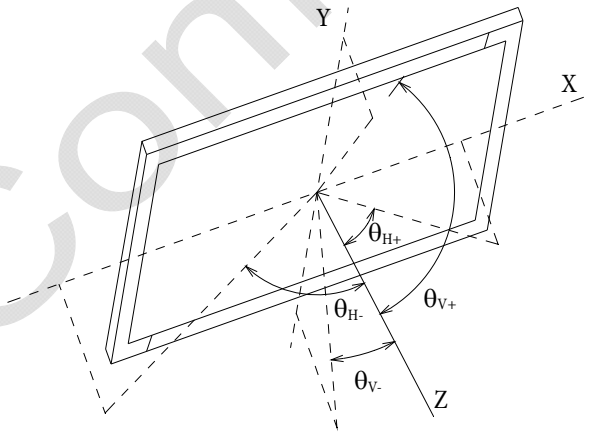
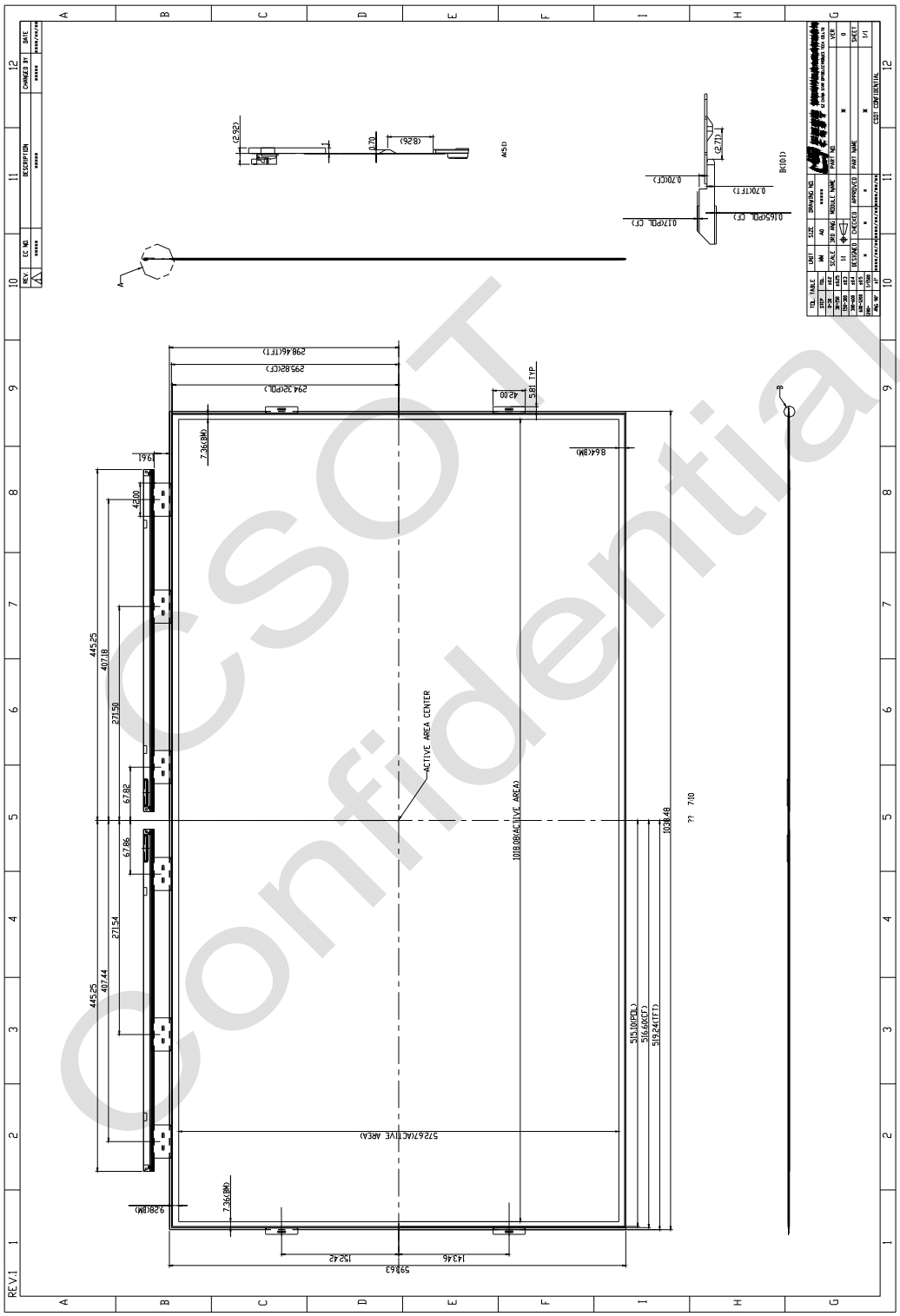


Fig. 6.6 Viewing angle coordination system

7. Mechanical Characteristics

7.1 Mechanical Specification

7.1.1 Mechanical Specification

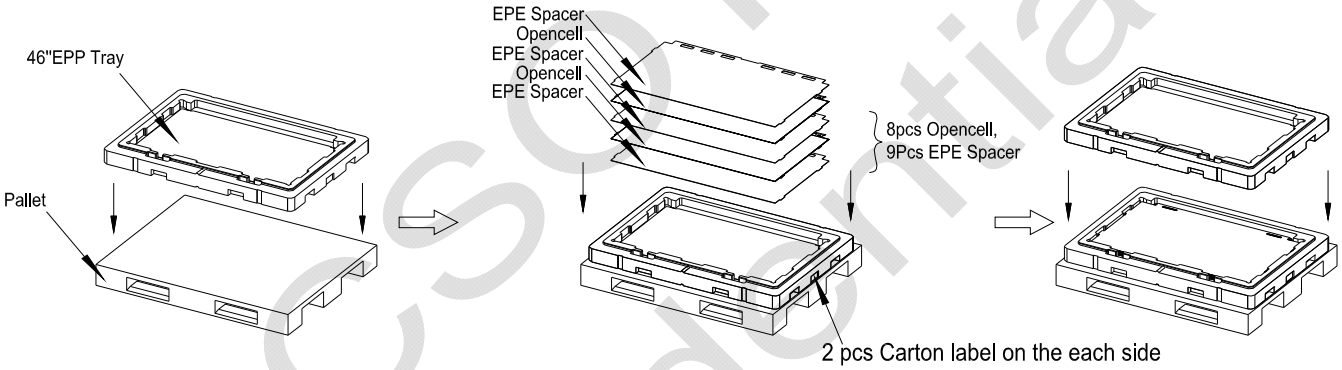


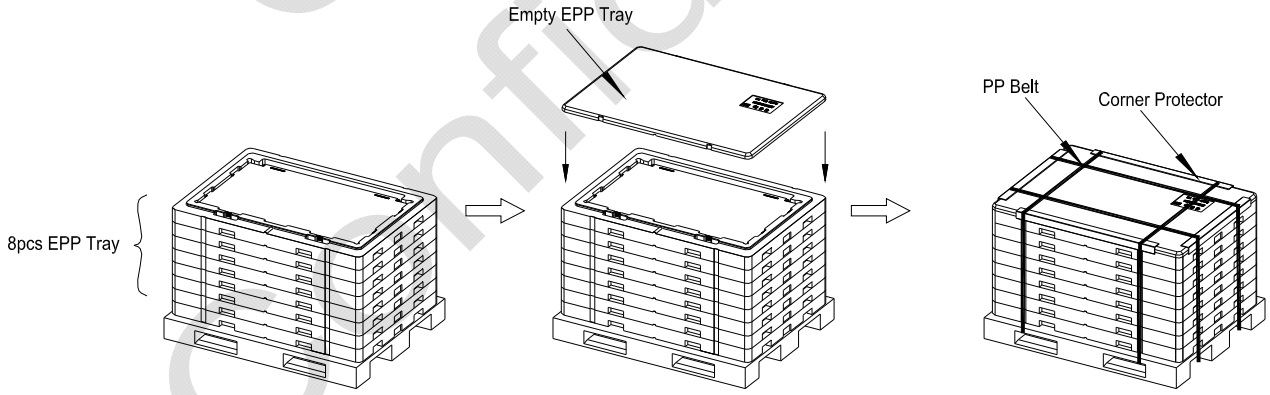
7.2 Packing

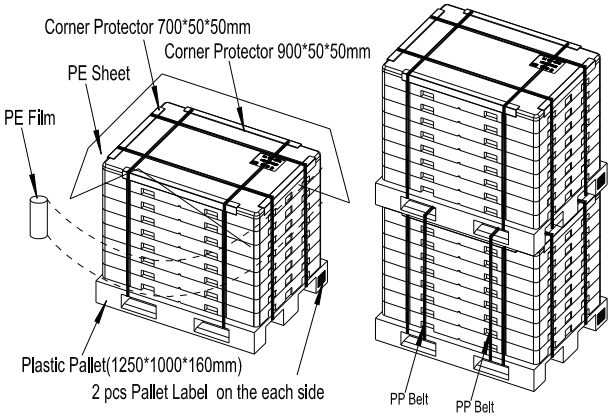
7.2.1 Packing Specifications

Item	Specification		
	Quantity	Dimension (mm)	Weight (kg)
Packing Box	8 pcs / box	1235(L) x 883 (W) x 110 (H)	Net Weight: 20.0 (Max.) Gross Weight: 24.4 (Max.)
Pallet	1	1250.00 (L) x 1000.00 (W) x 160.00 (H)	Net Weight: 23
Stack Layer	8		
Boxes per Pallet	8 boxes / pallet		
Pallet after Packing	64 pcs / pallet	1250.0 (L) x 1000.0 (W) x 1030.0 (H)	Gross Weight: 218.2kg / pallet

7.2.2 ST4601B02-2 Packing Method







8. Definition of Labels

8.1 Open Cell Label

XXXXXXXXXXXXXXXXXXXX XXXX ST4601B02-3 Ver. X.X

- Model Version Code
- Model Name
- Week Code
- Serial Number

Serial Number: XXXXXXXXXXXX XXXXXXXX

- CSOT Internal Use
- Panel ID

Week Code: XX XX

- Week
- Year

Model Name: ST4601B02-3

Ver.X.X: Version, for example: 0.1, 0.2, … , 1.1, 1.2, …, 2.1, 2.2, …

WC (Week Code): XX XX

- Week
- Year

Year: 2010 = 10, 2011 = 11 … 2020 = 20, 2021 = 21…

Week: 01, 02, 03 …

Serial Number: XXXXXXXXXXXX XXXXXXXX

- CSOT Internal Use
- Panel ID

8.2 Carton Label

Model Name: ST4601B02-3

For RoHS compliant products, CSOT will add RoHS for identification.

Serial Number: XXXXXXXX XX XXXXX XXXXX

- XXXXXXXX: CSOT Internal Use
- XX: Manufactured Date (Year, Month, Date)
- XXXXX: Model Version Code
- XXXXX: CSOT Internal Use

Manufactured Date:

Year: 2010 = 10, 2011 = 11…2020 = 20, 2021 = 21…

Month: 1~9, A~C, for Jan. ~ Dec.

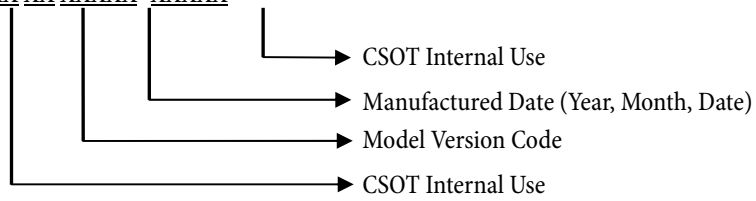
Date: 01~31, for 1st to 31st

Model Version Code: Version of product, for example: 01, 02, 11, 12…

8.3 Pallet Label

Model Name: ST4601B02-3

Serial Number: XXXXXX XX XXXXX XXXXX



9. Precautions

9.1 Assembly and Handling Precautions

(1) Do not apply rough force such as bending or twisting to the open cell during assembly.

(2) It is recommended to assemble or install a open cell into the user's system in clean working areas. The dust and oil may cause electrical short or damage the polarizer.

(3) Do not apply pressure or impulse to the open cell to prevent the damage to the open cell.

(4) Always follow the correct power-on sequence. This can prevent the damage and latch-up to the LSI chips.

(5) Do not plug in or pull out the interface connector while the open cell is in operation.

(6) Use soft dry cloth without chemicals for cleaning because the surface of polarizer is very soft and easily be scratched.

(7) Moisture can easily penetrate into the open cell and may cause the damage during operation.

(8) High temperature or humidity may deteriorate the performance of the open cell. Please store open cell in the specified storage conditions.

(9) When ambient temperature is lower than 10 ºC, the display quality might be deteriorated. For example, the response time will become slow.

9.2 Safety Precautions

(1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.

(2) After the open cell end of life, it is not harmful in case of normal operation and storage.