

STA124N

PNP Silicon Transistor

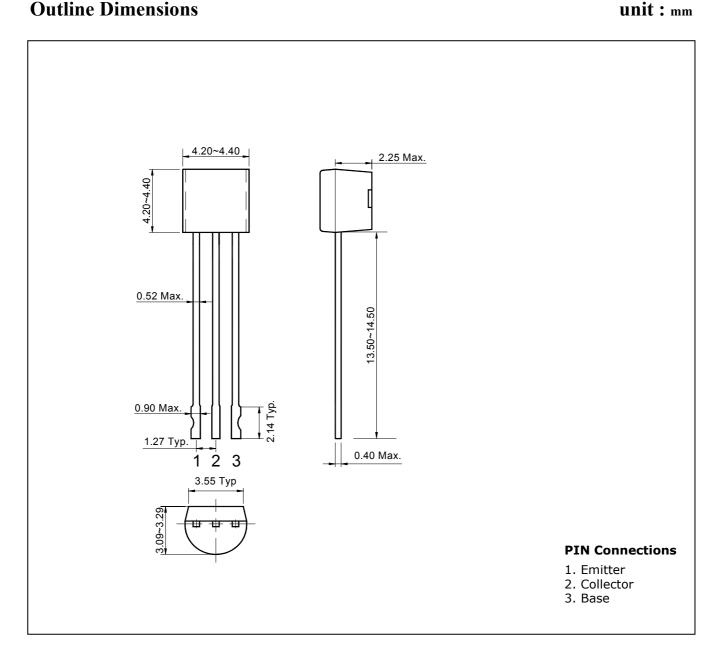
Features

- Suitable for low voltage large current drivers
- High DC current gain and large current capability
- Complementary pair with STC128N

Ordering Information

Type NO.	Marking	Package Code		
STA124N	STA124	TO-92N		

Outline Dimensions



STA124N

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-15	V
Collector-emitter voltage	V_{CEO}	-12	V
Emitter-base voltage	V_{EBO}	-6.5	V
Collector current	I_{C}	-1	Α
Collector power dissipation	P_{C}	400	mW
Junction temperature	T _J	150	°C
Storage temperature range	T_{stg}	-55~150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage	BV _{CEO}	$I_C=-1$ mA, $I_B=0$	-12	-	-	V
Collector cut-off current	I_{CBO}	V_{CB} =-15V, I_{E} =0	-	-	-0.1	μА
Emitter cut-off current	I_{EBO}	V_{EB} =-6.5V, I_{C} =0	-	-	-0.1	μА
DC current gain	h _{FE}	V _{CE} =-1V, I _C =-100mA	200	-	450	-
Collector-emitter saturation voltage	$V_{\text{CE(sat)}}$	I _C =-500mA, I _B =-50mA	-	-0.2	-0.4	V
Base-emitter voltage	V_{BE}	V _{CE} =-1V, I _C =-100mA	-	-0.7	-0.9	V
Transition frequency	f_{T}	V_{CE} =-5V, I_{C} =-50mA	-	260	-	MHz
Collector output capacitance	C _{ob}	V_{CB} =-10V, I_{E} =0, f=1MHz	-	5	ı	pF

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Fig. 1 P_C - T_a

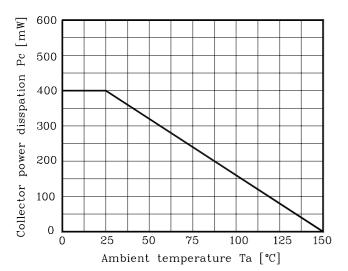


Fig. 3 h_{FE}-I_C

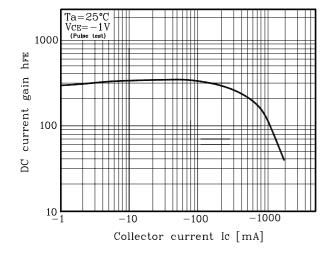


Fig. 2 I_C - V_{BE}

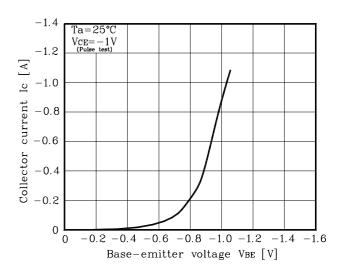
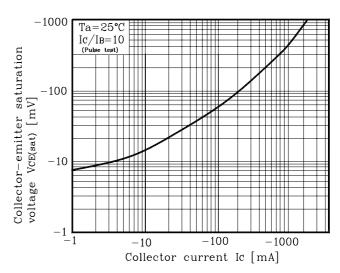


Fig. 4 V_{CE(sat)-}I_C



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