

STC4350Q

NPN Silicon Transistor

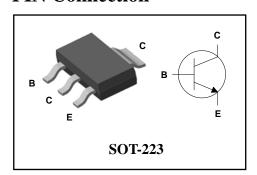
Applications

- Power amplifier application
- High current switching application

Features

- High collector breakdown voltage
- : $V_{CEO} = 50V$
- Low collector saturation voltage
- : $V_{CE(sat)} = 0.35V(Max.)$
- "Green" device and RoHS compliant device
- Available in full lead (Pb)-free device

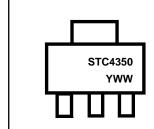
PIN Connection



Ordering Information

| Type NO. | Marking | Package Code | | |
|----------|---------|--------------|--|--|
| STC4350Q | STC4350 | SOT-223 | | |

Marking Diagram



Column 1: Device Code

Column 2 : Production Information

e.g.) YWW

-. Y : Year Code -. WW : Week Code **Absolute Maximum Ratings**

[Ta=25°C]

| Characteristic | Symbol | Rating | Unit |
|-----------------------------|--|---------|------|
| Collector-base voltage | V_{CBO} | 60 | V |
| Collector-emitter voltage | ollector-emitter voltage V _{CEO} 50 | | V |
| Emitter-base voltage | V_{EBO} | 6 | V |
| Collector current | I_{C} | 3 | Α |
| Collector Down dissination | P_{C} | 1.1 | W |
| Collector Power dissipation | P _C * | 1.5 | W |
| Junction temperature | T _J | 150 | °C |
| Storage temperature range | T_{stg} | -55~150 | °C |

[★] Device mounted on ceramic substrate (250mm² x 0.8t)

Thermal Characteristics

[Ta=25℃]

| Charac | cteristic | Symbol | Rating | Unit |
|------------|------------------|----------------------|-------------------|-------|
| Thermal | Jungtion Ambient | ם | 113.6 | °C /M |
| resistance | Junction-Ambient | K _{th(J-A)} | 83.3 [*] | °C /W |

Electrical Characteristics

[Ta=25°℃]

| Chara | cteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit | |
|------------------------------|--------------------|-------------------|---|-------|------|------|------|--|
| Collector-emitter | breakdown voltage | BV _{CEO} | I _C =10mA, I _B =0 | 50 | - | - | V | |
| Collector cut-off c | urrent | I_{CBO} | V _{CB} =60V, I _E =0 | - | - | 0.1 | μА | |
| Emitter cut-off cu | rrent | I_{EBO} | V _{EB} =6V, I _C =0 | - | - | 0.1 | μА | |
| DC august sain | | h _{FE} | V _{CE} =2V, I _C =0.1A* | 120 | - | 240 | | |
| DC current gain | | h _{FE} | V _{CE} =2V, I _C =2A* | 40 | - | - | - | |
| Collector-emitter | saturation voltage | $V_{CE(sat)}$ | I _C =2A, I _B =0.1A* | 4* | | 0.35 | V | |
| Base-emitter satu | ration voltage | $V_{BE(sat)}$ | I _C =2A, I _B =0.1A* | 1.2 | | 1.2 | V | |
| Transition frequer | ісу | f _T | V _{CE} =10V, I _C =0.05A | - 210 | | - | MHz | |
| Collector output capacitance | | C _{ob} | V _{CB} =10V, I _E =0, f=1MHz | - | 18 | - | pF | |
| Switching Time | Turn-on Time | t _{on} | INPUT IN OUTPUT | - | 100 | _ | | |
| | Storage Time | t _{stg} | | - | 300 | - | nS | |
| | Fall Time | t _f | IBI=-IB2=100mA | - | 50 | - | | |

^{*:} Pulse test : $t_P \le 300 \mu s$, Duty cycle $\le 2\%$

Electrical Characteristic Curves

Fig. 1 $P_{\rm C}\,$ - T_a

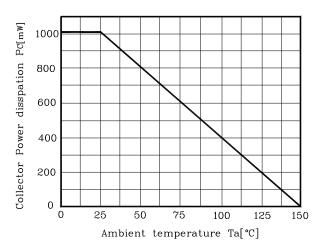


Fig. 2 I_C - V_{BE}

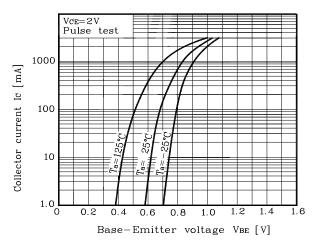


Fig. 3 $I_{\rm C}~$ - $V_{\rm CE}$

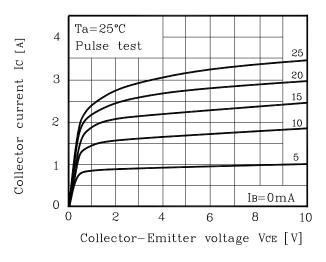


Fig. 4 h_{FE} - I_C

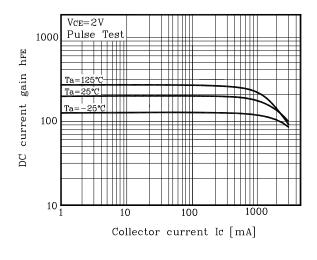


Fig. 5 $V_{\text{CE}(\text{sat})}$ - I_{C}

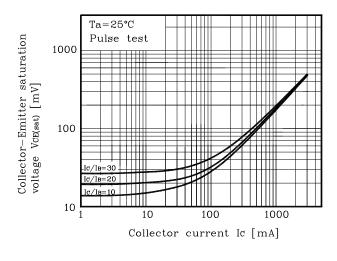
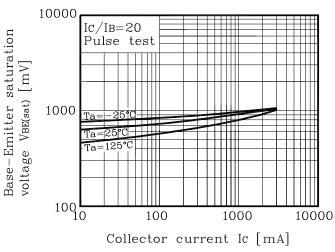


Fig. 6 $V_{BE(sat)}$ - I_C



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Electrical Characteristic Curves

Fig. 7 C_{Ob} - V_{CB}

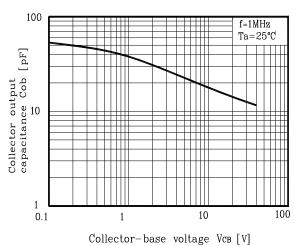
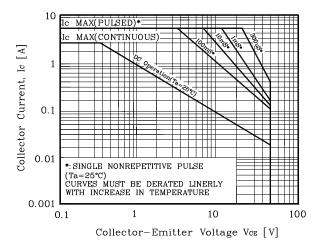
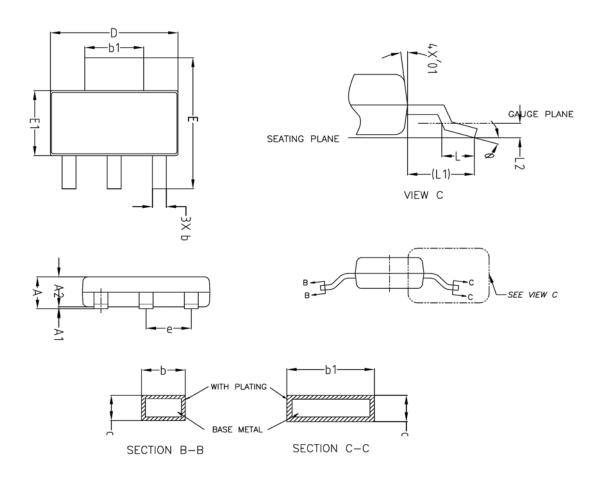


Fig. 8 Safe Operating Area

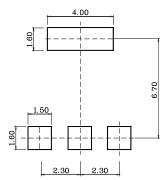


Outline Dimension (Unit: mm)



| | MILLIMETERS | | | | |
|----------------|-------------|----------|---------|------|--|
| SYMBOL | MINIMUM | NOMINAL | MAXIMUM | NOTE | |
| Α | _ | _ | 1.80 | | |
| A1 | 0.00 | _ | 0.10 | | |
| A2 | 1.60 | 1.65 | 1.70 | | |
| Ь | 0.68 | _ | 0.76 | | |
| Ь1 | 2.95 | _ | 3.07 | | |
| С | 0.23 | _ | 0.28 | | |
| D | 6.40 | 6.50 | 6.60 | | |
| E | 6.80 | 7.00 | 7.20 | | |
| E1 | 3.40 | 3.50 | 3.60 | | |
| е | 2.30 BSC | | | | |
| L | 0.45 | _ | 0.65 | | |
| L1 | 1.75 REF | | | | |
| L2 | | 0.10 BSC | | | |
| 0 | 0, | _ | 10° | | |
| 0 1 | 5* | _ | 10° | | |

***Recommend PCB solder land [Unit: mm]**



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