

STC503F

NPN Silicon Transistor

Applications

- Power amplifier application
- High current switching application

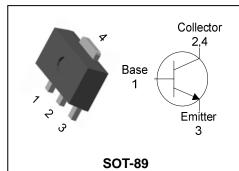
Features

- Power Transistor General Purpose application
 - Low saturation voltage

: V_{CE(sat)}=0.4V Typ.

• High Voltage: V_{CEO}=65V Min.

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
STC503F	C503 YWW	SOT-89

Absolute Maximum Ratings

[Ta=25°C]

Thosolute Maximum Ratings				
Characteristic	Symbol	Rating	Unit	
Collector-Base voltage	V_{CBO}	80	V	
Collector-Emitter voltage	V_{CEO}	65	V	
Emitter-base voltage	V_{EBO}	5	V	
Collector current	I _C	3	A(DC)	
Collector current	I _{CP} *	6	A(Pulse)	
Callector Dower dissinction	P _C	0.5	W	
Collector Power dissipation	P _C **	1	W	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55~150	°C	

^{*:} Single pulse, tp= 300 μ s

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Emitter breakdown voltage	BV _{CEO}	$I_C=1$ mA, $I_B=0$	65	-	-	V
Collector cut-off current	I _{CBO}	$V_{CB} = 65V, I_{E} = 0$	1	-	50	μΑ
Emitter cut-off current	I _{EBO}	$V_{EB}=5V$, $I_{C}=0$	1	-	50	μΑ
DC current gain	h _{FE} *	$V_{CE} = 5V$, $I_{C} = 0.5A$	300	-	500	-
Base-Emitter on voltage	V _{BE(ON)}	$V_{CE} = 5V$, $I_{C} = 0.5A$	-	0.7	1	V
Collector-Emitter saturation voltage	$V_{CE(sat)}$	$I_C = 2A$, $I_B = 0.2A$	-	0.4	1	V
Transition frequency	f _T	$V_{CB}=5V$, $I_{C}=50mA$	1	250	-	MHz
Collector output capacitance	C _{ob}	$V_{CB}=10V$, $I_{E}=0$, $f=1MHz$	1	15	-	pF

^{*} hFE rank : 300~500 Only

^{**:} When mounted on ceramic substrate(250 mm²×0.8t)

Electrical Characteristic Curves

Fig. 1 P_C - Ta

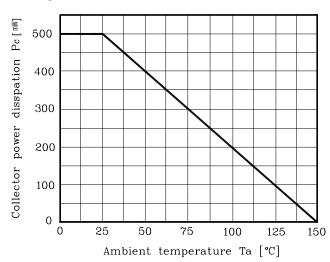


Fig. 2 I_C - V_{BE}

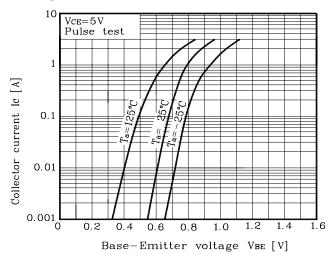


Fig. 3 $V_{CE(sat)}$. I_C

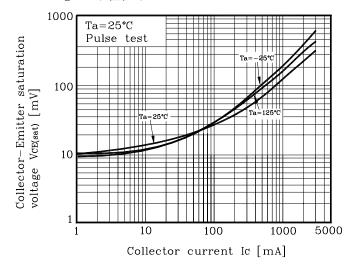


Fig. 4 I_C - V_{CE}

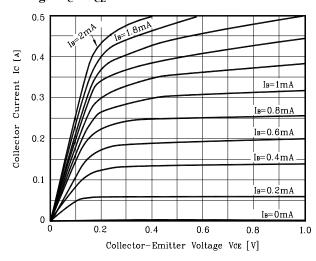


Fig. 5 I_{C} - V_{CE}

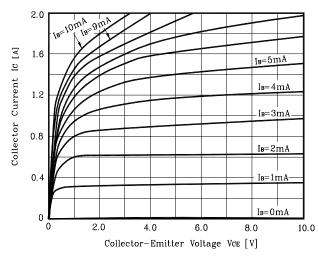
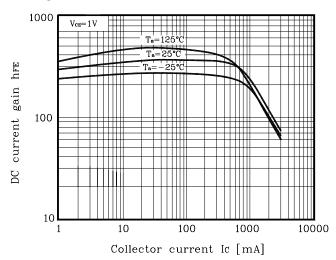


Fig. 6 h_{FE}.I_C



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Electrical Characteristic Curves

Fig. 7 $h_{FE}I_{C}$

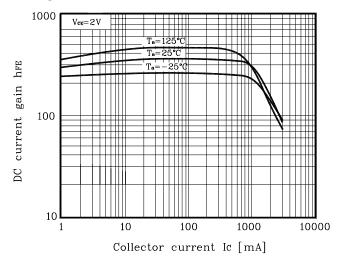


Fig. 8 h_{FE} I_C

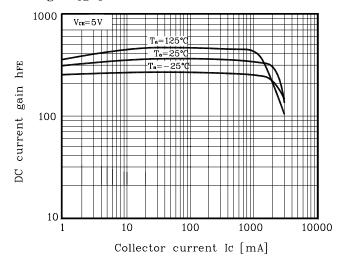


Fig. 9 Cob - V_{CB}

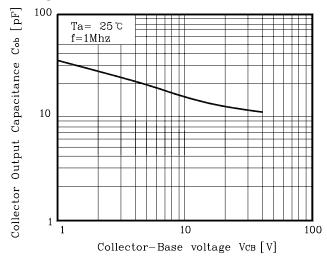


Fig. 10 f_{T} - I_{C}

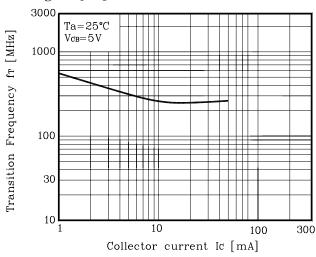
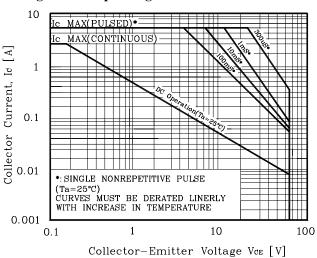
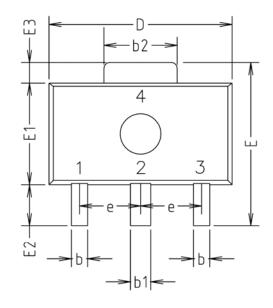


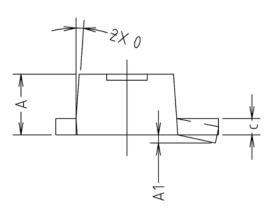
Fig. 11 Safe Operating Area



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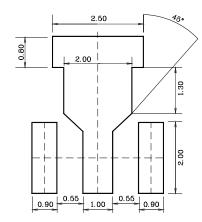
Outline Dimension(mm)





		MILLIMETERS	S	NOTE
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOTE
Α	1.40	1.50	1.60	
A1	0.00	_	0.10	
b	0.38	0.42	0.48	
b1	0.48	0.52	0.58	
b2	1.79	1.82	1.87	
С	0.40	0.42	0.46	
D	4.40	4.50	4.70	
Ε	3.70	4.00	4.30	
E1	2.40	2.50	2.70	
E2	0.80	1.00	1.20	
E3	0.40	0.50	0.60	
е		1.50 TYP.	·	
0		4° TYP.		

*Recommend PCB solder land [Unit: mm]



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