

**isc Silicon PNP Power Transistor**
**STD2805**
**DESCRIPTION**

- Low Collector-Emitter Saturation Voltage-  
:  $V_{CE(sat)} = -0.6V(\text{Max})$  ( $I_C = -5A$ ;  $I_B = -0.25A$ )
- DC Current Gain  $-h_{FE} = 85(\text{Min}) @ I_C = -5A$
- Fast -Switching speed
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

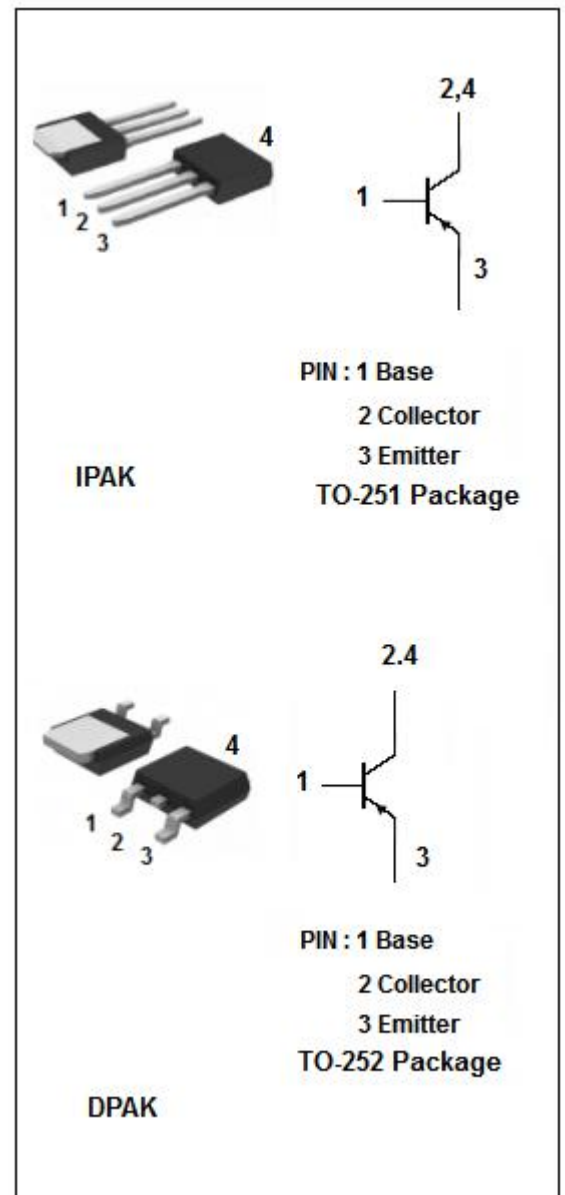
- CCFL drivers
- Voltage regulators
- Relay drivers
- High efficiency low voltage switching applications

**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-60	V
$V_{CEO}$	Collector-Emitter Voltage	-60	V
$V_{EBO}$	Emitter-Base Voltage	-6	V
$I_C$	Collector Current-Continuous	-5	A
$I_{CM}$	Collector Current-Peak	-10	A
$I_B$	Base Current	-2	A
$P_C$	Total Power Dissipation @ $T_C = 25^\circ\text{C}$	15	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-65~150	$^\circ\text{C}$

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	8.33	$^\circ\text{C/W}$



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## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> =-0.1mA, I <sub>C</sub> =0	-6			V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =-1mA, I <sub>B</sub> =0	-60			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> =-0.1mA, I <sub>E</sub> =0	-60			V
V <sub>CE(sat)</sub> *	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -100mA; I <sub>B</sub> = -5mA			-50	mV
		I <sub>C</sub> = -2A; I <sub>B</sub> = -50mA			-0.3	V
		I <sub>C</sub> = -3A; I <sub>B</sub> = -0.15A			-0.4	V
		I <sub>C</sub> = -5A; I <sub>B</sub> = -0.2A			-0.6	V
V <sub>BE(sat)</sub> *	Base-Emitter Saturation Voltage	I <sub>C</sub> = -2A; I <sub>B</sub> = -50mA			-1.2	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -60V; I <sub>E</sub> = 0			-100	nA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> =0			-100	nA
h <sub>FE</sub> *	DC Current Gain	I <sub>C</sub> = -100mA; V <sub>CE</sub> = -2V	200		400	
		I <sub>C</sub> = -5A; V <sub>CE</sub> = -2V	85			
		I <sub>C</sub> = -10A; V <sub>CE</sub> = -2V	20			
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = -50mA; V <sub>CE</sub> = -10V		150		MHZ
C <sub>OB</sub>	Output Capacitance	V <sub>CB</sub> =-10V; f=1.0MHz		60		pF

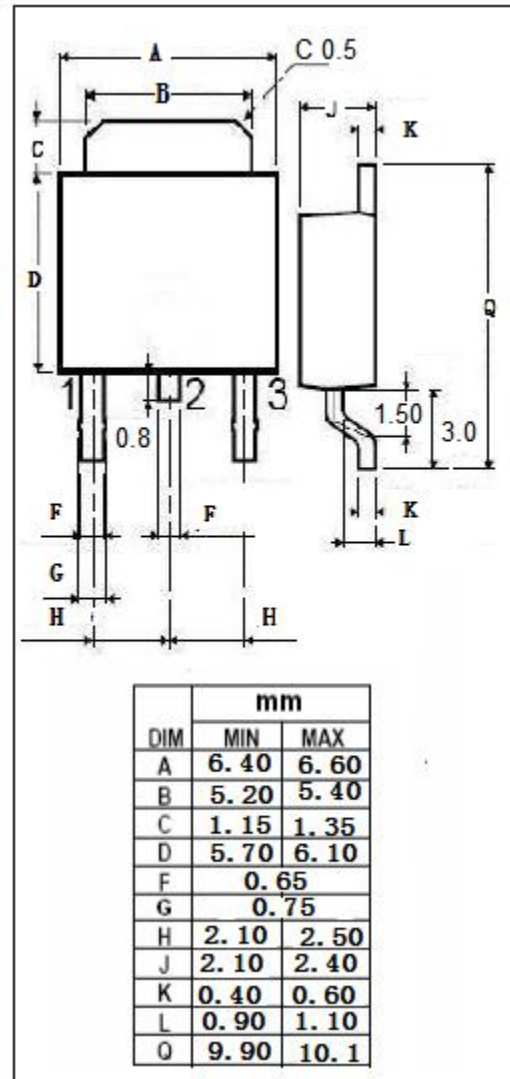
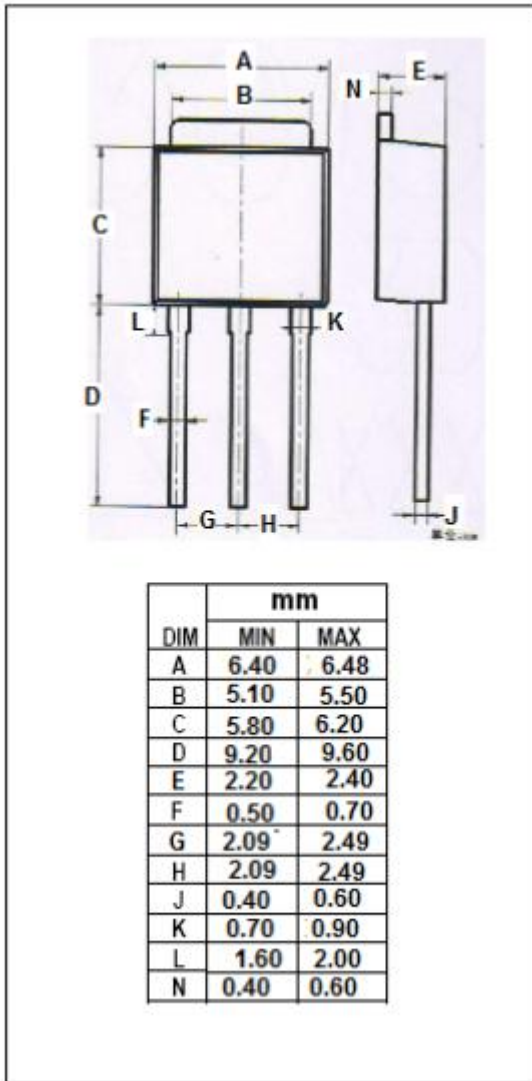
\* Pulse Test: PW≤300μs, Duty Cycle≤1.5%

Switching Times; Resistive Load

t <sub>on</sub>	Tur-on Time	I <sub>C</sub> = -1A; V <sub>CC</sub> = 30V; I <sub>B1</sub> = I <sub>B2</sub> = -0.1A		80		ns
t <sub>s</sub>	Storage Time			600		ns
t <sub>f</sub>	Fall Time			70		ns

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**Outline Drawing**

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