

STD361

NPN Silicon Transistor

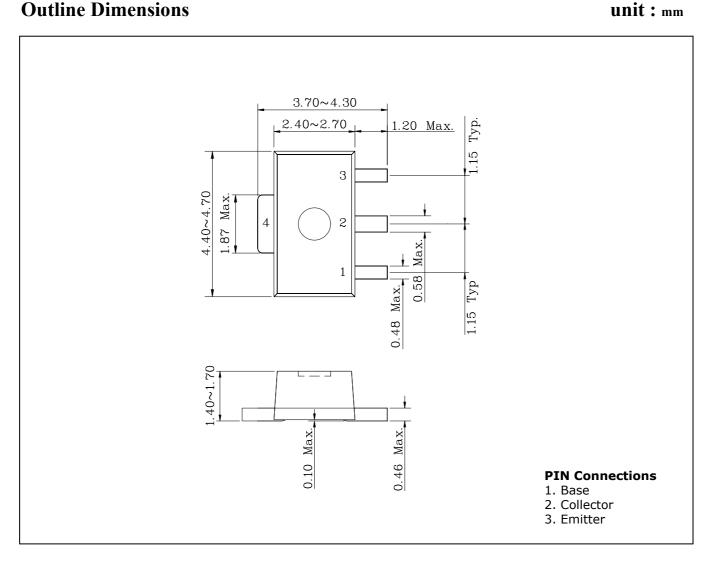
Description

- Extremely low collector-to-emitter saturation voltage ($V_{CE(SAT)}$ =0.2V Typ. @ I_C/I_B =3A/150 mA)
 - Suitable for low voltage large current drivers
 - Switching Application

Ordering Information

Type NO.	Marking	Package Code		
STD361	YA	SOT-89		

Outline Dimensions



KST-8007-002 1 Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit	
Collector-Base voltage	V_{CBO}	40	V	
Collector-Emitter voltage	V _{CEO}	15	V	
Emitter-Base voltage	V_{EBO}	7	V	
Collector current	I_{C}	5	Α	
Collector newer discipation	P _C	0.5	W	
Collector power dissipation	P _C *	2		
Junction temperature	Tı	150	°C	
Storage temperature	T _{stg}	-55~150	°C	

^{*:} When mounted on 40×40×0.8 mm ceramic substate

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	$I_{C}=50 \mu A, I_{E}=0$	40	-	1	V
Collector-Emitter breakdown voltage	BV _{CEO}	$I_C=1$ mA, $I_B=0$	15	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E=50 \mu A, I_C=0$	7	-	1	V
Collector cut-off current	I_{CBO}	$V_{CB} = 30V, I_{E} = 0$	-	-	0.1	μA
Emitter cut-off current	I_{EBO}	$V_{EB}=5V$, $I_C=0$	-	-	0.1	μA
DC current gain	h _{FE1}	V_{CE} =2V, I_{C} =500 mA	160	-	320	-
	h _{FE2}	$V_{CE}=2V$, $I_{C}=3A$	40	-	-	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	$I_C=3A$, $I_B=150$ mA	-	-	0.3	V
Transition frequency	f _T	V_{CE} =6 V , I_{E} =-50 mA	-	150	1	MHz
Collector output capacitance	C _{ob}	V_{CB} =20V, I_{E} =0, f=1 MHz	-	-	50	pF

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Electrical Characteristic Curves

Fig. 1 Pc - Ta

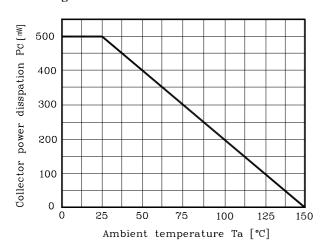


Fig. 2 h_{FE} - I_C

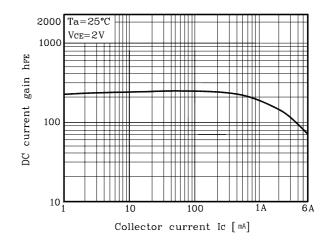


Fig. 3 $V_{CE(sat)}$ - I_C

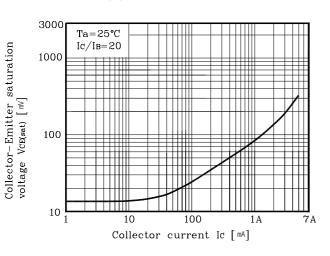


Fig. 4 f_T - I_C

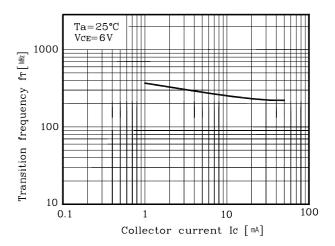
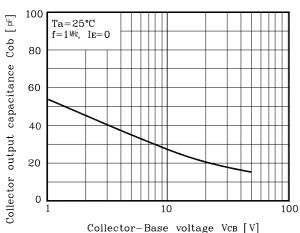


Fig. 5 C_{ob} - V_{CB}



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