

## INCHANGE SEMICONDUCTOR

## Isc N-Channel MOSFET Transistor

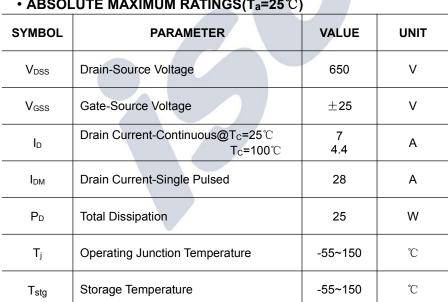
## **STF8N65M5**

## • FEATURES

- · Excellent switching performance
- · Easy to drive
- 100% avalanche tested
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

· Switching applications

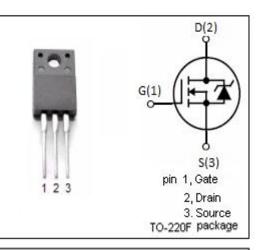


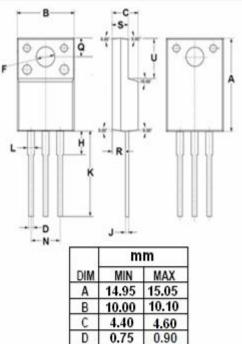
### • ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER		UNIT	
Rth(ch-c)	Channel-to-case thermal resistance	5	°C <b>/W</b>	
Rth(ch-a)	Channel-to-ambient thermal resistance	62.5	°C <b>/W</b>	

1





3.10

3.70

0.50

13.4 1.10

5.00

2.70

2.20

2.65

6.40

3.30

3.90

0.70

13.6

1.30

5.20

2.90

2.40

2.90

6.60

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isc website: www.iscsemi.cn



# **Isc N-Channel MOSFET Transistor**

## STF8N65M5

### **ELECTRICAL CHARACTERISTICS**

#### $T_{C}\text{=}25^{\circ}\!\!\!\mathrm{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 1mA	650			V
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>DS</sub> =±25V; I <sub>D</sub> =0.25mA	3		5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =3.5A		0.56	0.6	Ω
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±25V;V <sub>DS</sub> =0V			±0.1	μ Α
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 650V; V <sub>GS</sub> = 0V; TJ=25℃ TJ=125℃			1 100	μA
V <sub>SDF</sub>	Diode forward voltage	$I_{SD}$ =7A, $V_{GS}$ = 0 V			1.5	v

## **NOTICE:**

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2