



# STH360N4F6-2

## N-channel 40 V, 180 A STripFET™ VI DeepGATE™ Power MOSFET in H<sup>2</sup>PAK-2 package

Datasheet – preliminary data

### Features

Order code	V <sub>DSS</sub>	R <sub>DS(on)</sub> max	I <sub>D</sub>
STH360N4F6-2	40 V	< 1.25 mΩ	180 A <sup>(1)</sup>

1. Current limited by package

- Low gate charge
- Very low on-resistance
- High avalanche ruggedness

### Applications

- Switching applications

### Description

This device is an N-channel Power MOSFET developed using the 6<sup>th</sup> generation of STripFET™ DeepGATE™ technology, with a new gate structure. The resulting Power MOSFET exhibits the lowest R<sub>DS(on)</sub> in all packages.

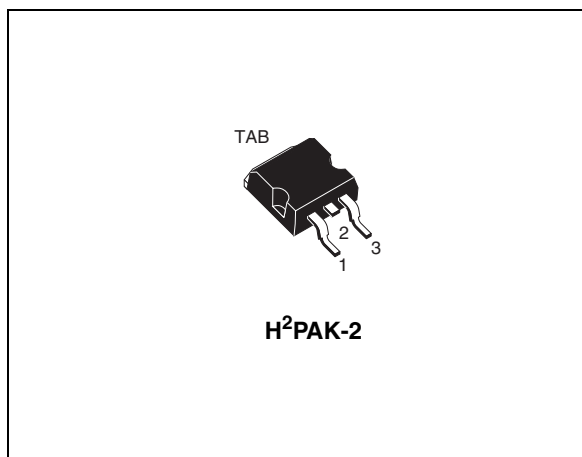
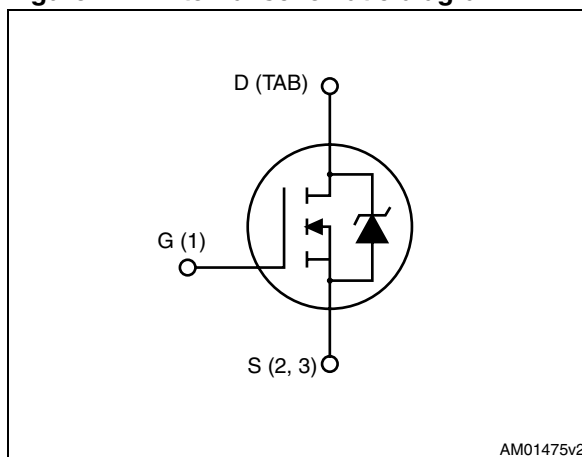


Figure 1. Internal schematic diagram



AM01475v2

Table 1. Device summary

Order code	Marking	Package	Packaging
STH360N4F6-2	360N4F6	H <sup>2</sup> PAK-2	Tape and reel

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# 1 Electrical ratings

**Table 2. Absolute maximum ratings**

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-source voltage	40	V
$V_{GS}$	Gate-source voltage	$\pm 20$	V
$I_D^{(1)}$	Drain current (continuous) at $T_C = 25\text{ }^\circ\text{C}$	180	A
$I_D^{(1)}$	Drain current (continuous) at $T_C = 100\text{ }^\circ\text{C}$	180	A
$I_{DM}^{(1)}$	Drain current (pulsed)	720	A
$P_{TOT}$	Total dissipation at $T_C = 25\text{ }^\circ\text{C}$	300	W
	Derating factor	2	W/ $^\circ\text{C}$
$T_{stg}$	Storage temperature	- 55 to 175	$^\circ\text{C}$
$T_j$	Operating junction temperature		

1. Current limited by package

**Table 3. Thermal data**

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case max	0.5	$^\circ\text{C}/\text{W}$
$R_{thj-pcb}^{(1)}$	Thermal resistance junction-pcb max	35	$^\circ\text{C}/\text{W}$

1. When mounted on FR-4 board of 1 inch<sup>2</sup>, 2 oz Cu

## 2 Electrical characteristics

( $T_{CASE} = 25\text{ °C}$  unless otherwise specified)

**Table 4. On/off states**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage ( $V_{GS} = 0$ )	$I_D = 250\ \mu\text{A}$	40			V
$I_{DSS}$	Zero gate voltage Drain current ( $V_{GS} = 0$ )	$V_{DS} = 40\ \text{V}$ $V_{DS} = 40\ \text{V}, T_C = 125\text{ °C}$			1 100	$\mu\text{A}$ $\mu\text{A}$
$I_{GSS}$	Gate-body leakage current ( $V_{DS} = 0$ )	$V_{GS} = \pm 20\ \text{V}$			$\pm 100$	nA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	3		4.5	V
$R_{DS(on)}$	Static drain-source on-resistance	$V_{GS} = 10\ \text{V}, I_D = 60\ \text{A}$		TBD	1.25	m $\Omega$

**Table 5. Dynamic**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$C_{iss}$	Input capacitance			17930		pF
$C_{oss}$	Output capacitance	$V_{DS} = 25\ \text{V}, f = 1\ \text{MHz},$ $V_{GS} = 0$	-	1560	-	pF
$C_{rss}$	Reverse transfer capacitance			1170		pF
$Q_g$	Total gate charge			340		nC
$Q_{gs}$	Gate-source charge	$V_{DD} = 20\ \text{V}, I_D = 120\ \text{A},$ $V_{GS} = 10\ \text{V}$	-	TBD	-	nC
$Q_{gd}$	Gate-drain charge			TBD		nC

**Table 6. Switching times**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ $t_r$	Turn-on delay time Rise time	$V_{DD} = 20\ \text{V}, I_D = 60\ \text{A}$ $R_G = 4.7\ \Omega, V_{GS} = 10\ \text{V}$	-	TBD	-	ns
$t_{d(off)}$ $t_f$	Turn-off-delay time Fall time		-	TBD	-	ns

**Table 7. Source drain diode**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$I_{SD}^{(1)}$	Source-drain current				180	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				720	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 180 \text{ A}, V_{GS} = 0$			1.1	V
$t_{rr}$	Reverse recovery time	$I_{SD} = 120 \text{ A}, V_{DD} = 32 \text{ V}$ $di/dt = 100 \text{ A}/\mu\text{s},$ $T_j = 150 \text{ }^\circ\text{C}$	-	TBD		ns
$Q_{rr}$	Reverse recovery charge					nC
$I_{RRM}$	Reverse recovery current					A

1. Current limited by package
2. Pulsed: pulse duration = 300  $\mu\text{s}$ , duty cycle 1.5%

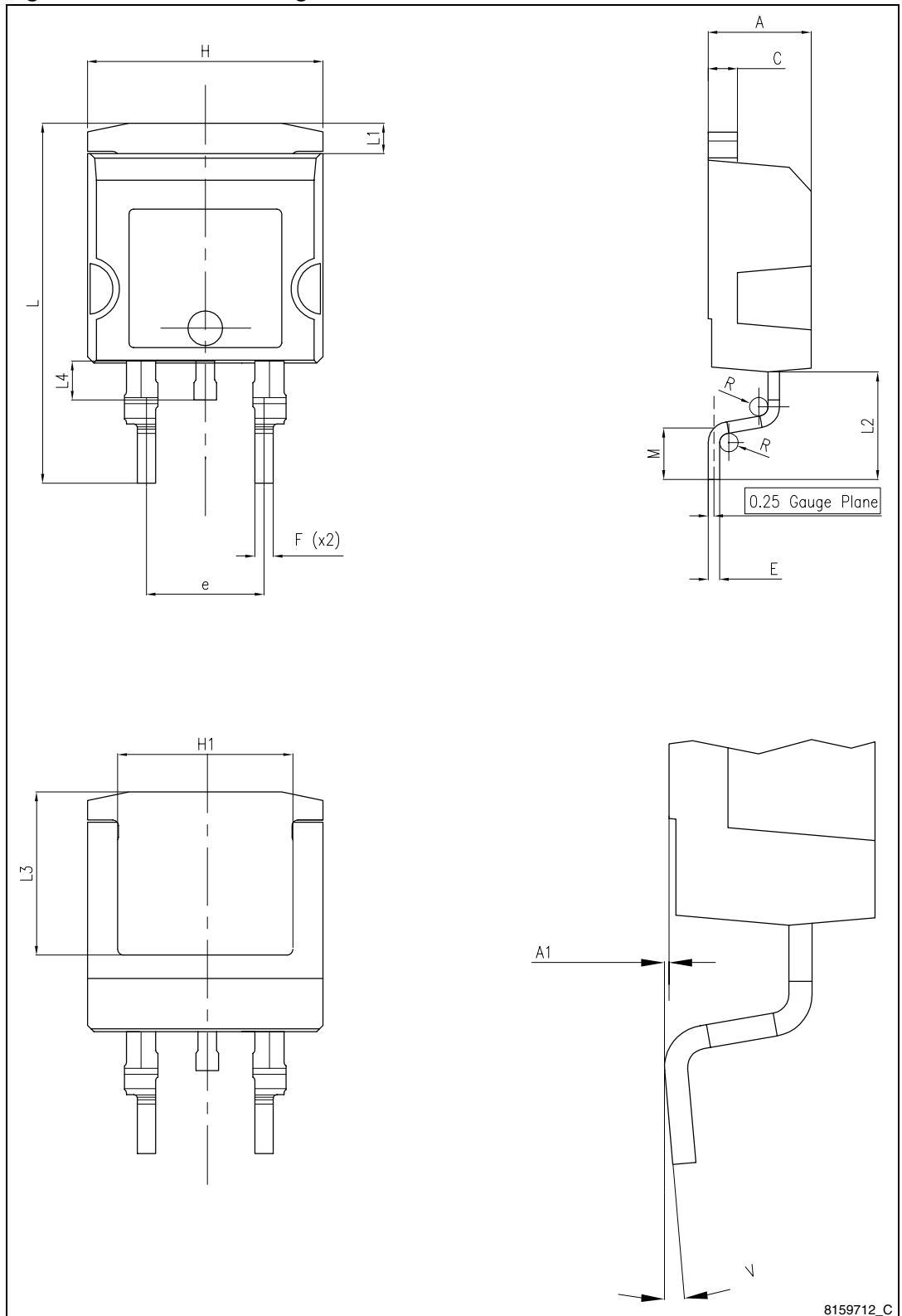
### 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK is an ST trademark.

**Table 8. H<sup>2</sup>PAK 2 mechanical data**

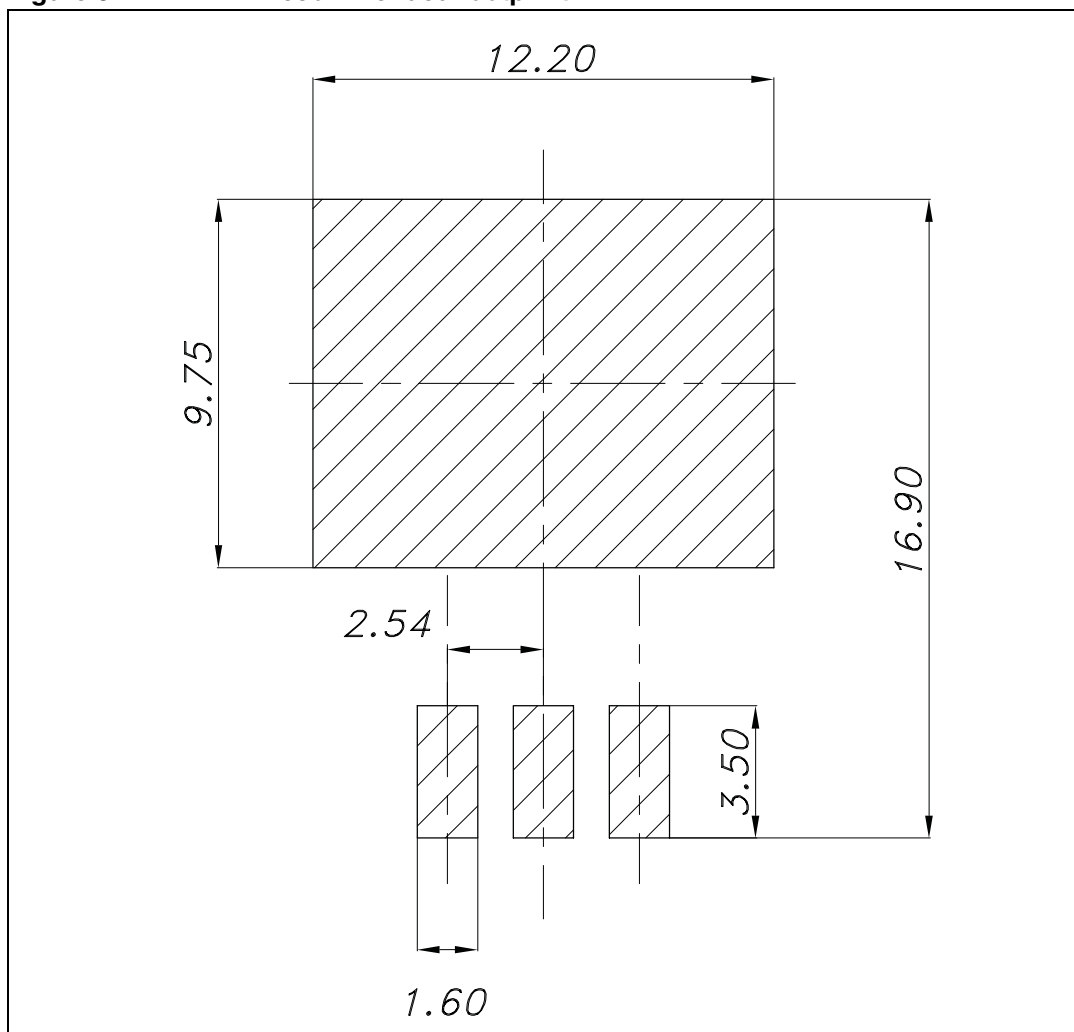
Dim.	mm		
	Min.	Typ.	Max.
A	4.30		4.80
A1	0.03		0.20
C	1.17		1.37
e	4.98		5.18
E	0.50		0.90
F	0.78		0.85
H	10.00		10.40
H1	7.40		7.80
L	15.30		15.80
L1	1.27		1.40
L2	4.93		5.23
L3	6.85		7.25
L4	1.5		1.7
M	2.6		2.9
R	0.20		0.60
V	0°		8°

Figure 2. H<sup>2</sup>PAK 2 drawing



8159712\_C

Figure 3. H<sup>2</sup>PAK 2 recommended footprint





## 4 Packaging mechanical data

Table 9. H<sup>2</sup>PAK 2 tape and reel mechanical data

Tape			Reel		
Dim.	mm		Dim.	mm	
	Min.	Max.		Min.	Max.
A0	10.5	10.7	A		330
B0	15.7	15.9	B	1.5	
D	1.5	1.6	C	12.8	13.2
D1	1.59	1.61	D	20.2	
E	1.65	1.85	G	24.4	26.4
F	11.4	11.6	N	100	
K0	4.8	5.0	T		30.4
P0	3.9	4.1			
P1	11.9	12.1	Base qty		1000
P2	1.9	2.1	Bulk qty		1000
R	50				
T	0.25	0.35			
W	23.7	24.3			

Figure 4. Tape

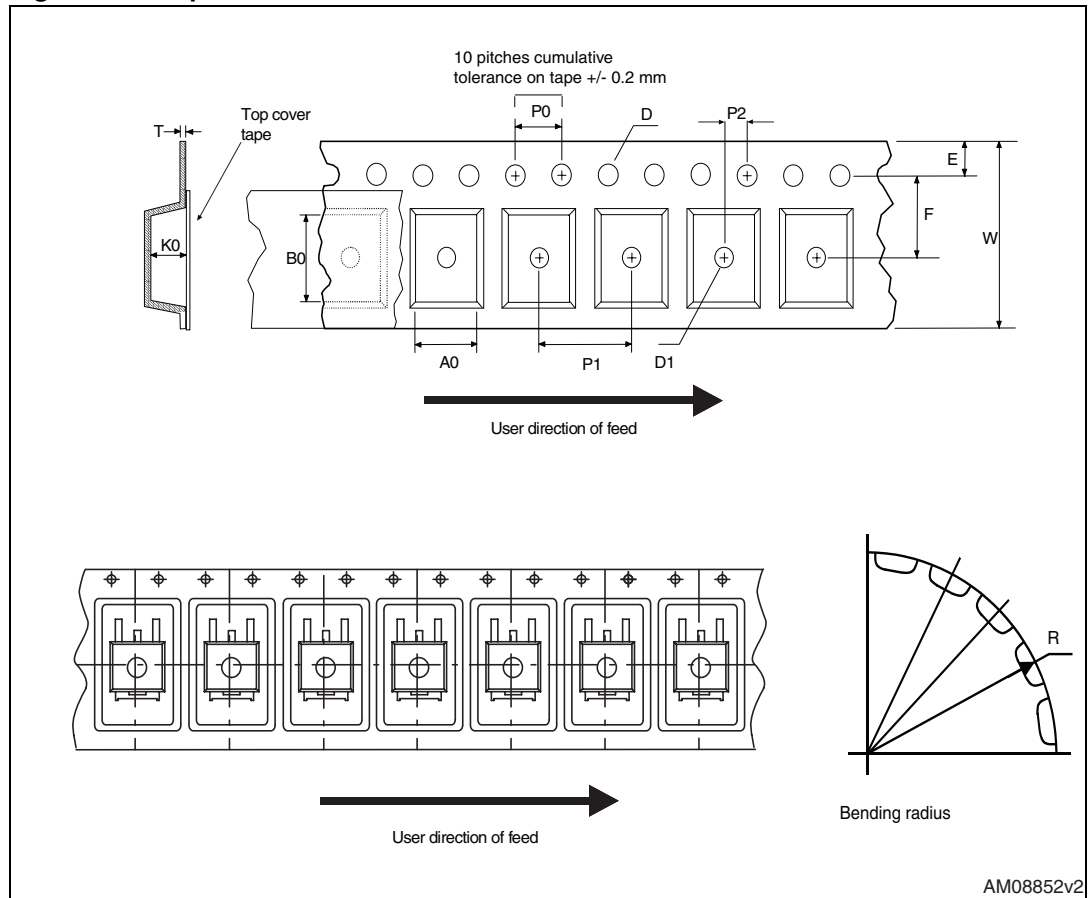
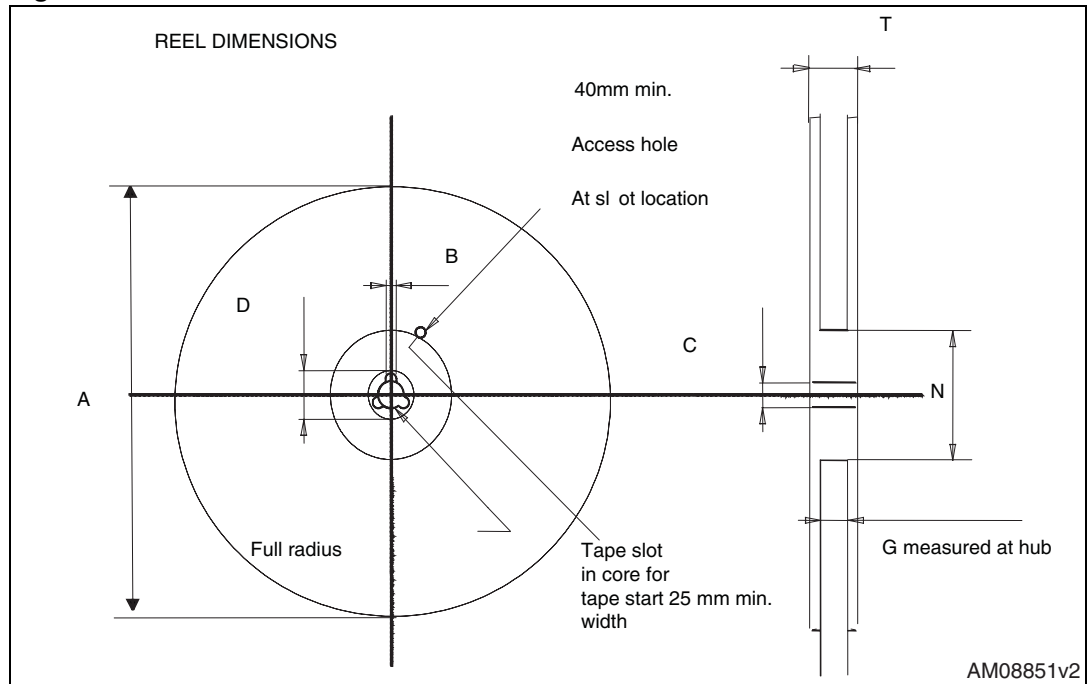


Figure 5. Reel



## 5 Revision history

Table 10. Document revision history

Date	Revision	Changes
08-Aug-2012	1	First release.

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