

# Isc N-Channel MOSFET Transistor

# STH80N10F7-2

### • FEATURES

- Extremely low gate charge
- Ultra low on-resistance
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • APPLICATIONS

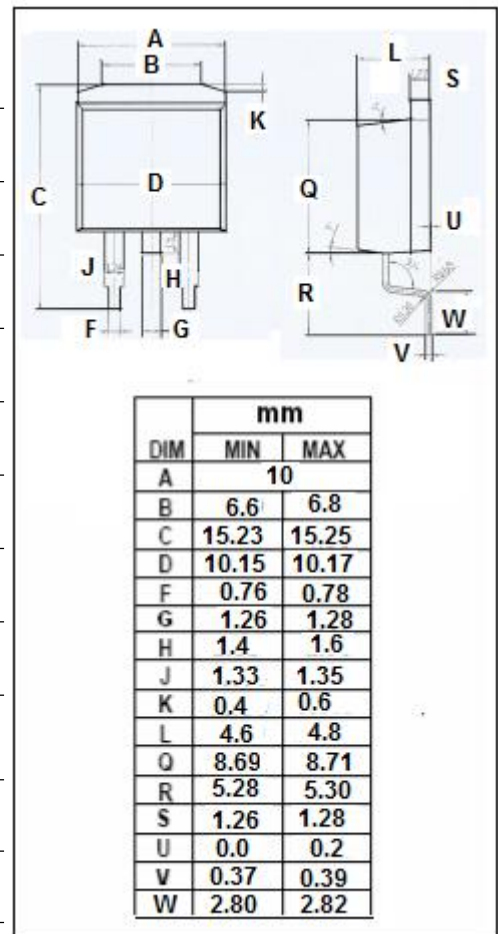
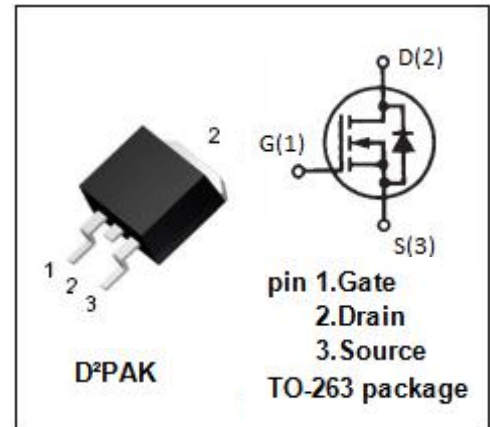
- Switching applications

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous@ $T_c=25^{\circ}\text{C}$ $T_c=100^{\circ}\text{C}$	80 54	A
$I_{DM}$	Drain Current-Single Pulsed	320	A
$P_D$	Total Dissipation	110	W
$T_j$	Max. Operating Junction Temperature	-55~175	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature	-55~175	$^{\circ}\text{C}$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.36	$^{\circ}\text{C/W}$



**Isc N-Channel MOSFET Transistor****STH80N10F7-2****ELECTRICAL CHARACTERISTICS**T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	100			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =±20V; I <sub>D</sub> =0.25mA	2.5		4.5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =40A			9.5	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V; V <sub>DS</sub> = 0V			±100	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 100V; V <sub>GS</sub> = 0V; T <sub>J</sub> =25°C T <sub>J</sub> =125°C			1 100	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> =80A, V <sub>GS</sub> = 0 V			1.1	V

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