

**Portable Equipment Application.
Notebook Application.**

Features

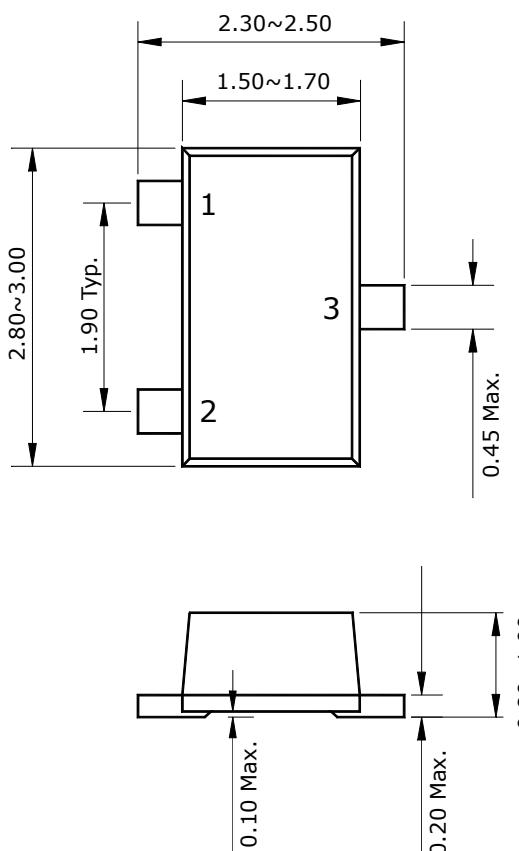
- Low $V_{GS(th)}$: $V_{GS(th)} = -0.6 \sim -1.4V$
- Small footprint due to small package
- Low $R_{DS(on)}$: $R_{DS(on)} = 68m\Omega$ (Typ.)

Ordering Information

Type NO.	Marking	Package Code
STJ001SF	J01	SOT-23F

Outline Dimensions

unit : mm



PIN Connections

1. Gate
2. Source
3. Drain

Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V _{DSS}	-20	V
Gate-source voltage	V _{GSS}	±12	V
Drain current (DC) **	I _D	-2.8	A
Drain current (Pulsed) *	I _{DP}	-11.2	A
Total Power dissipation **	P _D	0.5	W
Avalanche current (Single) ②	I _{AS}	-2.8	A
Single pulsed avalanche energy ②	E _{AS}	28	mJ
Avalanche current (Repetitive) ①	I _{AR}	-2.8	A
Repetitive avalanche energy ①	E _{AR}	1.3	mJ
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55~150	

* Limited by maximum junction temperature

** Device mounted on a glass-epoxy board

Characteristic	Symbol	Typ.	Max	Unit
Thermal resistance	R _{th(J-a)} **	-	250	°C/W

P-CH Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV _{DSS}	I _D =-250μA, V _{GS} =0	-20	-	-	V
Gate threshold voltage	V _{GS(th)}	I _D =-250μA, V _{DS} =V _{GS}	-0.6	-	-1.4	V
Drain-source cut-off current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-	1	μA
Gate leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
Drain-source on-resistance	R _{DS(ON)}	V _{GS} =-5.0V, I _D =-1.4A	-	68	88	mΩ
		V _{GS} =-2.5V, I _D =-1.4A	-	72	93	mΩ
Forward transfer conductance ④	g _{fs}	V _{DS} =-5V, I _D =-2.8A	-	15	-	S
Input capacitance	C _{iss}	V _{GS} =0V, V _{DD} =-10V, f=1MHz	-	880	1320	pF
Output capacitance	C _{oss}		-	210	320	
Reverse transfer capacitance	C _{rss}		-	110	170	
Turn-on delay time	t _{d(on)}	V _{DD} =-10V, I _D =-2.8A R _G =10Ω	-	5.2	-	ns
Rise time	t _r		-	10	-	
Turn-off delay time	t _{d(off)}		-	17.6	-	
Fall time	t _f		-	10	-	
Total gate charge	Q _g	V _{DD} =-10V, V _{GS} =-5V I _D =-2.8A	-	8.0	12	nC
Gate-source charge	Q _{gs}		-	1.3	2.0	
Gate-drain charge	Q _{gd}		-	2.3	3.5	

Source-Drain Diode Ratings and Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Source current	I _S	Integral reverse diode in the MOSFET	-	-	-0.5	A
Source current(Plused) ①	I _{SM}		-	-	-2.8	
Forward voltage ④	V _{SD}	V _{GS} =0V, I _S =-0.5A	-	-0.9	-1.3	V
Reverse recovery time	t _{rr}	I _s =-0.5A dI _s /dt=100A/us	-	73	-	ns
Reverse recovery charge	Q _{rr}		-	250	-	uC

Note :

- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ② L=2.0mH, I_{AS}=-2.8A, V_{DD}=-10V, R_G=25Ω
- ③ Pulse Test : Pulse Width < 300us, Duty cycle≤ 2%
- ④ Essentially independent of operating temperature

P-CH Electrical Characteristic Curves

Fig. 1 I_D - V_{DS}

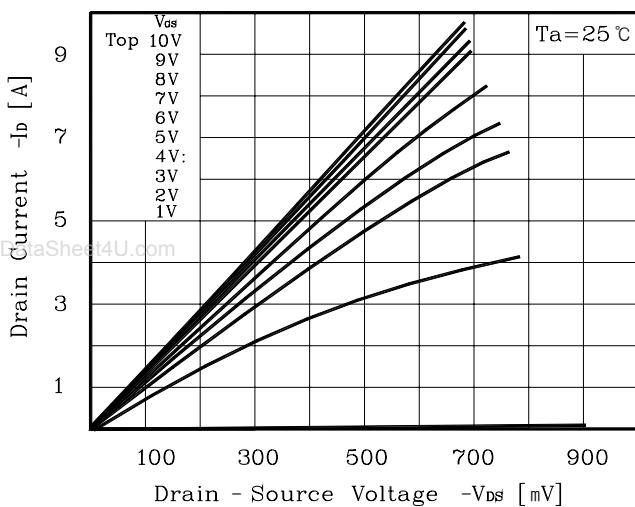


Fig. 2 I_D - V_{GS}

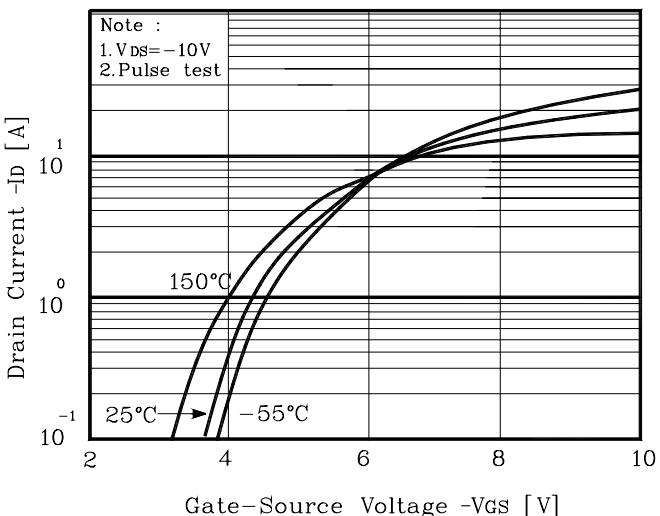


Fig. 3 $R_{DS(on)}$ - I_D

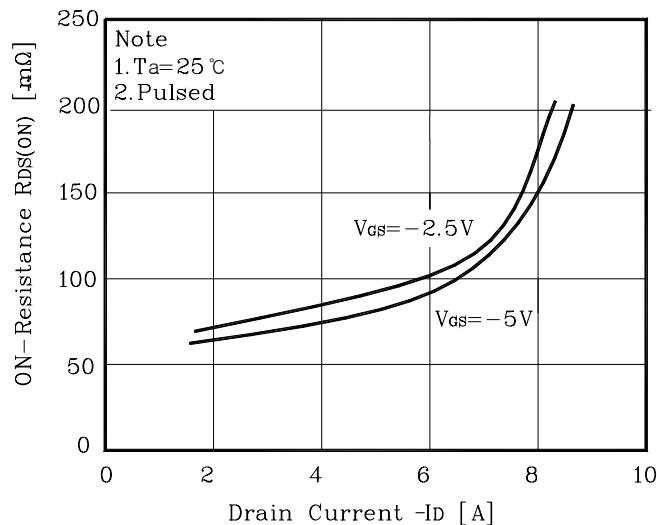


Fig. 4 I_S - V_{SD}

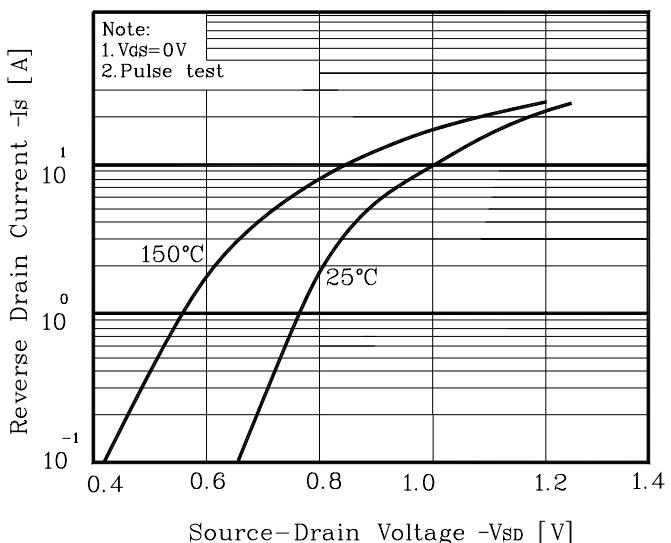


Fig. 5 Capacitance - V_{DS}

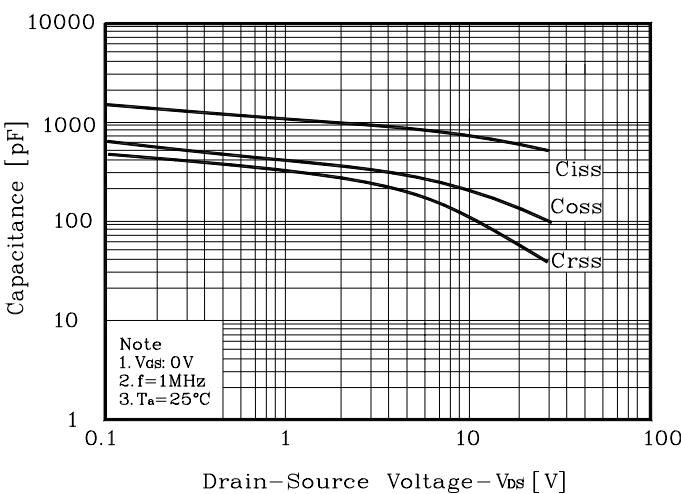


Fig. 6 V_{GS} - Q_G

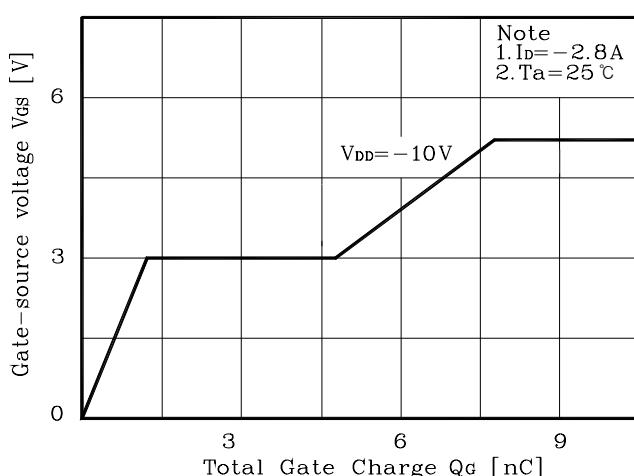


Fig. 7 V_{DSS} - T_J

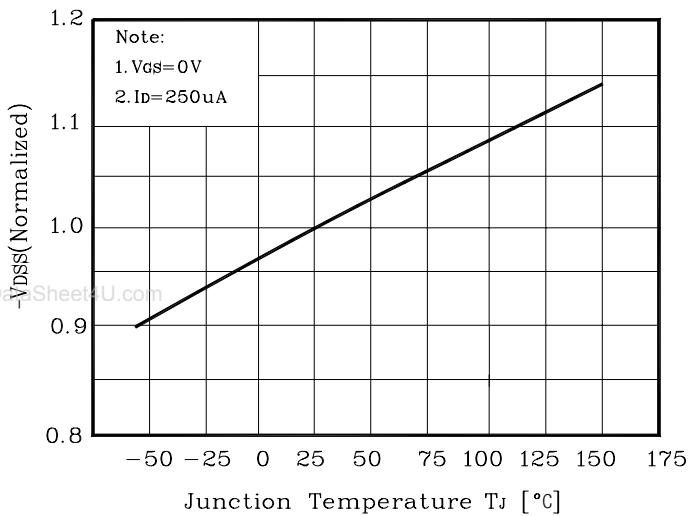


Fig. 8 $R_{DS(on)}$ - T_J

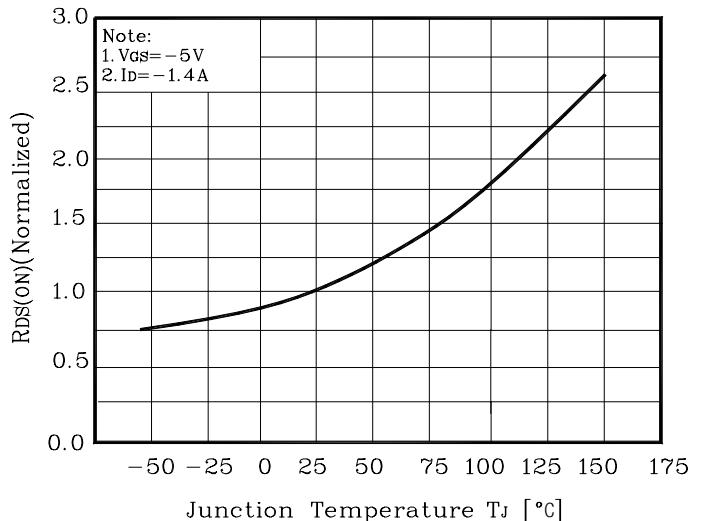


Fig. 9 I_D - T_a

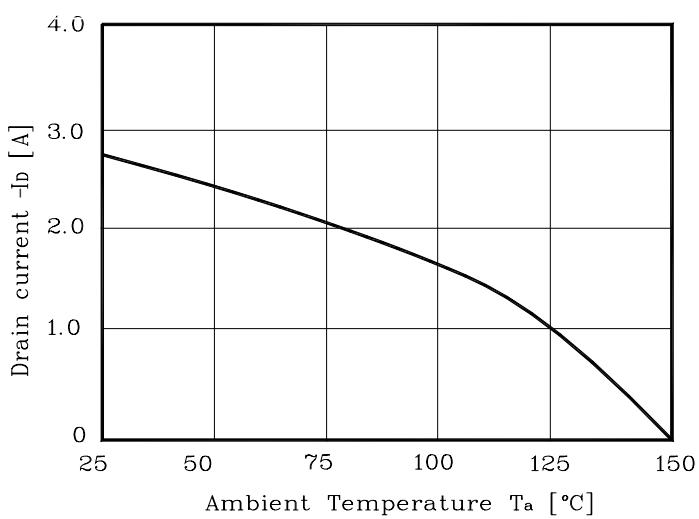


Fig. 10 Safe Operating Area

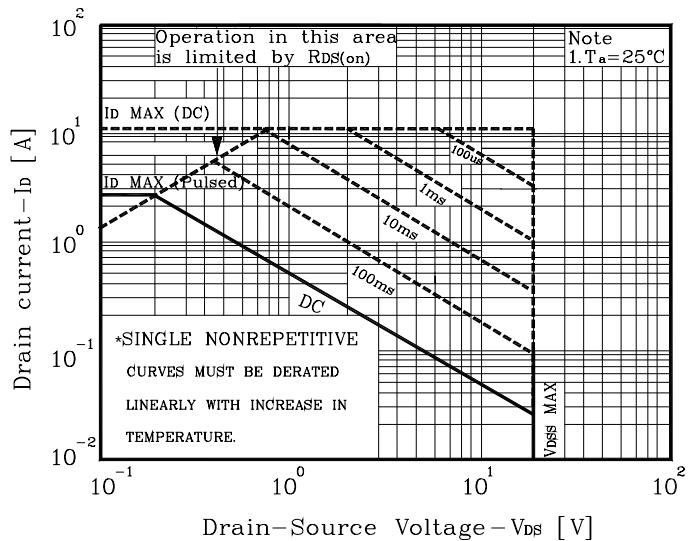


Fig. 11 Gate Charge Test Circuit & Waveform

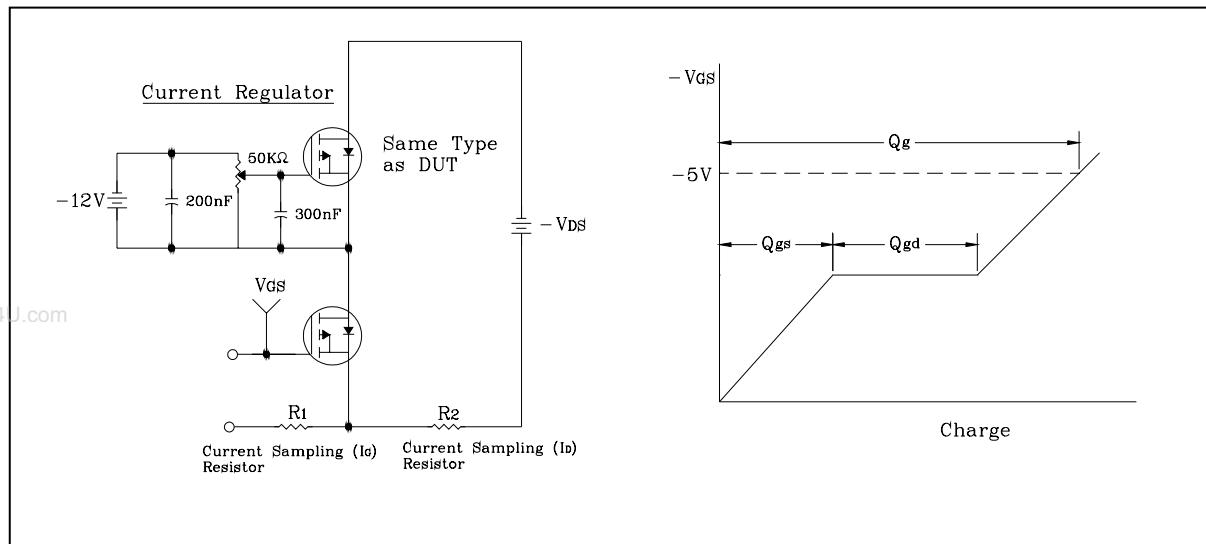


Fig. 12 Resistive Switching Test Circuit & Waveform

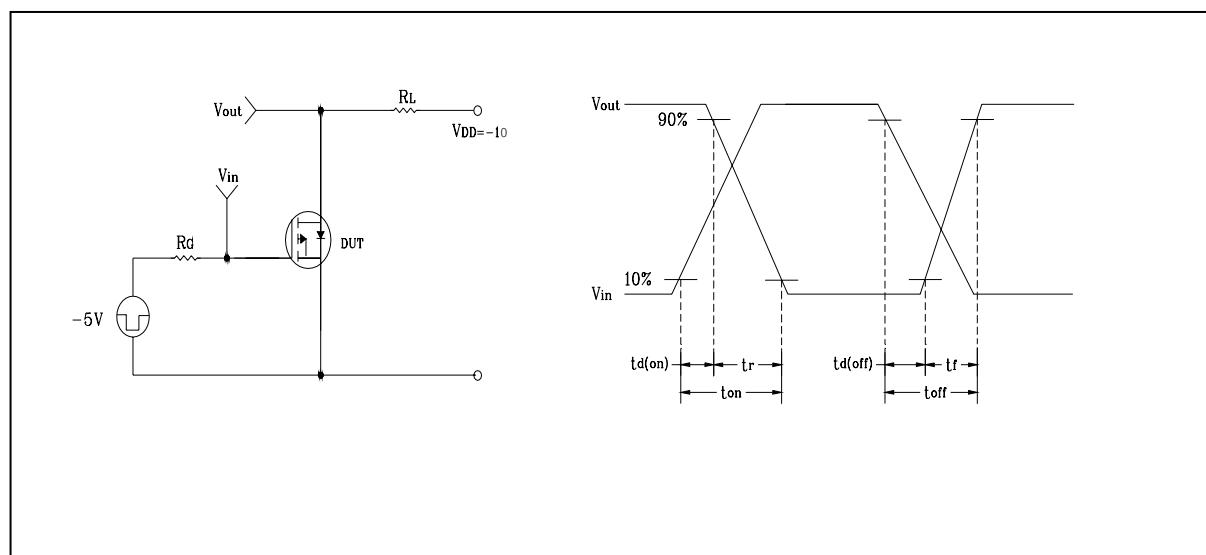


Fig. 13 E_{AS} Test Circuit & Waveform

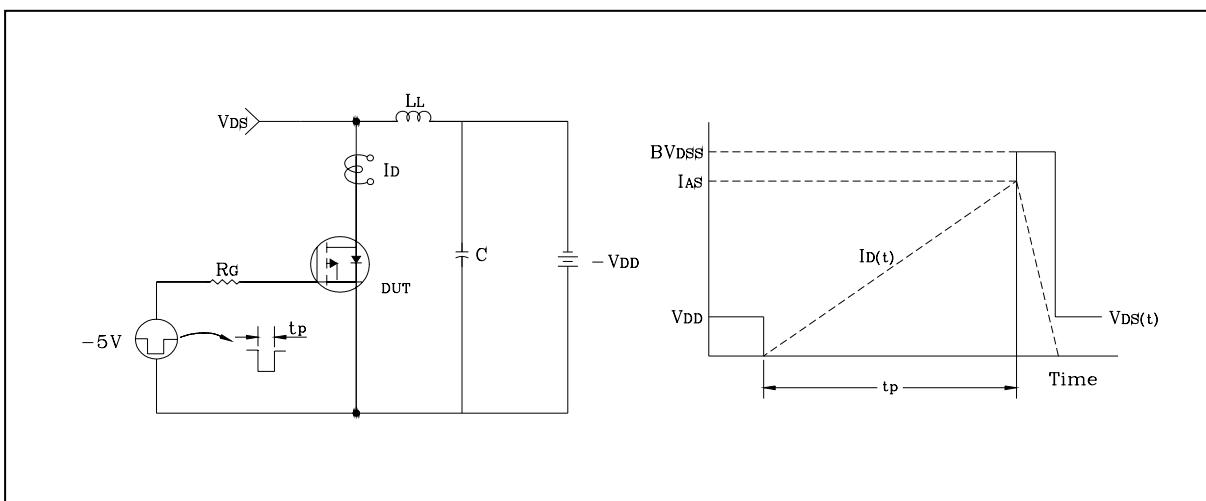
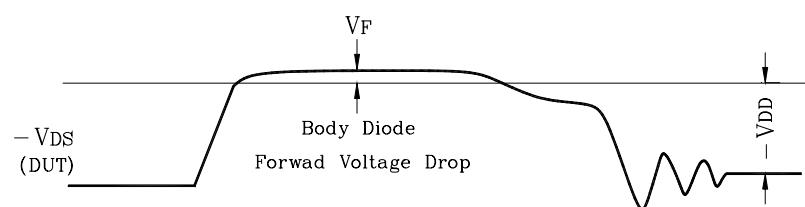
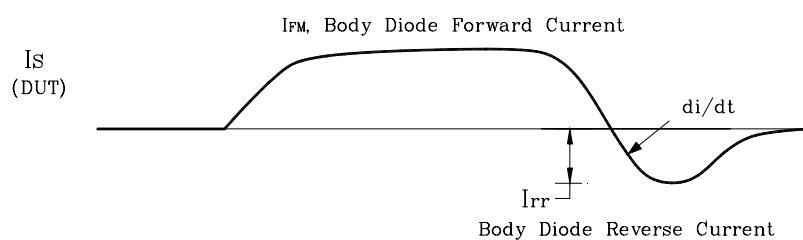
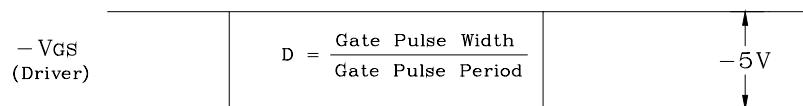
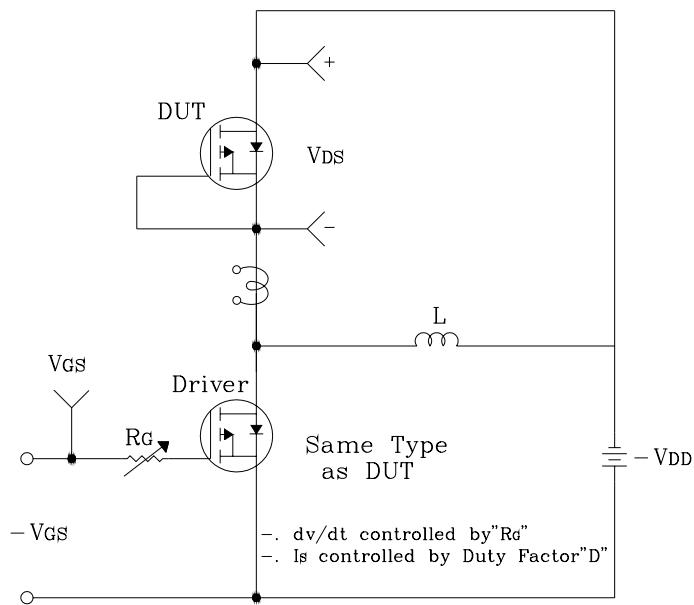


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



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