



STK 3251G TFT LCD driver board

Specifications

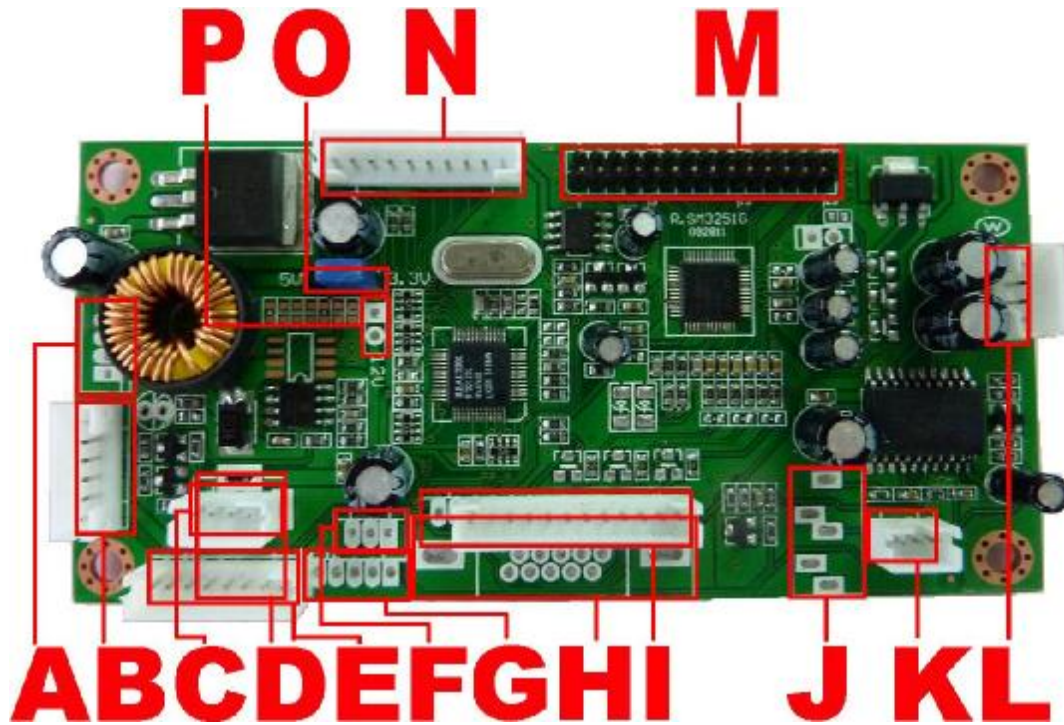
Version: 1.0

2009-08-15



STK 3251G TFT LCD driver board

Specifications



R.SM3251G board picture

n Board main function

- Support analog RGB signal
- Support two channel audio
- Support single/dual channel LVDS
- Support resolution up to SXGA/WXGA+(1440x900)

n Board data

support LCD	type	TFT-LCD
	signal interface	single/dual channel LVDS
	color	6 Bit/8 Bit



	aspect ratio	4:3 / 5:4 etc		
	size	6.4 inch ~ 26 inch		
	voltage	3.3V / 5V / 12V		
	brand			
input output interface	input	VGA	anologyRGB15pinD-sub terminals (socked)x1	
		audio	PC audio terminals × 1	
			PC audio socked × 1	
		power	DC source terminal(socked)× 1	
	preset source interface			
	output	keyboard	10Pin 2.0line-inset socked × 1	
		audio	4Pin 2.0 line-inset horn socked × 1	
		LVDS socked	30Pin dual line-inset gold pin × 1	
high voltage socked		6Pin 2.0 line-inset socked× 1		
singal type	VGA	synchronizing signal	line-field syns set	
		signal swing	0.7Vp-p	
		frequency range	HS: 30-88KHz; VS: 43-75Hz	
		DDC	VESA EDID V1.3	
power	input source	DC 12 V (+/-0.6V)		
	work mode	normal mode , low power consumption mode (<3W)		
	DPMS	support		
keyboard	support 5 grounded key			
remote control	unsupport			
PA	Class B, 2x3W (8Ω)			
main IC	RTD2025L/ RTD2525L	Scalar		
	RTD2120	MCU		
	TDA7496	audio processing IC		
dimensions	108mm(L)×51.6mm (W)			
OSD language	support Chinese,English etc			
other	support VGA interface upgrate			

n board structure picture



_MODE_1152x864_70HZ,
_MODE_1152x864_75HZ,
_MODE_1152x864_85HZ,
_MODE_1152x870_75HZ,
_MODE_1152x900_66HZ,
_MODE_1152x900_76HZ,
_MODE_1280x720_60HZ,
_MODE_1280x720_75HZ,
_MODE_1280x768_60HZ,
_MODE_1280x768_70HZ,
_MODE_1280x768_75HZ,
_MODE_1280x960_60HZ,
_MODE_1280x960_75HZ,
_MODE_1280x960_85HZ,
_MODE_1280x1024_60HZ,
_MODE_1280x1024_70HZ,
_MODE_1280x1024_75HZ,
_MODE_1280x1024_85HZ,
_MODE_1600x1200_60HZ,
_MODE_1600x1200_65HZ,
_MODE_1600x1200_70HZ,
_MODE_1600x1200_75HZ,
_MODE_1600x1200_85HZ,
_MODE_1680x1050_60HZ,
_MODE_1680x1050_75HZ,
_MODE_1920x1200_60HZ,
_MODE_1920x1200_75HZ,
_MODE_1920x1440_60HZ,
_MODE_1440x480i_60HZ,
_MODE_1920x1080i_60HZ,
_MODE_1920x1080_60HZ,
_MODE_1440x900_60HZ,
_MODE_1440x900_75HZ,

the default mode is the normal mode, special mode need to be add

n mark description

No	description	interface
A	<u>5V preset source socked interface</u>	-
B	<u>INVERTER interface</u>	-
C	<u>12V source socked interface</u>	-
D	12V source teminal interface	-



E	<u>preset source socked interface 1</u>	-
F	<u>IR preset socked interface</u>	-
G	<u>preset source socked interface 2</u>	-
H	<u>VGA signal input teminal interface</u>	-
I	<u>VGA signal input socked interface</u>	-
J	<u>audio input teminal interface</u>	-
K	<u>audio input socked interface</u>	-
L	<u>audio output socked interface</u>	-
M	<u>LVDS signal interface</u>	-
N	<u>key socked interface</u>	-
O	<u>3.3/5V panel source interface</u>	-
P	<u>12V panel source interface</u>	-

n input/output interface description

A (4PIN/2.0): 5V preset source socked interface

pin No	definition	description
1	GND	ground
2	GND	ground
3	5V	5V source
4	5V	5V source

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B (6PIN/2.0): INVERTER socked interface

pin No	definition	description
1	12V	12V INVERTER source
2	12V	12V INVERTER source
3	BLON	INVERTER switch control
4	ADJ	backlight brightness adjust
5	GND	ground
6	GND	ground

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C (4PIN/2.0): 12V source socked interface

pin No	definition	description
1	GND	ground
2	GND	ground
3	12V	12V source
4	12V	12V source

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E (8PIN/2.0) preset source socked interce 1

pin No	definition	description
1	5V	5V source
2	5V	5V source
3	ADJ	backlight brightness adjust
4	BLON	INVERTER switch control
5	GND	ground
6	GND	ground
7	12V	12V source
8	12V	12V source

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F (3PIN/2.0): IR preset socked interce

pin No	definition	description
1	5V	5V source
2	GND	ground
3	IR	IR input

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G (5PIN/2.0) preset source socked interce 2

pin No	definition	description
1	GND	ground
2	PVCC	panel source 3.3/5/12V
5	GND	ground
4	12V	12V source
5	GND	ground

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O (3PIN/2.5) 3.3V/5V panel source interface

pin No	definition	description
1	3.3V	3.3V source
2	PVCC	panel source
3	5V	5V source

P (2PIN/2.5) 12V panel source interface

pin No	definition	description
1	PVCC	panel source
2	12V	12V source

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N (10PIN/2.0) key socket interface

pin No	definition	description
1	K0	key socket K0
2	LED-R	red LED
3	LED-G	green LED
4	GND	ground
5	K1	key socket K1
6	K2	key socket K2
7	K3	key socket K3
8	K4	key socket K4
9	K5	key socket K5
10	K6	key socket K6

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I (14PIN/2.0): VGA signal input socked interface

pin No	definition	description
1	GND	ground
2	VS	field Sync
3	HS	Line Sync
4	GND	ground
5	RIN	red signal input
6	GND	ground
7	GIN	green signal input
8	GND	ground
9	BIN	blue signal input
10	GND	ground
11	SDA	DDC data interface(or upgrate interface)
12	SCL	DDC time interface(or upgrate interface)
13	RXD	serial receive interface
14	TXD	serial port interface

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K (3PIN/2.0) audio input socked interface

pin No	definition	description
1	L_IN	left sound channel
2	GND	ground
3	R_IN	right sound channel

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L (4PIN/2.0) audio output socked interface

pin No	definition	description
1	L_OUT	left sound channel
2	GND	ground
3	GND	ground
4	R_OUT	right sound channel

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M (30PIN/2.0): LVDS signal interface

pin No	definition	type	description
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1	VDD	Power	Power for Panel
2	VDD	Power	Power for Panel
3	VDD	Power	Power for Panel
4	GND	Ground	Ground
5	GND	Ground	Ground
6	GND	Ground	Ground
7	Rx0 0-	Output	LVDS Signal of Odd Channel 0(-)
8	Rx0 0+	Output	LVDS Signal of Odd Channel 0(+)
9	Rx0 1-	Output	LVDS Signal of Odd Channel 1(-)
10	Rx0 1+	Output	LVDS Signal of Odd Channel 1(+)
11	Rx0 2-	Output	LVDS Signal of Odd Channel 2(-)
12	Rx0 2+	Output	LVDS Signal of Odd Channel 2(+)
13	GND	Ground	Ground
14	GND	Ground	Ground
15	Rx0 C-	Output	LVDS Signal of Odd Channel Clock(-)
16	Rx0 C+	Output	LVDS Signal of Odd Channel Clock(+)
17	Rx0 3-	Output	LVDS Signal of Odd Channel 3(-)
18	Rx0 3+	Output	LVDS Signal of Odd Channel 3(+)
19	RxE 0-	Output	LVDS Signal of Even Channel 0(-)
20	RxE 0+	Output	LVDS Signal of Even Channel 0(+)
21	RxE 1-	Output	LVDS Signal of Even Channel 1(-)
22	RxE 1+	Output	LVDS Signal of Even Channel 1(+)
23	RxE 2-	Output	LVDS Signal of Even Channel 2(-)
24	RxE 2+	Output	LVDS Signal of Even Channel 2(+)
25	GND	Ground	Ground
26	GND	Ground	Ground
27	RxE C-	Output	LVDS Signal of Even Channel Clock(-)
28	RxE C+	Output	LVDS Signal of Even Channel Clock(+)
29	RxE 3-	Output	LVDS Signal of Even Channel 3(-)
30	RxE 3+	Output	LVDS Signal of Even Channel 3(+)

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n transport, storage, use requirements

- avoid weight, bending, deformation
- avoid electrostatic
- relative humidity: $\leq 80\%$
- storage temperature: $-10\sim+60$
- operating temperature: $0\sim+40$