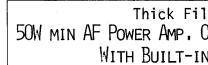


No.967

STK8250



Thick Film Hybrid Integrated Circuit 50W MIN AF POWER AMP. OUTPUT STAGE (DUAL SUPPLIES) WITH BUILT-IN QUASI CLASS A BIAS CIRCUIT

Features

- 1. Switching distortion peculiar to class B amp. is zero.
- 2. Since power stage, bias controller, and temperature compensator are incorporated on the IMST substrate having good thermal conduction, no complicated temperature compensation using thermistor, etc. is required, thereby enabling good thermal stability.
- By setting bias current externally, optimum conditions can be set.

Maximum Ratings at Ta=25°C Maximum Supply Voltage	V _{CC} max 0j-C			± 56	unit V
Thermal Resistance	⊖j~c	Ideal heat dissipation		1.8	OC/W
Collector Current	Ic			5	A
Junction Temperature	I _Ç Tj			150	ОС
Storage Temperature	Tstg		-30	to +105	°C
Available Time for Load	ts	$V_{CC} = \pm 38V + , f = 50Hz,$		2	sec
Shorted		V _{CC} =±38V*,f=50Hz, Po=50W,R _L =80hm			

Recommended Operating Conditions at Ta=25°C unit **±**38 Recommended Supply Voltage V Load Resistance ohm

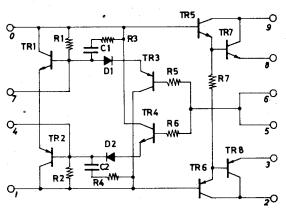
Operating Characteristics at Ta=25°C, V_{CC}=±38V, R_L=80hm, Rg=6000hm, VG=26.5dB, at specified test circuit (based on Sample Application

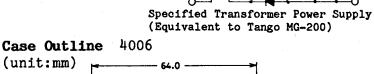
Circuit) min typ max unit Quiescent Current V_C==43V** Icco 80 mΑ THĎ=0.01%.f=20Hz to 20kHz Output Power Po W Total Harmonic Distortion THD Po=1.0 to 50W. 0.01 f=20Hz to 20kHz

*: For measuring available time for load shorted, use the o specified transformer power supply shown right.

**: Maximize VR1(30kB) of Application Circuit.

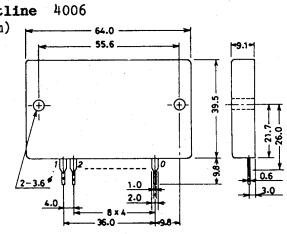
Equivalent circuit





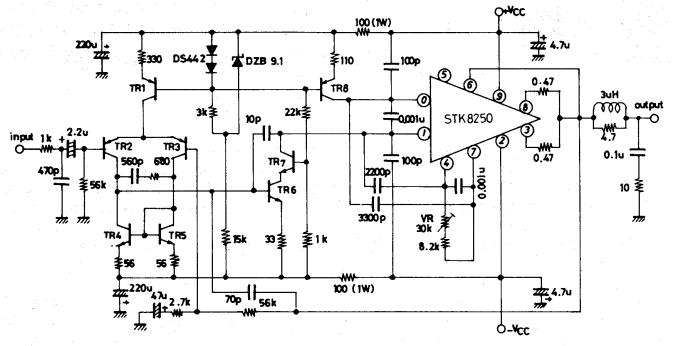
+ VCC

∙Vċc



These specifications are subject to change without notice

Sample Application Circuit: 50W min AF Power Amp.



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