



SamHop Microelectronics Corp.

# STM4800S

Jun.07 2006

## N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DSON</sub> (mΩ) Max
30V	8A	20 @ V <sub>GS</sub> = 10V
		28 @ V <sub>GS</sub> = 4.5V

### FEATURES

- Super high dense cell design for low R<sub>DSON</sub>.
- Rugged and reliable.
- Surface Mount Package.



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous @ T <sub>j</sub> =25°C -Pulsed <sup>b</sup>	I <sub>D</sub>	8	A
	I <sub>DM</sub>	32	A
Drain-Source Diode Forward Current	I <sub>S</sub>	1.7	A
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	2.5	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	50	°C/W
---	------------------	----	------

# STM4800S

ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$		1		$\mu\text{A}$
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$		$\pm 100$		$\text{nA}$
ON CHARACTERISTICS <sup>b</sup>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	1.7	3	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=8\text{A}$		15	20	$\text{m ohm}$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=7\text{A}$		19	28	$\text{m ohm}$
On-State Drain Current	$I_{\text{D(ON)}}$	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=10\text{V}$	10			A
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=8\text{A}$		17		S
DYNAMIC CHARACTERISTICS <sup>c</sup>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		820		$\text{pF}$
Output Capacitance	$C_{\text{OSS}}$			177		$\text{pF}$
Reverse Transfer Capacitance	$C_{\text{RSS}}$			60		$\text{pF}$
Gate resistance	$R_g$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1.0\text{MHz}$		2.6		$\text{ohm}$
SWITCHING CHARACTERISTICS <sup>c</sup>						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}}=15\text{V}$ $I_{\text{D}}=1\text{ A}$ $V_{\text{GS}}=10\text{V}$ $R_{\text{GEN}}=6\text{ ohm}$		10		ns
Rise Time	$t_r$			11		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			36		ns
Fall Time	$t_f$			9		ns
Total Gate Charge	$Q_g$	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=8\text{A}, V_{\text{GS}}=10\text{V}$		15		$\text{nC}$
		$V_{\text{DS}}=15\text{V}, I_{\text{D}}=8\text{A}, V_{\text{GS}}=4.5\text{V}$		7.7		$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=8\text{A}$ $V_{\text{GS}}=10\text{V}$		2.2		$\text{nC}$
Gate-Drain Charge	$Q_{\text{gd}}$			4.7		$\text{nC}$

# STM4800S

ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ C$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0 \text{ V}$ , $I_S = 1.7 \text{ A}$		0.8	1.2	V

## Notes

- a.Surface Mounted on FR4 Board,  $t \leq 10 \text{ sec}$ .
- b.Pulse Test:Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
- c.Guaranteed by design, not subject to production testing.

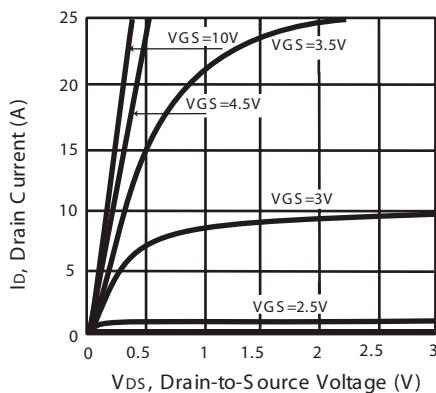


Figure 1. Output Characteristics

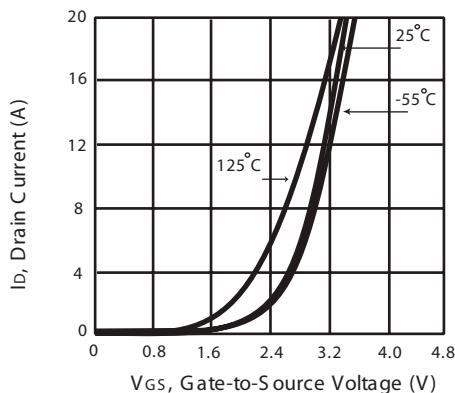


Figure 2. Transfer Characteristics

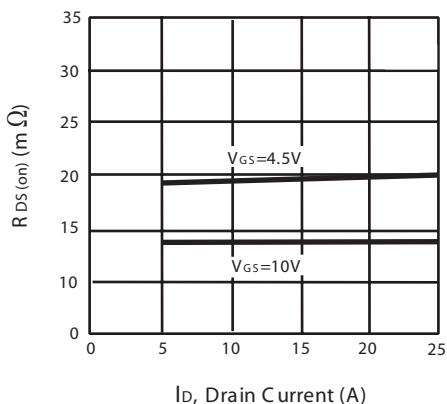


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

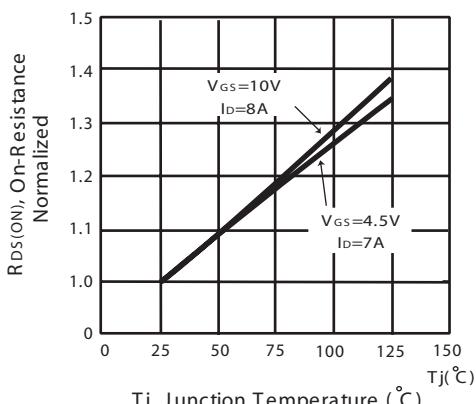


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STM4800S

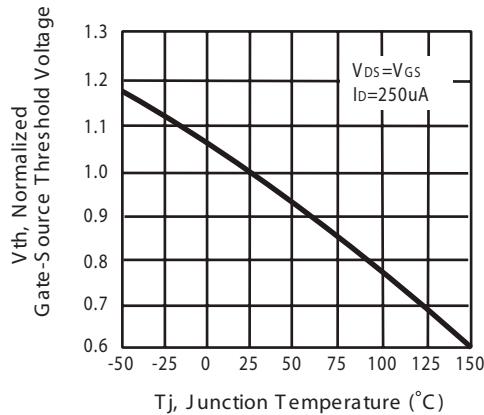


Figure 5. Gate Threshold Variation with Temperature

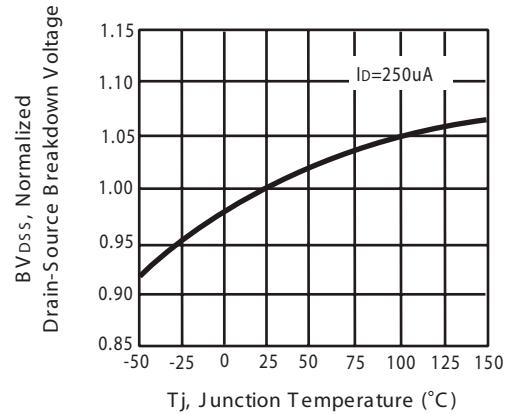


Figure 6. Breakdown Voltage Variation with Temperature

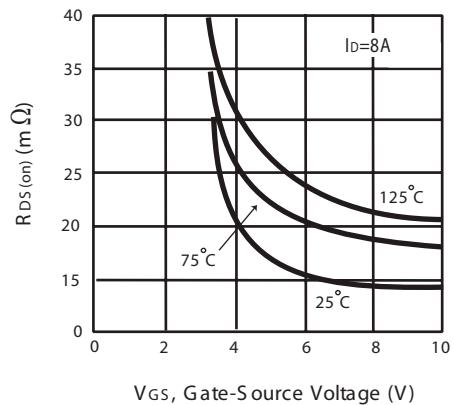


Figure 7. On-Resistance vs. Gate-Source Voltage

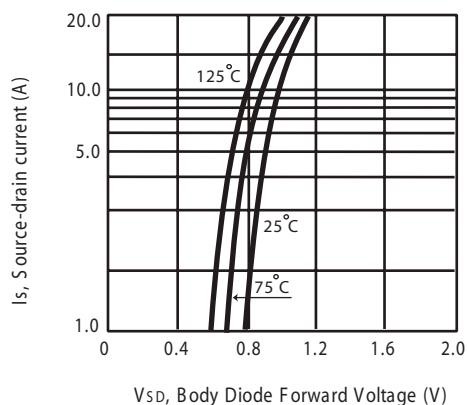


Figure 8. Body Diode Forward Voltage Variation with Source Current

# STM4800S

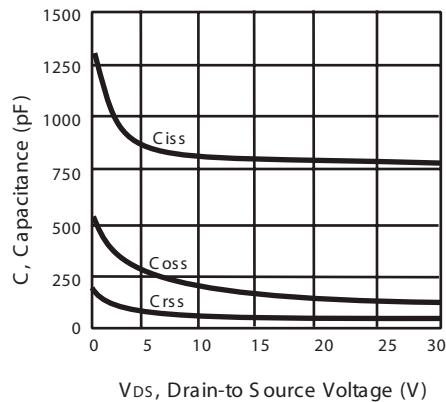


Figure 9. Capacitance

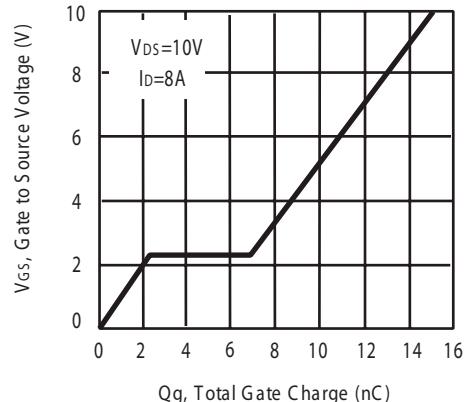


Figure 10. Gate Charge

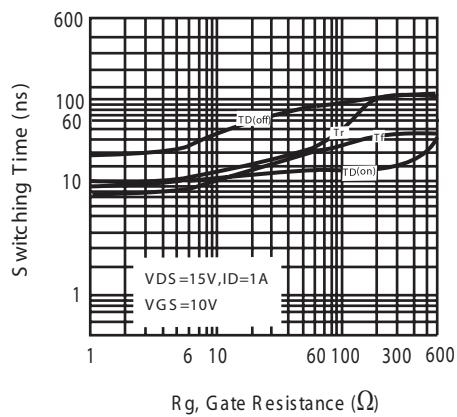


Figure 11. switching characteristics

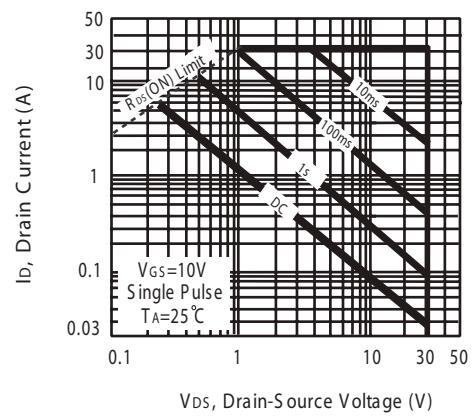


Figure 12. Maximum Safe Operating Area

# STM4800S

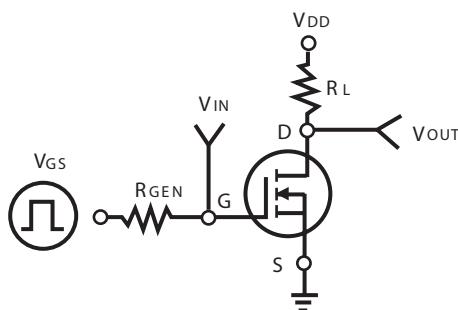


Figure 11. S switching Test Circuit

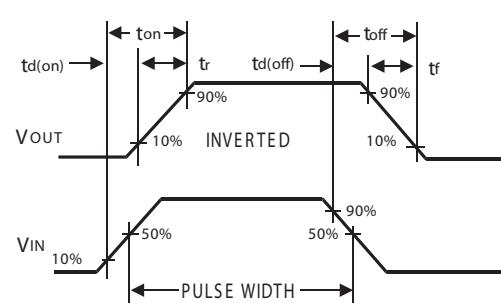


Figure 12. S switching Waveforms

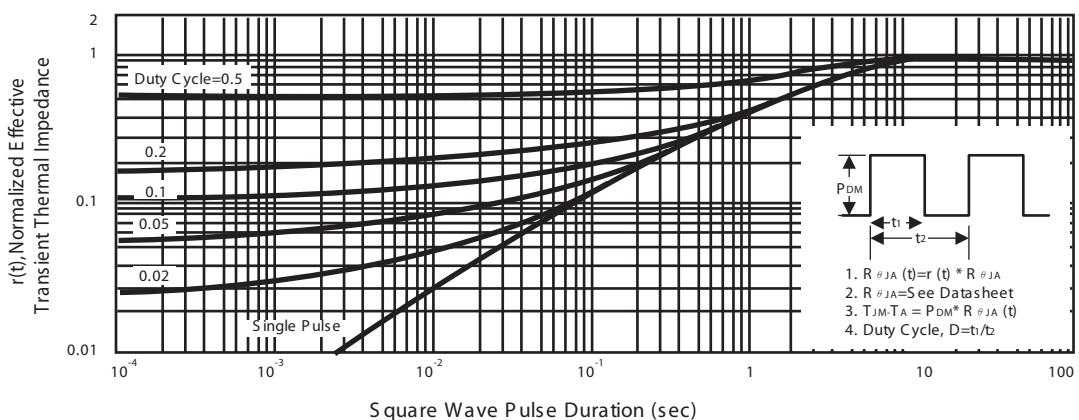
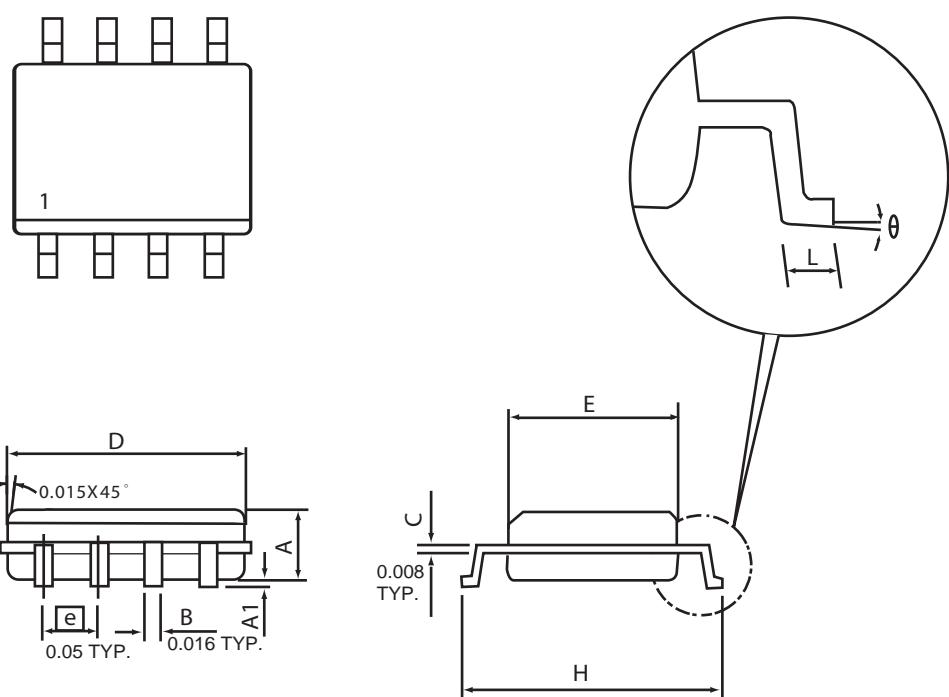


Figure 13. Normalized Thermal Transient Impedance Curve

# STM4800S

## PACKAGE OUTLINE DIMENSIONS

SO-8

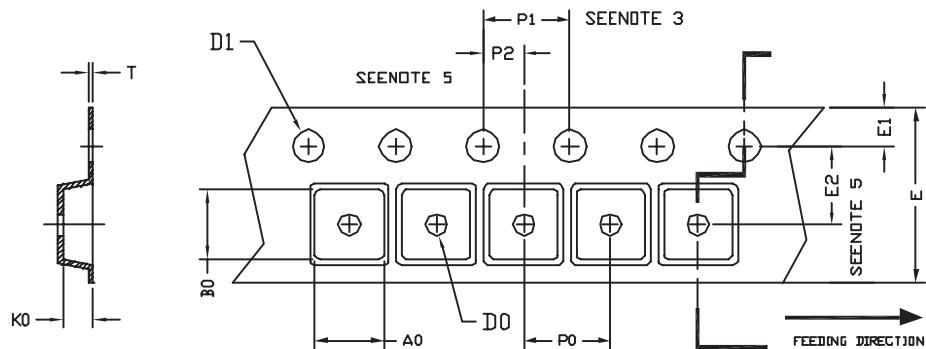


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

# STM4800S

## SO-8 Tape and Reel Data

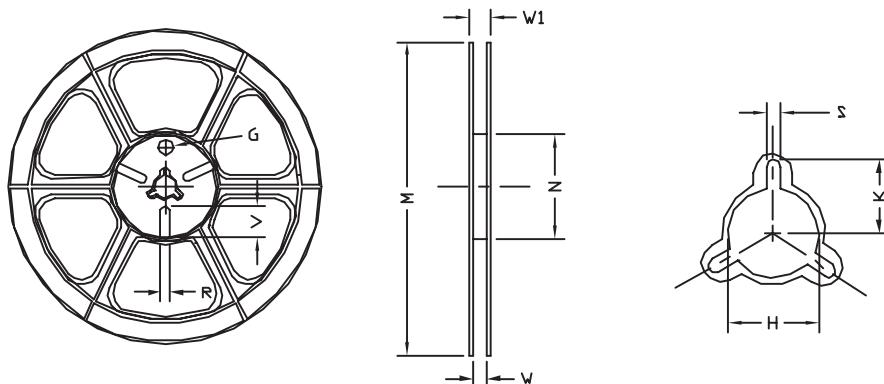
### SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.40	5.20	2.10	$\phi 1.5$ (MIN)	$\phi 1.5$ $+ 0.1$ $- 0.0$	12.0 $\pm 0.3$	1.75	$5.5$ $\pm 0.05$	8.0	4.0	$\pm 0.05$	0.3 $\pm 0.05$

### SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	$330$ $\pm 1$	$62$ $\pm 1.5$	$12.4$ $+ 0.2$	$16.8$ $- 0.4$	$\phi 12.75$ $+ 0.15$	---	$2.0$ $\pm 0.15$	---	---	---