

# High performance low voltage NPN transistor

#### **Features**

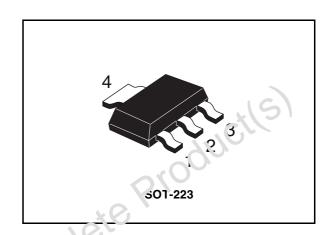
- Very low collector to emitter saturation voltage
- DC current gain, h<sub>FE</sub> > 100
- 3 A continuous collector current
- 40 V breakdown voltage V<sub>(BR)CER</sub>
- SOT-223 plastic package for surface mounting circuits in tape and reel packaging

### **Applications**

- Power management in portable equipment
- Voltage regulation in bias supply circuits
- Switching regulator in battery charger applications
- Heavy load driver

### **Description**

The device in manufactured in low voitage NPN planar technology by using a "Base Island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage.



Co(2,4)
BO
E O(3)

SC06965

Table 1. Device summary

Order code	Marking	Package	Packaging
STN690A	N690A	SOT-223	Tape and reel

Electrical ratings STN690A

# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)	40	V
V <sub>CER</sub>	Collector-emitter voltage ( $R_{BE} = 47 \Omega$ )	40	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	30	٧
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)	5	٧
I <sub>C</sub>	Collector current	3 (9	А
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	6	Α
P <sub>tot</sub>	Total dissipation at T <sub>amb</sub> = 25 °C	0.0	W
T <sub>stg</sub>	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	ů

Table 3. Thermal data

Symbo	Parameter	Value	Unit	
R <sub>thj-aml</sub>	Thermal resistance junction-an $5^{(1)}$	max	78	°C/W

<sup>1.</sup> Device mounted on PCB area of 1 cm<sup>2</sup>.

# 2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C \text{ unless otherwise specified})$ 

Table 4. Electrical characteristics

	Cumbal	Parameter Test conditions		Min.	Tvn	Mov	Unit	
Symbol		Parameter		nunuons	WIIII.	Тур.	Max.	Unit
	I <sub>CBO</sub>	Collector cut-off current	$V_{CB} = 30 \text{ V}$				10	μA
	020	$(I_E = 0)$	$V_{CB} = 30 \text{ V};$	T <sub>C</sub> = 100 °C			100	μΑ
	I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 4 V				10	μΑ
V	(BR)CEO (1)	Collector-emitter breakdown voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA		30	40	ازر	V
V	(BR)CER <sup>(1)</sup>	Collector-emitter breakdown voltage $(R_{BE} = 47 \Omega)$	I <sub>C</sub> = 10 mA	*6	40			V
\	√ <sub>(BR)CBO</sub>	Collector-base breakdown voltage (I <sub>E</sub> = 0)	I <sub>C</sub> = 100 μΑ	Vers	40			V
\	V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage (I <sub>C</sub> = 0)	: <u>-</u> = 100 μA		5			٧
		16	I <sub>C</sub> = 0.5 A	$I_B = 5 \text{ mA}$		0.08	0.15	V
			$I_C = 1.2 A$	$I_B = 20 \text{ mA}$		0.1	0.22	V
V	CE(sat) (1)	Colluctor emitter	$I_C = 2 A$	$I_B = 20 \text{ mA}$		0.175	0.35	V
		saturation voltage	$I_C = 3 A$	$I_B = 100 \text{ mA}$		0.2	0.4	V
	01		$I_C = 3 A$	$I_B = 100 \text{ mA}$				
			$T_C = 100  ^{\circ}C$			0.3		<b>V</b>
000	BE(sat) (1)	Base-emitter saturation voltage	I <sub>C</sub> = 1 A	I <sub>B</sub> = 10 mA		0.8	1	٧
V	/ <sub>BE(on)</sub> (1)	Base-emitter on voltage	I <sub>C</sub> = 1 A	V <sub>CE</sub> = 2 V		0.8	1	٧
			I <sub>C</sub> = 10 mA	V <sub>CE</sub> = 2 V	100	200	400	
			$I_C = 500 \text{ mA}$	$V_{CE} = 2 V$	100	200	400	
	h <sub>FE</sub> <sup>(1)</sup>	DC current gain	I <sub>C</sub> = 1 A	$V_{CE} = 2 V$	100			
			$I_C = 2 A$	$V_{CE} = 1 V$	100	160		
			$I_C = 3 A$	$V_{CE} = 1 V$	90	130		

Electrical characteristics STN690A

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
f <sub>t</sub>	Transition frequency	$I_C = 50 \text{ mA}$ $V_{CE} = 5 \text{ V}$ $f = 50 \text{ MHz}$		100		MHz
t <sub>d</sub>	Resistive load Delay time	I <sub>C</sub> = 3 A V <sub>CC</sub> = 20 V		50		ns
t <sub>r</sub>	Rise time	$I_C = 3 \text{ A}$ $V_{CC} = 20 \text{ V}$ $I_{B1} = -I_{B2} = 60 \text{ mA}$		120		ns
t <sub>s</sub> t <sub>f</sub>	Storage time Fall time	see Figure 8		465 80		ns ns

<sup>1.</sup> Pulse duration = 300 µs, duty cycle ≤1.5%

### 2.1 Electrical characteristics (curves)

Figure 2. DC current gain

ະurves) Figure 3. DC current yaim

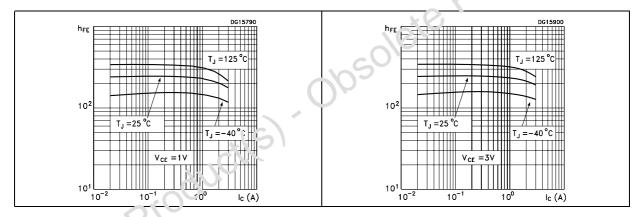


Figure 4. Collector-emitter saturation

Figure 5. Base-emitter saturation voltage

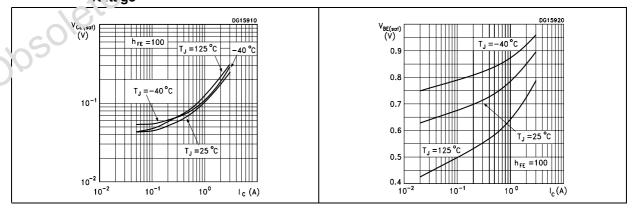
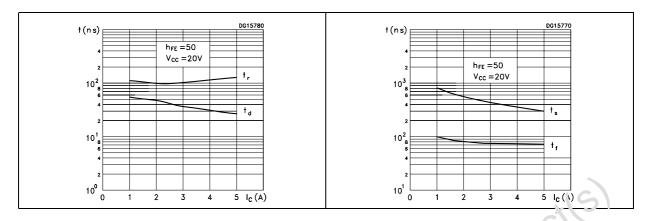


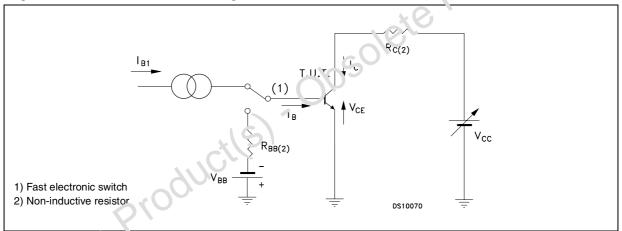
Figure 6. Switching time resistive load

Figure 7. Switching time resistive load



#### 2.2 Test circuit

Figure 8. Resistive load switching test circuit



# 3 Package mechanical data

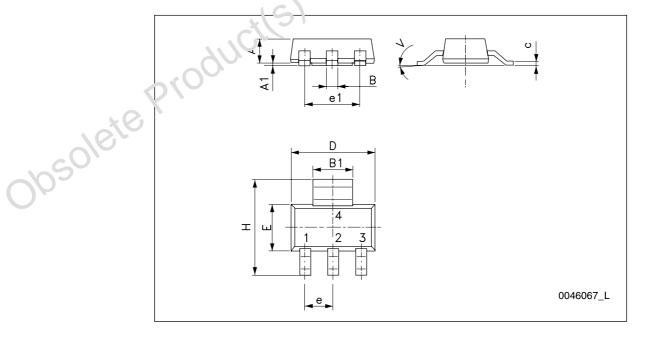
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#### SOT-223 mechanical data

DIM.	mm.					
DIWI.	min.	typ	max.			
Α			1.80			
A1	0.02		0.1			
В	0.60	0.70	0.85			
B1	2.90	3.00	3. '5			
С	0.24	0.26	0.35			
D	6.30	6.50	6.70			
е		2.30				
e1		4 60				
E	3.30	3.50	3.70			
Н	6.70	7.00	7.30			
٧			10 °			



Revision history STN690A

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes
20-Oct-2006	1	Initial release.
10-Feb-2009	2	Updated SOT-223 mechanical data.

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