

DESCRIPTION

The STP2305A is the P-Channel logic enhancement mode power field effect transistor is produced using high cell density. advanced trench technology to provide excellent $R_{DS(ON)}$.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , and low in-line power loss are needed in a very small outline surface mount package.

FEATURE

- ◆ **-16V/-3.5A, $R_{DS(ON)} = 63m\Omega @ V_{GS} = -10V$**
- ◆ **-16V/-3.0A, $R_{DS(ON)} = 80m\Omega @ V_{GS} = -4.5V$**
- ◆ **-16V/-2.2A, $R_{DS(ON)} = 90m\Omega @ V_{GS} = -2.5V$**
- ◆ Super high density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and Maximum DC current capability

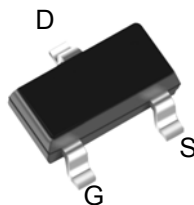
APPLICATIONS

- ◆ High Frequency Point-of-Load Synchronous
- ◆ Newworking DC-DC Power System
- ◆ Load Switch

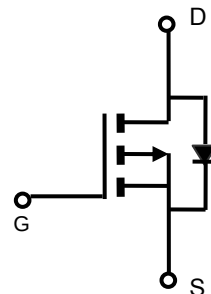


P-Channel Enhancement Mode MOSFET

PIN CONFIGURATION



TOP VIEW
SOT-23



P-Channel

PART NUMBER INFORMATION

STP2305AX-XX X

Lead Plating Code
Handling Code
Package Code

Lead Plating Code

G : Green product.
This product is Halogen free

Handling Code

TR : Tape&Reel

Package Code

S : SOT-23

ORDERING INFORMATION

Part Number	Package Code	Package	Shipping
STP2305AS-TRG	S	SOT-23	3000 / Tape&Reel

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Typical	Unit
V_{DS}	Drain-Source Voltage	-16	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current, $V_{GS}=10V^A$	$T_A=25^\circ\text{C}$ -3.5	A
I_{DM}	Pulsed Drain Current ^B	-10	A
P_D	Power Dissipation	$T_A=25^\circ\text{C}$ $T_A=70^\circ\text{C}$ 1.25 0.8	W
T_J	Operation Junction Temperature	-55/150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55/150	$^\circ\text{C}$

Note:

A.The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.

B.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$

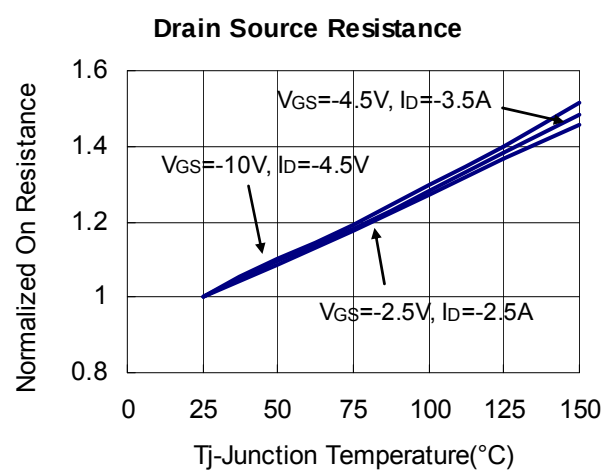
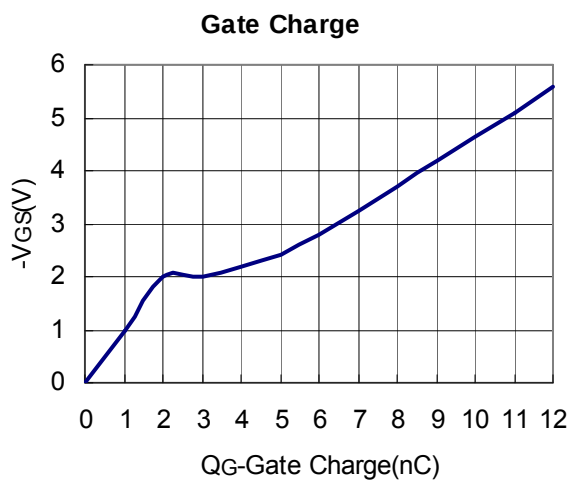
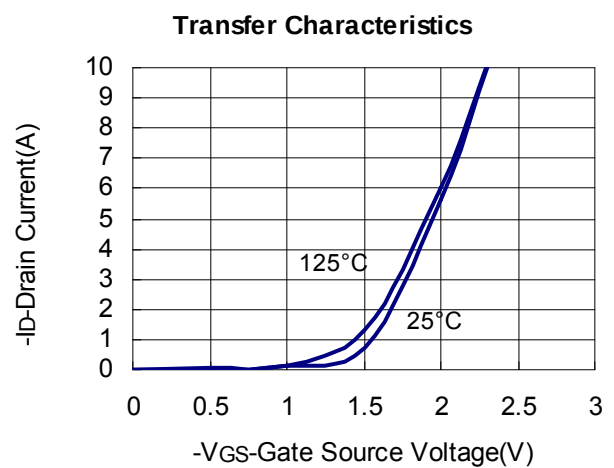
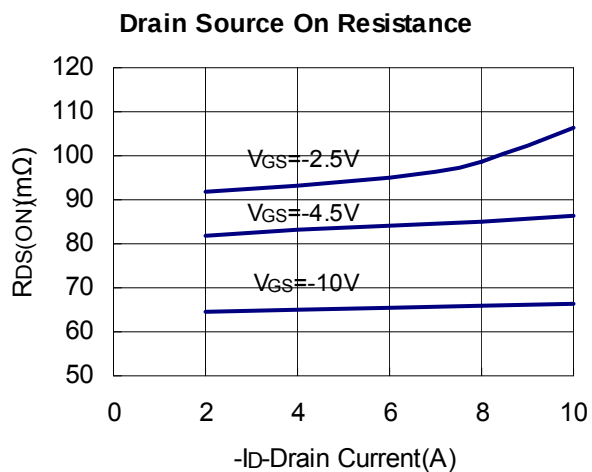
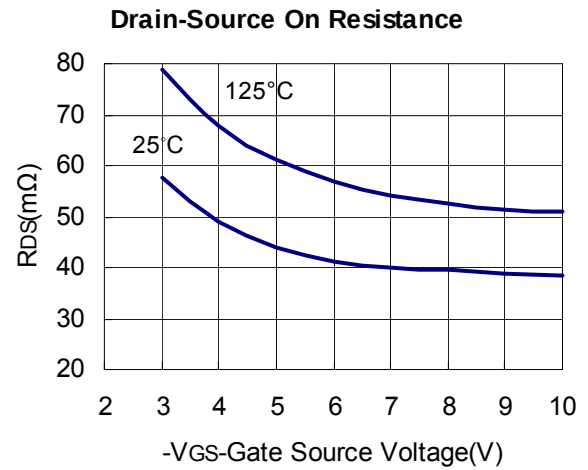
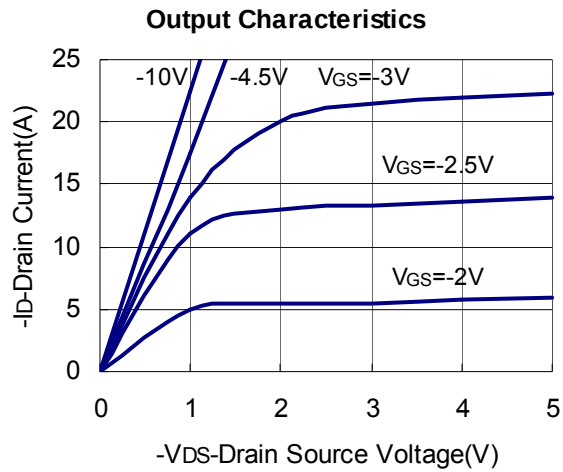
THERMAL DATA

Symbol	Parameter	Min	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient			120	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case			65	$^\circ\text{C/W}$

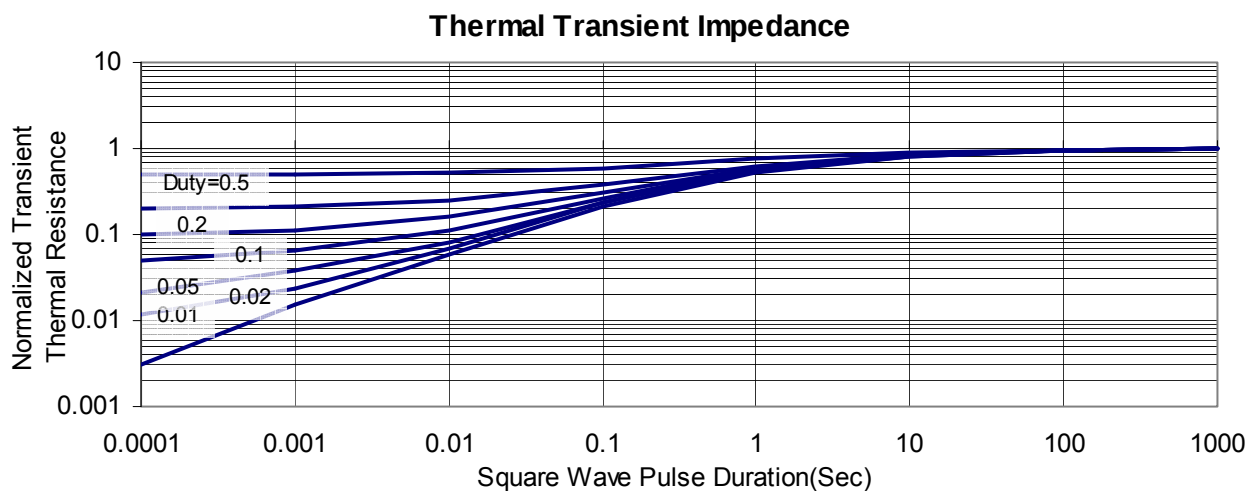
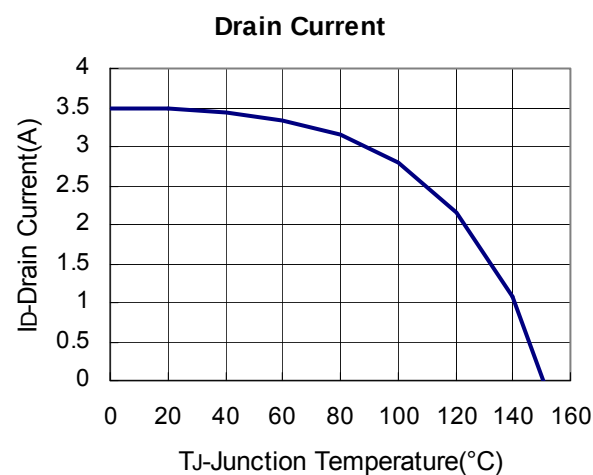
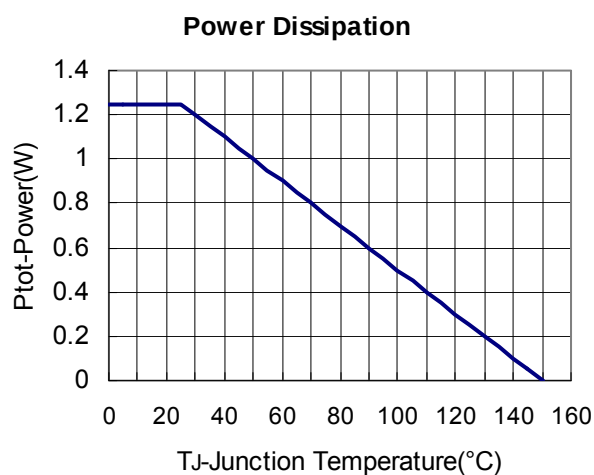
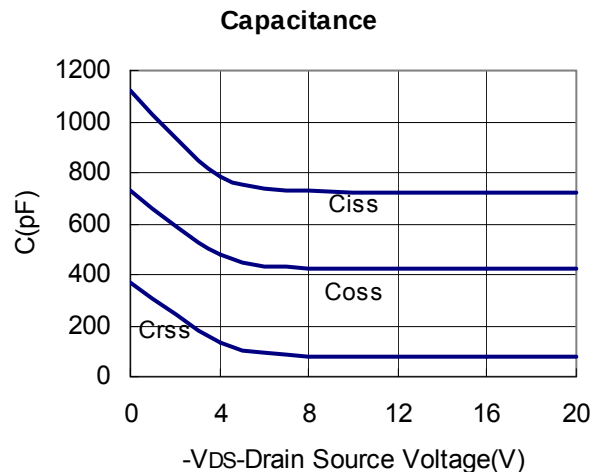
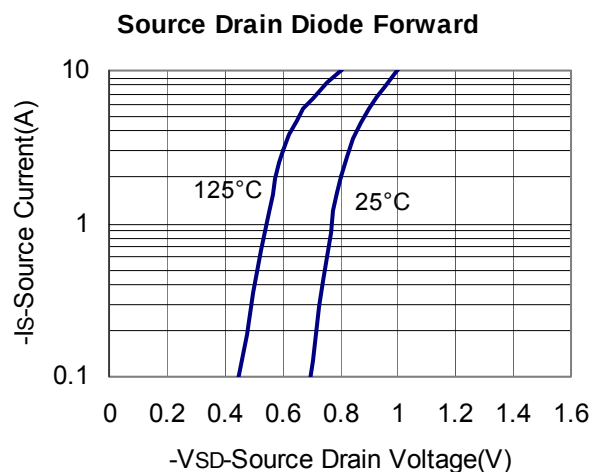
ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ Unless otherwise noted)

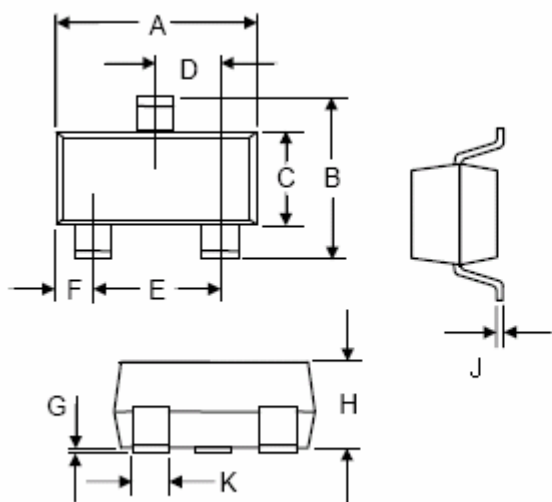
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Parameters						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D =-250μA	-16			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ,I _D =-250μA	-0.3		-1.0	V
I _{GSS}	Gate Leakage Current	V _{DS} =0V,V _{GS} =±12V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-16V,V _{GS} =0V			-1	μA
		V _{DS} =-16V,V _{GS} =0V T _J =55°C			-10	
R _{DS(ON)}	Drain-source On-Resistance	V _{GS} =-10V,I _D =-3.5A V _{GS} =-4.5V,I _D =-3.0A V _{GS} =-2.5V,I _D =-2.2A		63 80 90	85 105 135	mΩ
G _{fs}	Forward Transconductance	V _{DS} =-5V,I _D =-3.5A		10		S
Source-Drain Diode						
V _{SD}	Diode Forward Voltage	I _S =-1.6A,V _{GS} =0V		-0.8	-1.2	V
Dynamic Parameters						
Q _g	Total Gate Charge	V _{DS} =-15V V _{GS} =-10V I _D =-4.0A		8		nC
Q _{gs}	Gate-Source Charge			15		
Q _{gd}	Gate-Drain Charge			2.2		
C _{iss}	Input Capacitance	V _{DS} =-15V V _{GS} =0V f=1MHz		720		pF
C _{oss}	Output Capacitance			420		
C _{rss}	Reverse Transfer Capacitance			80		
t _{d(on)}	Turn-On Time	V _{DD} =-15V R _L =15Ω I _D =-1A V _{GEN} =-10V R _G =6Ω		14	20	nS
t _r				6	10	
t _{d(off)}	Turn-Off Time			40	48	
t _f				7	15	

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



■ SOT-23 PACKAGE DIMENSIONS


Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A	0.110	0.120	2.80	3.04
B	0.83	0.098	2.10	2.64
C	0.47	0.055	1.20	1.40
D	0.35	0.041	0.89	1.03
E	0.70	0.081	1.78	2.05
F	0.18	0.024	0.45	0.60
G	0.001	0.0039	0.013	0.100
H	0.035	0.044	0.89	1.12
J	0.003	0.007	0.085	0.18
K	0.015	0.02	0.37	0.51