

STPS1L40-Y

Automotive low drop power Schottky rectifier

Features

- Very small conduction losses
- Negligible switching losses
- Low forward voltage drop
- Surface mount miniature packages
- Avalanche capability specified
- AEC-Q101 qualified
- ECOPACK®2 compliant components

Description

Single chip Schottky rectifiers suited to switched mode power supplies and high frequency DC to DC converters.

Packaged in SMA and SMB this device is especially intended for surface mounting and used in low voltage, high frequency inverters, free wheeling and polarity protection in automotive applications.

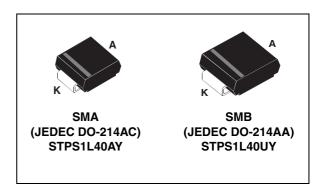


Table 1. Device summary

Symbol	Value
I _{F(AV)}	1 A
V_{RRM}	40 V
T _j (max)	150 °C
V _F (max)	0.42 V

Characteristics STPS1L40-Y

Characteristics 1

Table 2. **Absolute ratings (limiting values)**

Symbol	Paramete	Value	Unit	
V_{RRM}	Repetitive peak reverse voltage		40	V
I _{F(RMS)}	Forward rms current		8	Α
I _{F(AV)}	Average forward current $T_L = 130 ^{\circ}\text{C} \delta = 0.5$		1	Α
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms sinusoidal	60	Α
I _{RRM}	Repetitive peak reverse current $t_p = 2 \mu s$ F = 1 kHz square		1	Α
I _{RSM}	Non repetitive peak reverse current $t_p = 100 \mu s$ square		1	Α
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25 °C$		900	W
T _{stg}	Storage temperature range	- 65 to + 150	°C	
T _j	Operating junction temperature range ⁽¹⁾		-40 to + 150	°C
dV/dt	Critical rate of rise of reverse voltage		10000	V/µs

^{1.} $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance

Symbol	Parameter		Value	Unit
В	Junction to lead	1A	30	°C/W
R _{th(j-l)}	SME	/IB	25	C/VV

Table 4. Static electrical characteristics

Symbol	Parameter	Tests conditions		Min.	Тур.	Max.	Unit
ı_ (1)	Reverse leakage current	T _j = 25 °C	$V_R = V_{RRM}$	-	-	35	μΑ
'R`		T _j = 125 °C		-	6	10	mA
	V _F ⁽¹⁾ Forward voltage drop	T _j = 25 °C	I _F = 1 A	-	-	0.5	V
V (1)		T _j = 125 °C		-	0.37	0.42	
VF \		T _j = 25 °C		-	-	0.63	V
		T _j = 125 °C	IF - Z A	-	0.5	0.61	

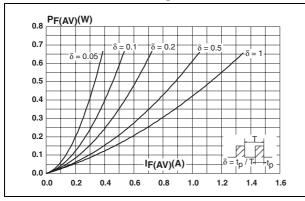
^{1.} Pulse test: $t_p = 380 \mu s$, $\delta < 2\%$

To evaluate the conduction losses use the following equation: P = 0.23 x $I_{F(AV)}$ + 0.19 $I_{F}^{2}_{(RMS)}$

$$P = 0.23 \times I_{F(AV)} + 0.19 I_{F^2(RMS)}$$

STPS1L40-Y Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus versus average forward current ambient temperature (SMA, δ = 0.5)



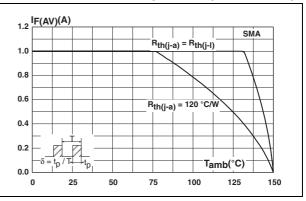
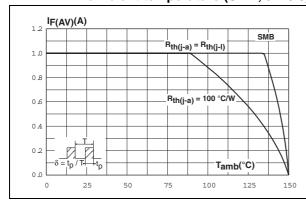


Figure 3. Average forward current versus ambient temperature (SMB, δ = 0.5)

Figure 4. Normalized avalanche power derating versus pulse duration



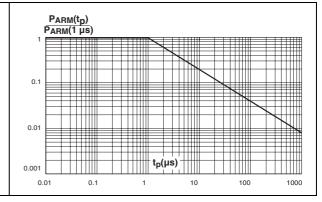
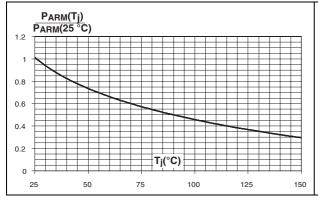
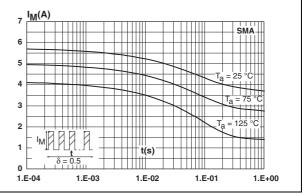


Figure 5. Normalized avalanche power derating versus junction temperature

Figure 6. Non repetitive surge peak forward current versus overload duration (maximum values, SMA)

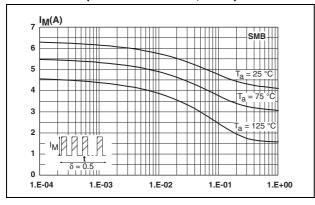




Characteristics STPS1L40-Y

Figure 7. Non repetitive surge peak forward current versus overload duration (maximum values, SMB)

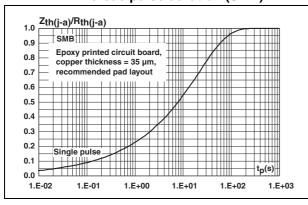
Figure 8. Relative variation of thermal impedance junction to ambient versus pulse duration (SMA)



 $Z_{th(j-a)}/R_{th(j-a)}$ 1.0 SMA 0.9 Epoxy printed circuit board, 0.8 copper thickness = 35 µm, recommended pad layout 0.7 0.6 0.5 0.4 0.3 0.2 0.1 ·t_p(s) 0.0 1.E-02

Figure 9. Relative variation of thermal impedance junction to ambient versus pulse duration (SMB)

Figure 10. Reverse leakage current versus reverse voltage applied (typical values)



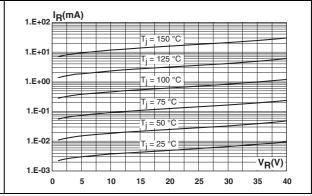
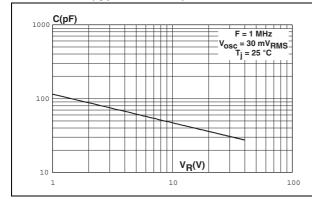
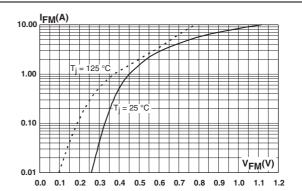


Figure 11. Junction capacitance versus reverse voltage applied (typical values)

Figure 12. Forward voltage drop versus forward current (typical values, high level)





STPS1L40-Y Characteristics

Figure 13. Forward voltage drop versus forward current (typical values, low level)

3.0 IFM(A)

2.5
2.0
1.5
1.0
0.5
0.0
0.0
0.1
0.2
0.3
0.4
0.5
0.6
0.7

Figure 14. Thermal resistance junction to ambient versus copper surface under each lead (SMA)

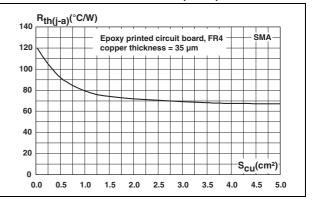
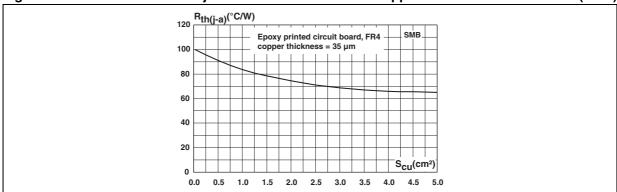


Figure 15. Thermal resistance junction to ambient versus copper surface under each lead (SMB)



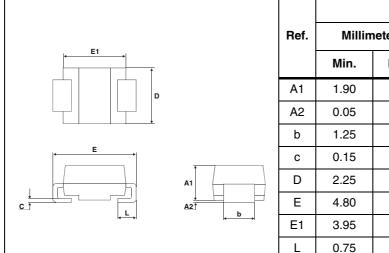
Package information STPS1L40-Y

2 Package information

- Epoxy meets UL94, V0
- Cathode band (SMA, SMB)

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 5. SMA dimensions



	Dimensions				
Ref.	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
A1	1.90	2.45	0.075	0.094	
A2	0.05	0.20	0.002	0.008	
b	1.25	1.65	0.049	0.065	
С	0.15	0.40	0.006	0.016	
D	2.25	2.90	0.089	0.114	
Е	4.80	5.35	0.189	0.211	
E1	3.95	4.60	0.156	0.181	
L	0.75	1.50	0.030	0.059	

Figure 16. Footprint, dimensions in mm (inches)

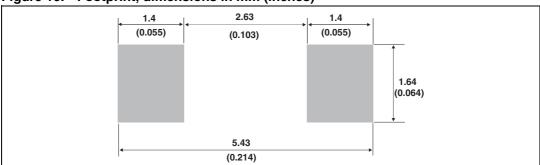
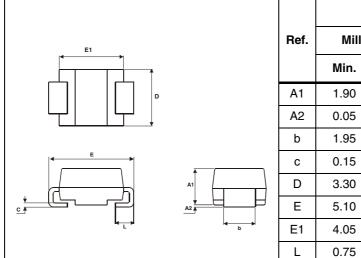
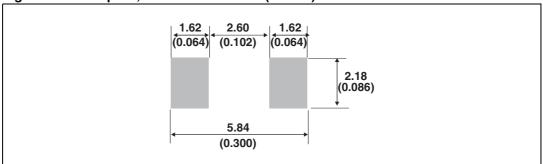


Table 6. SMB dimensions



	Dimensions				
Ref.	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
A1	1.90	2.45	0.075	0.096	
A2	0.05	0.20	0.002	0.008	
b	1.95	2.20	0.077	0.087	
С	0.15	0.40	0.006	0.016	
D	3.30	3.95	0.130	0.156	
Е	5.10	5.60	0.201	0.220	
E1	4.05	4.60	0.159	0.181	
L	0.75	1.50	0.030	0.059	

Figure 17. Footprint, dimensions in mm (inches)



Ordering information STPS1L40-Y

3 Ordering information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS1L40AY	GB4Y	SMA	0.068 g	5000	Tape and reel
STPS1L40UY	GC4Y	SMB	0.107 g	2500	Tape and reel

4 Revision history

Table 8. Document revision history

Date	Revision	Changes
21-Oct-2011	1	First issue.

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