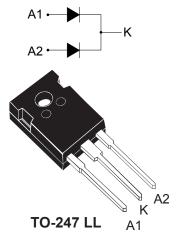


1200 V, 40 A power Schottky silicon carbide diode









Product summary				
I _{F(AV)}	2 x 20 A			
V _{RRM}	1200 V			
T _j (max.)	175 °C			
V _F (typ.)	1.35 V			

Features

- None or negligible reverse recovery
- · Switching behavior independent of temperature
- Robust high voltage periphery
- Operating T_i from -40 °C to 175 °C
- ECOPACK2 compliant component

Applications

- Solar inverter
- Boost PFC
- Air conditioning equipment
- UPS power supply
- Telecom / Server power equipment
- HEV/EV OBC (On board battery chargers)
- EV Charging station

Description

The SiC diode, available in TO-247 LL, is an ultrahigh performance power Schottky rectifier. It is manufactured using a silicon carbide substrate. The wide band-gap material allows the design of a low V_F Schottky diode structure with a 1200 V rating. Due to the Schottky construction, no recovery is shown at turn-off and ringing patterns are negligible. The minimal capacitive turn-off behavior is independent of temperature.

Especially suited for use in PFC and secondary side applications, this ST SiC diode will boost the performance in hard switching conditions. This rectifier will enhance the performance of the targeted application. Its high forward surge capability ensures a good robustness during transient phases.



1 Characteristics

Table 1. Absolute ratings (limiting values at 25 °C, unless otherwise specified)

Symbol		Value	Unit			
V_{RRM}	Repetitive peak reverse voltage (T _j = -40	1200	V			
I _{F(RMS)}	Forward rms current			38	Α	
		T _C = 150 °C, δ = 1		20/40		
I _{F(AV)} Average forward current	Average forward current	$T_c = 135 ^{\circ}C, \delta = 1$	Per diode/perdevice	27/54	A	
		T _C = 25 °C, δ = 1		38/76		
I _{FRM}	Repetitive peak forward current	T _c = 150 °C, T _j = 175 °C	79	Α		
		$t_p = 10 \text{ ms sinusoidal}$	T _c = 25 °C	140		
I_{FSM}	Surge non repetitive forward current	t _p = 10 ms smusoidai	T _c = 150 °C	120	Α	
		t _p = 10 μs square	T _C = 25 °C	700		
T _{stg}	Storage temperature range	-65 to +175	°C			
Tj	Operating junction temperature range			-40 to +175	°C	

Table 2. Thermal resistance parameters

	Symbol	Parameter -			Value	
Symbol		rarameter		Тур.	Max.	Unit
Г	P.,	lunction to coop	Per diode	0.40	0.55	°CAM
	R _{th(j-c)}	Junction to case	Per device	0.20	0.28	°C/W

For more information, please refer to the following application note:

AN5088: Rectifiers thermal management, handling and mounting recommendations

Table 3. Static electrical characteristics

Symbol	Parameter	Test co	nditions	Min.	Тур.	Max.	Unit
L (1)		T _j = 25 °C	V V	-	10	120	
I _R ⁽¹⁾	Reverse leakage current	T _j = 150 °C	$V_R = V_{RRM}$	-	60	800	μA
V _F ⁽²⁾ Forward voltage drop		T _j = 25 °C	I _E = 20 A	-	1.35	1.50	V
VF (=/	Forward voltage drop	T _j = 150 °C	IF - 20 A	-	1.75	2.25	V

- 1. Pulse test: $t_p = 10 \text{ ms}, \ \delta < 2\%$
- 2. Pulse test: t_p = 500 μ s, δ < 2%

To evaluate the conduction losses, use the following equation:

 $P = 1.07 \times I_{F(AV)} + 0.059 \times I_{F}^{2}_{(RMS)}$

For more information, please refer to the following application notes related to the power losses:

- AN604: Calculation of conduction losses in a power rectifier
- AN4021: Calculation of reverse losses on a power diode

DS12030 - Rev 2 page 2/10



Table 4. Dynamic electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Q _{Cj} (1)	Total capacitive charge	V _R = 800 V	-	129	-	nC
C.	Total conscitones	V _R = 0 V, T _c = 25 °C, F = 1 MHz	-	1650	-	nΕ
C _j	Total capacitance	V _R = 800 V, T _c = 25 °C, F = 1 MHz	-	110	-	pF

1. Most accurate value for the capacitive charge: $Q_{cj}(V_R) = \int\limits_0^{V_R} C_j(V) dV$

DS12030 - Rev 2 page 3/10



1.1 Characteristics (curves)

Figure 1. Forward voltage drop versus forward current (typical values, per diode)

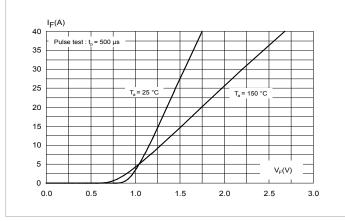


Figure 2. Reverse leakage current versus reverse voltage applied (typical values, per diode)

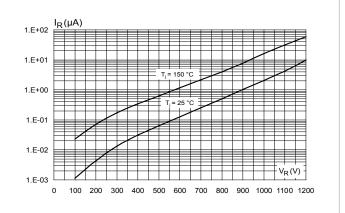


Figure 3. Peak forward current versus case temperature (per diode)

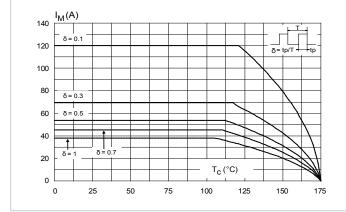


Figure 4. Junction capacitance versus reverse voltage applied (typical values, per diode)

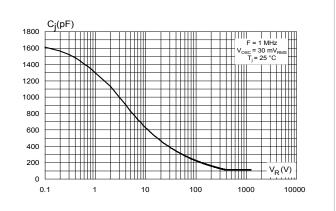


Figure 5. Relative variation of thermal impedance junction to case versus pulse duration

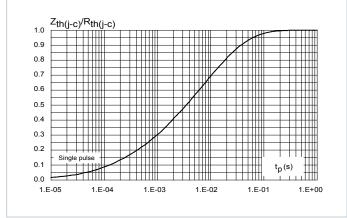
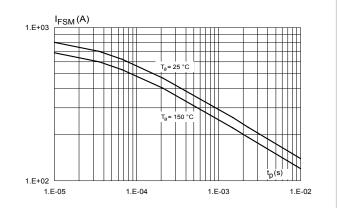
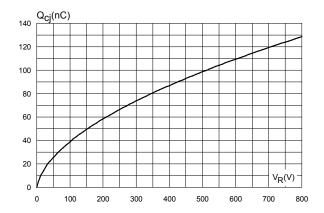


Figure 6. Non-repetitive peak surge forward current versus pulse duration (sinusoidal waveform, per diode)



DS12030 - Rev 2 page 4/10

Figure 7. Total capacitive charges versus reverse voltage applied (typical values, per diode)



DS12030 - Rev 2 page 5/10



Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

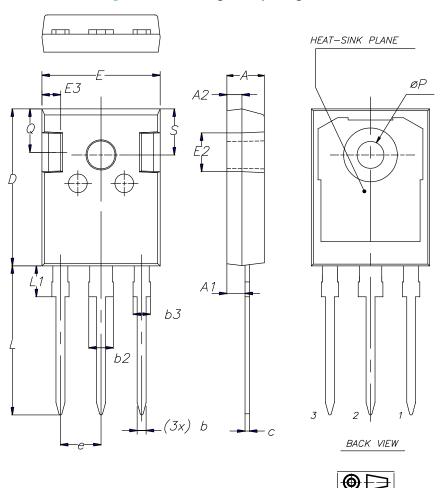
2.1 TO-247 long leads package information

Epoxy meets UL94, V0

Cooling method: by conduction (C)
Recommended torque value: 0.8 N·m

Maximum torque value: 1.0 N·m

Figure 8. TO-247 long leads package outline



DS12030 - Rev 2 page 6/10



Table 5. TO-247 long leads package mechanical data

Dim.	mm.			Inches		
Dilli.	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	4.90	-	5.15	0.192	-	0.203
A1	2.25	-	2.55	0.088	-	0.101
A2	1.85	-	2.10	0.072	-	0.083
b	1.07	-	1.32	0.042	-	0.052
b2	2.87	-	3.38	0.112	-	0.134
b3	1.90	-	2.38	0.074	-	0.094
С	0.55	-	0.67	0.021	-	0.027
D	20.82	-	21.10	0.819	-	0.831
E	15.70	-	16.02	0.618	-	0.631
E2	4.90	-	5.10	0.192	-	0.201
E3	2.40	-	2.60	0.094	-	0.103
е	5.34	-	5.54	0.210	-	0.219
L	19.80	-	20.30	0.779	-	0.800
L1	4.16	-	4.47	0.163	-	0.176
Р	3.50	-	3.70	0.137	-	0.146
Q	5.49	-	6.00	0.216	-	0.237
S	6.04	-	6.29	0.237	-	0.248

DS12030 - Rev 2 page 7/10



3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode	
STPSC40H12CWL	STPSC40H12CWL	TO247 3L long leads	6.09 g	30	Tube	

DS12030 - Rev 2 page 8/10



Revision history

Table 7. Document revision history

Date	Revision	Changes
28-Feb-2017	1	Initial release.
13-Sep-2023	2	Updated TO247 3L long leads package information. Minor text changes.

DS12030 - Rev 2 page 9/10



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DS12030 - Rev 2 page 10/10