



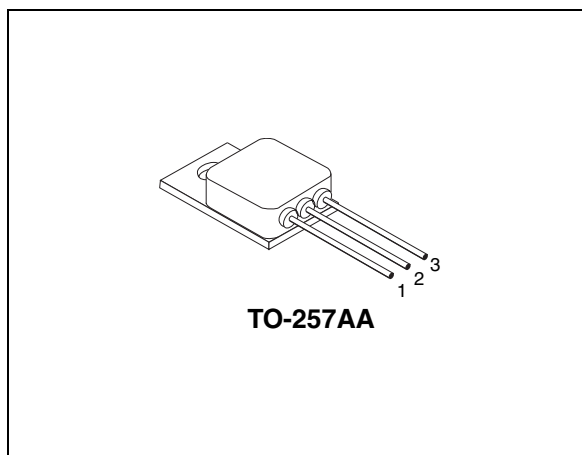
# STRH10N25ESY3

N-channel 250V - 0.95Ω - TO-257AA  
Rad-hard low gate charge STripFET™ Power MOSFET

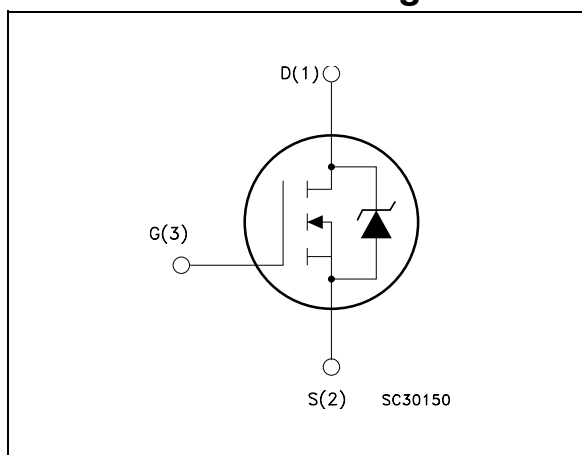
## General features

Type	V <sub>DSS</sub>
STRH10N25ESY3	250V

- Low R<sub>DS(on)</sub>
- Fast switching
- Single event effect (SEE) hardened
- Low total gate charge
- Light weight
- 100% avalanche tested
- Application oriented characterization
- Hermetically sealed
- Heavy ion SOA
- 100kRad TID
- SEL & SEGR with 34Mev/cm<sup>2</sup>/mg LET ions



## Internal schematic diagram



## Description

This Power MOSFET series realized with STMicroelectronics unique STripFET process has specifically been designed to sustain high TID and provide immunity to heavy ion effects. It is therefore suitable as power switch in mainly high-efficiency DC-DC converters. It is also intended for any application with low gate charge drive requirements.

## Applications

- Satellite
- High reliability applications

## Order codes

Part number	Marking	Package	Packaging
STRH10N25ESY1 <sup>(1)</sup>	RH10N25ESY1	TO-257AA	Tube
STRH10N25ESY3 <sup>(2)</sup>	RH10N25ESY3	TO-257AA	Tube

1. Mil temp range

2. Space flights parts (full ESA flow screening)

# Contents

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# 1 Electrical ratings

**Table 1. Absolute maximum ratings (pre-irradiation)**

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-source voltage ( $V_{GS} = 0$ )	250	V
$V_{GS}$	Gate-source voltage	$\pm 16$	V
$I_D^{(1)}$	Drain current (continuous) at $T_C = 25^\circ\text{C}$	10	A
$I_D^{(1)}$	Drain current (continuous) at $T_C = 100^\circ\text{C}$	6.5	A
$I_{DM}^{(2)}$	Drain current (pulsed)	40	A
$P_{TOT}^{(1)}$	Total dissipation at $T_C = 25^\circ\text{C}$	85	W
$dv/dt^{(3)}$	Peak diode recovery voltage slope	1.9	V/ns
$T_{stg}$	Storage temperature	-55 to 150	$^\circ\text{C}$
$T_j$	Max. operating junction temperature	150	$^\circ\text{C}$

1. Rated according to the  $R_{thj-case}$
2. Pulse width limited by safe operating area
3.  $I_{SD} \leq 12\text{A}$ ,  $di/dt \leq 300\text{A}/\mu\text{s}$ ,  $V_{DD} = 80\%V_{(BR)DSS}$

**Table 2. Thermal data**

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case	1.47	$^\circ\text{C}/\text{W}$
$R_{thc-s}$	Case-to-sink	0.2	$^\circ\text{C}/\text{W}$
$R_{thj-amb}$	Thermal resistance junction -amb	62.5	$^\circ\text{C}/\text{W}$

**Table 3. Avalanche characteristics**

Symbol	Parameter	Value	Unit
$I_{AR}$	Avalanche current, repetitive or not-repetitive (pulse width limited by $T_j \text{ Max}$ )	10	A
$E_{AS}$	Single pulse avalanche energy (starting $T_j = 25^\circ\text{C}$ , $I_d = I_{AR}$ , $V_{DD} = 50\text{V}$ )	60	mJ
$E_{AR}$	Repetitive avalanche	8.5	mJ

## 2 Electrical characteristics

( $T_{CASE} = 25^{\circ}C$  unless otherwise specified)

### 2.1 Pre-irradiation

**Table 4. On/off states**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$I_{DSS}$	Zero gate voltage drain current ( $V_{GS} = 0$ )	80% $BV_{DSS}$			10	$\mu A$
$I_{GSS}$	Gate body leakage current ( $V_{DS} = 0$ )	$V_{GS} = \pm 16V$			$\pm 100$	nA
$BV_{DSS}$	Drain-to-source breakdown voltage	$I_D = 1mA, V_{GS} = 0V$	250			V
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 1mA$	2		4.5	V
$R_{DS(on)}$	Static drain-source on resistance	$V_{GS} = 12V, I_D = 6A$		0.95	1.1	$\Omega$

**Table 5. Dynamic**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$C_{iss}$	Input capacitance	$V_{DS} = 0V, f=1MHz,$ $V_{GS}=12V$		890		pF
$C_{oss}$	Output capacitance			83		pF
$C_{rss}$	Reverse transfer capacitance			10		pF
$Q_g$	Total gate charge	$V_{DD} = 200V, I_D = 6A,$ $V_{GS}=12V$		23	32	nC
$Q_{gs}$	Gate-to-source charge			5	7	nC
$Q_{gd}$	Gate-to-drain ("Miller") charge			7	10	nC
$R_G$	Gate input resistance	f=1MHz Gate DC Bias=0 Test signal level=20mV open drain		3.2	6	$\Omega$

**Table 6. Switching times**

Symbol	Parameter	Test conditions	Min.	Typ.	Max	Unit
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 125V, I_D = 10A,$ $R_G = 4.7\Omega, V_{GS} = 12V$		14		ns
$t_r$	Rise time			19		ns
$t_{d(off)}$	Turn-off-delay time			27		ns
$t_f$	Fall time			9		ns

**Table 7. Source drain diode**

Symbol	Parameter	Test conditions	Min.	Typ.	Max	Unit
$I_{SD}$	Source-drain current				10	A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				40	A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD} = 10A, V_{GS} = 0$			1.5	V
$t_{rr}$	Reverse recovery time	$I_{SD} = 12A, di/dt = 100A/\mu s$ $V_{DD} = 62V, T_j = 25^\circ C$		424		ns
$Q_{rr}$	Reverse recovery charge			3.7		$\mu C$
$I_{RRM}$	Reverse recovery current			18		A
$t_{rr}$	Reverse recovery time	$I_{SD} = 12A, di/dt = 100A/\mu s$ $V_{DD} = 62V, T_j = 150^\circ C$		498		ns
$Q_{rr}$	Reverse recovery charge			4.8		$\mu C$
$I_{RRM}$	Reverse recovery current			19.5		A

1. Pulse width limited by safe operating area

2. Pulsed: pulse duration = 300 $\mu s$ , duty cycle 1.5%

## 2.2 Radiation characteristics

The ST Rad-Hard Power MOSFETs are tested to verify the radiation capability. The technology is extremely resistant to assurance well functioning of the device inside the radiation environments. Every manufacturing lot is tested for total ionizing dose.

(@ $T_j=25^\circ C$  up to 100Krad<sup>(a)</sup>)

**Table 8. On/off states**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$I_{DSS}$	Zero gate voltage drain current ( $V_{GS} = 0$ )	80% $BV_{DSS}$			10	$\mu A$
$I_{GSS}$	Gate body leakage current ( $V_{DS} = 0$ )	$V_{GS} = \pm 16V$			$\pm 100$	nA
$BV_{DSS}$	Drain-to-source breakdown voltage	$I_D = 1mA, V_{GS} = 0V$	250			V
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 1mA$	2		4.5	V
$R_{DS(on)}$	Static drain-source on resistance	$V_{GS} = 12V, I_D = 6A$		0.95	1.1	$\Omega$

a. According to ESCC 22900 specification, Co60 gamma rays, dose rags:0.01rad/sec.

**Table 9. Single event effect, SOA<sup>(1)</sup>**

Ion	Let (Mev/(mg/cm2))	Energy (MeV)	Range (μm)	V <sub>DS</sub> (V) @V <sub>GS</sub> 0V
Kr	34	316	43	250
Xe	55.9	459	43	244

1. Rad-Hard Power MOSFETs have been characterized in heavy ion environment for single event effect (SEE). Single event effect characterization is illustrate

**Table 10. Source drain diode**

Symbol	Parameter	Test conditions	Min.	Typ.	Max	Unit
I <sub>SD</sub>	Source-drain current				10	A
I <sub>SDM</sub> <sup>(1)</sup>	Source-drain current (pulsed)				40	A
V <sub>SD</sub> <sup>(2)</sup>	Forward on voltage	I <sub>SD</sub> = 10A, V <sub>GS</sub> = 0			1.5	V
t <sub>rr</sub>	Reverse recovery time	I <sub>SD</sub> = 12A, di/dt = 100A/μs V <sub>DD</sub> = 62V, T <sub>j</sub> = 25°C		424		ns
Q <sub>rr</sub>	Reverse recovery charge			3.7		μC
I <sub>RRM</sub>	Reverse recovery current			18		A
t <sub>rr</sub>	Reverse recovery time	I <sub>SD</sub> = 12A, di/dt = 100A/μs V <sub>DD</sub> = 62V, T <sub>j</sub> = 150°C		498		ns
Q <sub>rr</sub>	Reverse recovery charge			4.8		μC
I <sub>RRM</sub>	Reverse recovery current			19.5		A

1. Pulse width limited by safe operating area
2. Pulsed: pulse duration = 300μs, duty cycle 1.5%

### 2.3 Electrical characteristics (curves)

Figure 1. Safe operating area

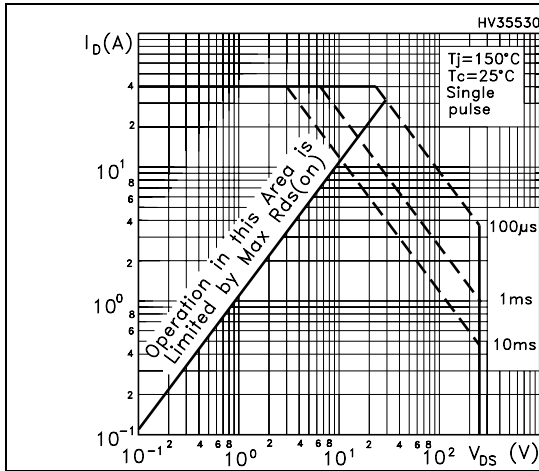


Figure 2. Thermal impedance

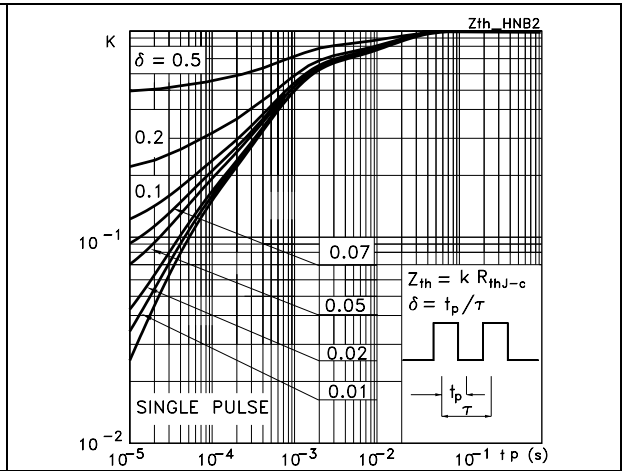


Figure 3. Output characteristics

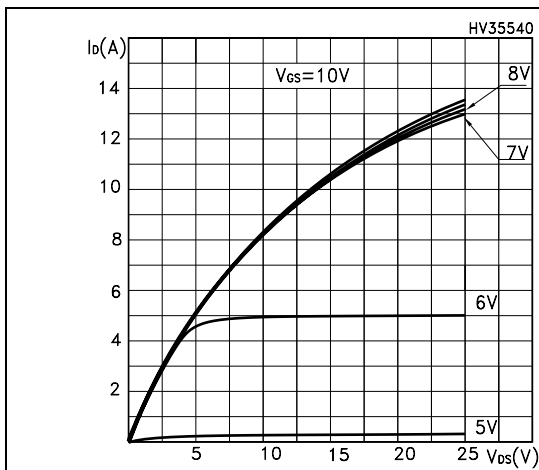


Figure 4. Transfer characteristics

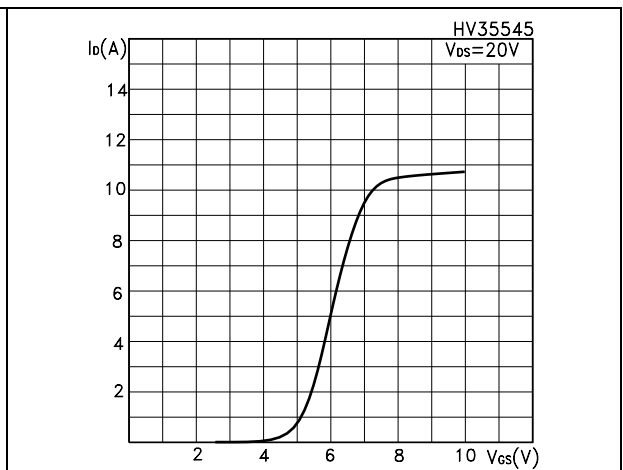


Figure 5. Gate charge vs gate-source voltage

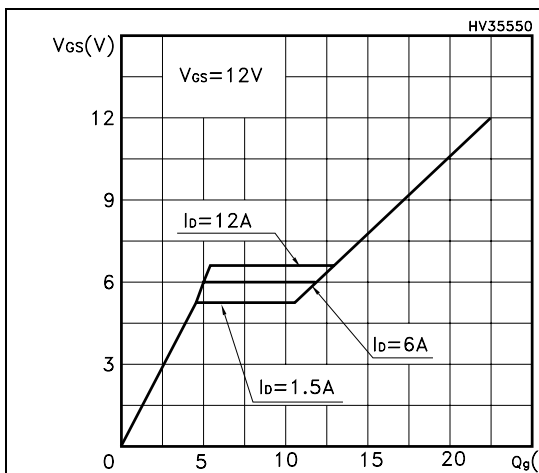


Figure 6. Capacitance variations

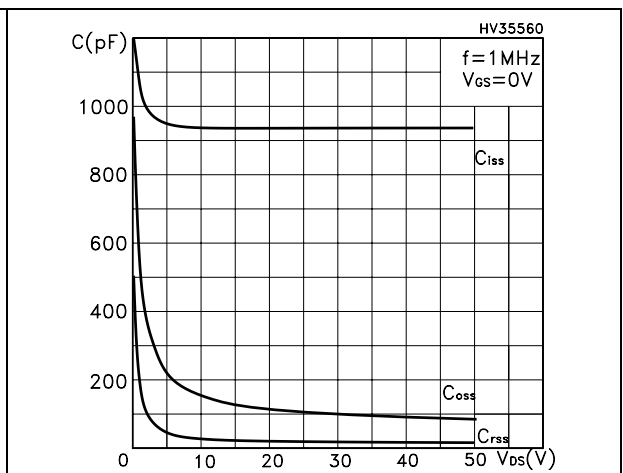


Figure 7. Normalized  $BV_{DSS}$  vs temperature      Figure 8. Static drain-source on resistance

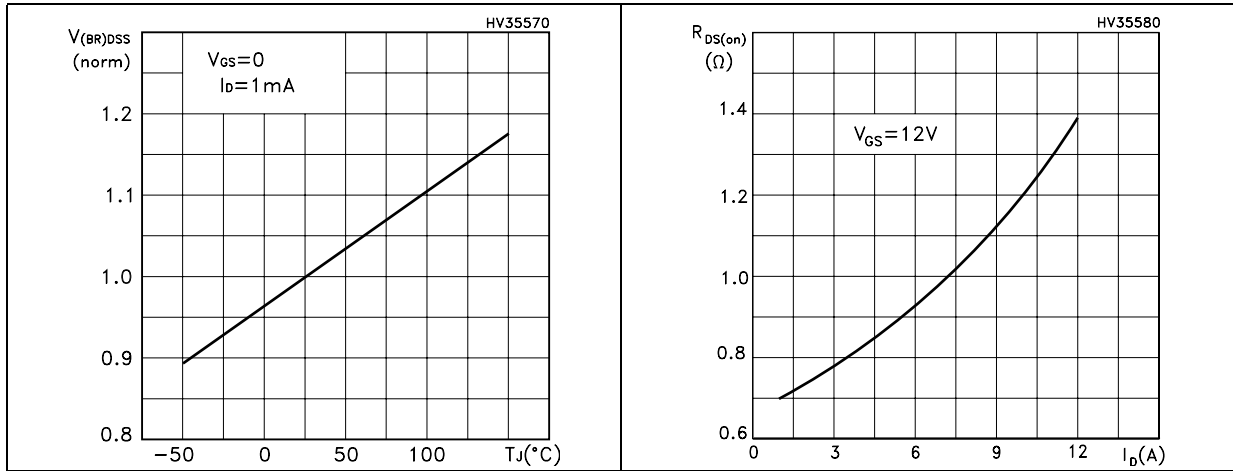


Figure 9. Normalized gate threshold voltage vs temperature      Figure 10. Normalized on resistance vs temperature

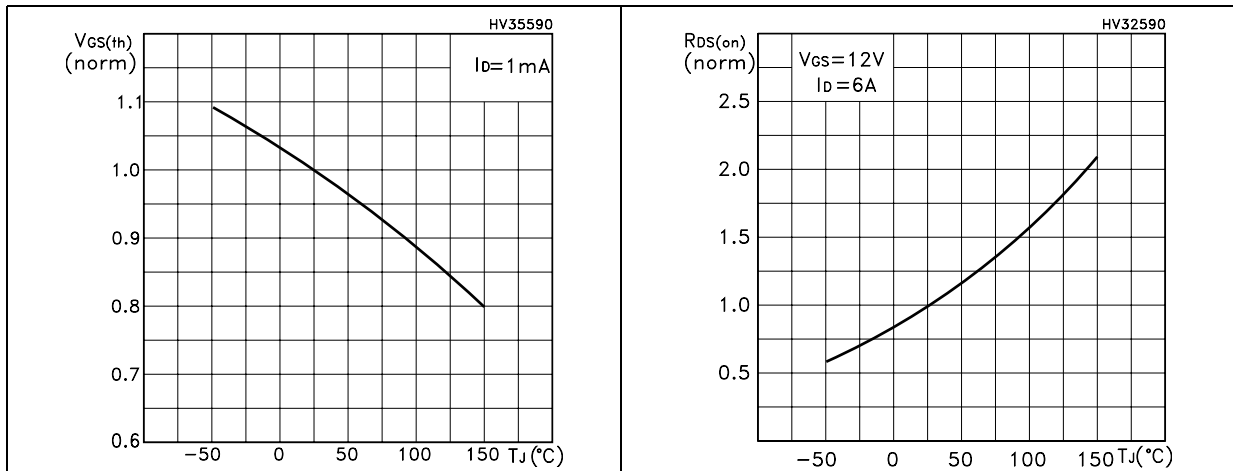
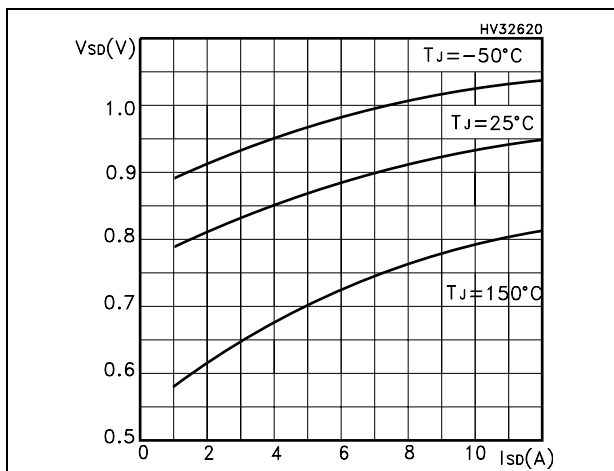


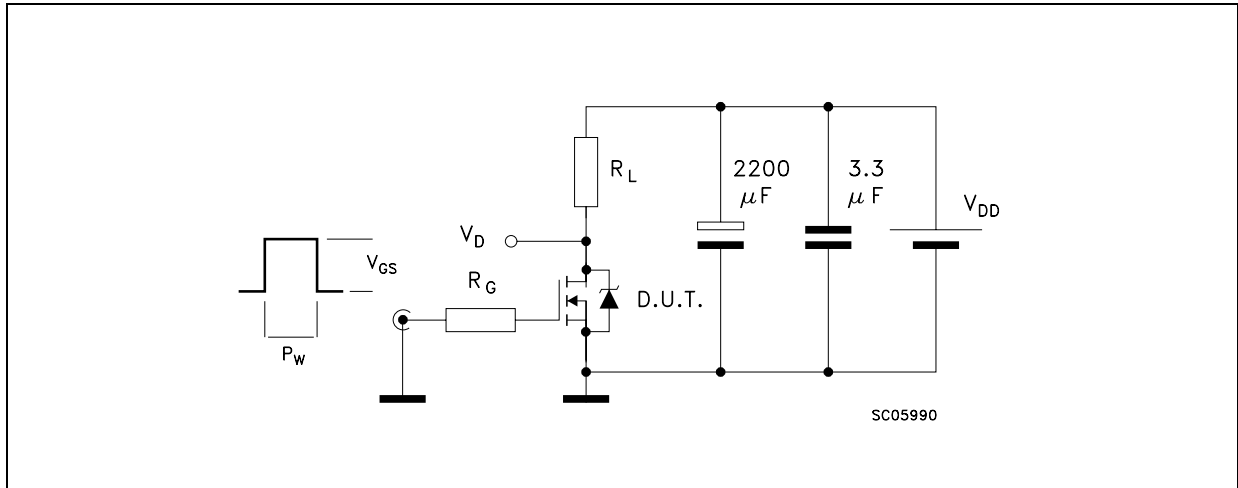
Figure 11. Source drain-diode forward characteristics





### 3 Test circuit

Figure 12. Switching times test circuit for resistive load (1)

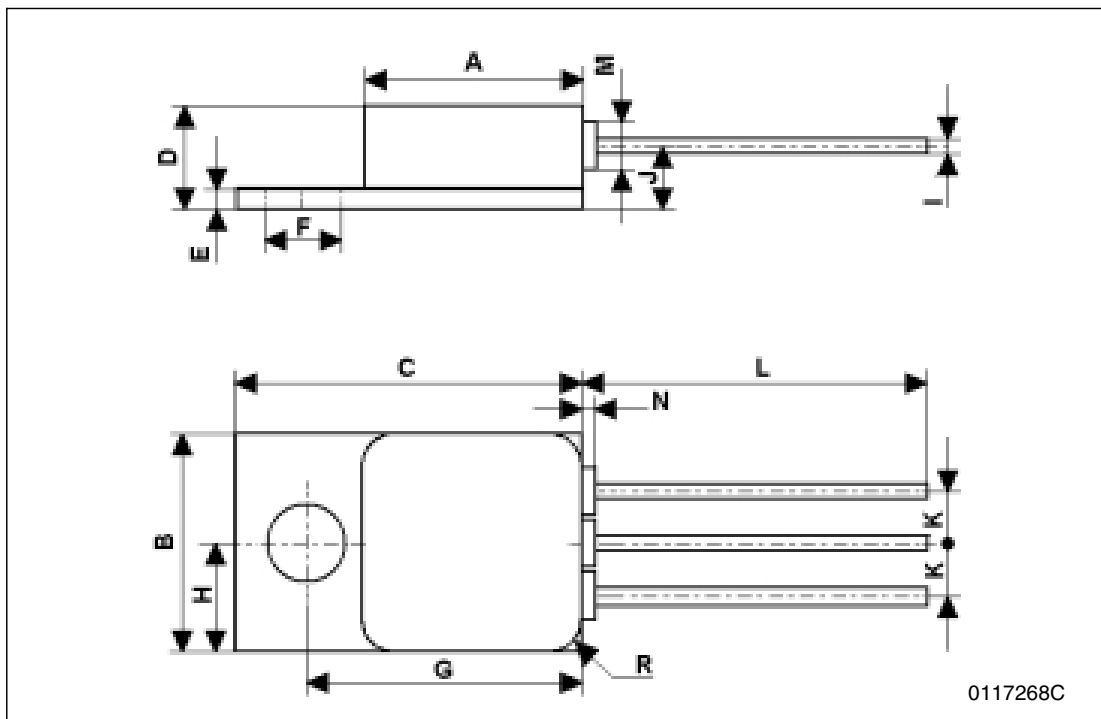


1. Max driver  $V_{GS}$  slope = 1V/ns (no DUT)

# 4 Package mechanical data

**TO-257AA MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A		10.54			0.415	
B		10.54			0.415	
C		16.64			0.655	
D	4.7		5.33	0.185		0.210
E		1.02			0.40	
F	3.56	3.68	3.81	0.140	0.145	0.150
G		13.51			0.532	
H		5.26			0.207	
I		0.76			0.030	
J		3.05			0.120	
K		2.54			0.100	
L	15.2		16.5	0.598		0.650
M		2.29			0.090	
N			0.71			0.028
R		1.65			0.065	



0117268C

## 5 Revision history

Table 11. Revision history

Date	Revision	Changes
18-Dec-2006	1	First release
26-Mar-2007	2	Complete version

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