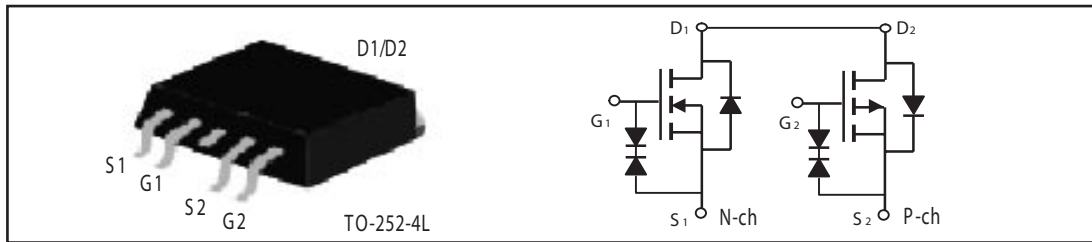




## Dual Enhancement Mode Field Effect Transistor ( N and P Channel)

PRODUCT SUMMARY (N-Channel)		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> ( mΩ ) Max
30V	19A	20 @ V <sub>GS</sub> = 10V
		28 @ V <sub>GS</sub> = 4.5V

PRODUCT SUMMARY (P-Channel)		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(ON)</sub> ( mΩ ) Max
-30V	-15A	30 @ V <sub>GS</sub> = -10V
		44 @ V <sub>GS</sub> = -4.5V



### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V <sub>DS</sub>	30	-30	V
Gate-Source Voltage		V <sub>GS</sub>	±20	±20	V
Drain Current-Continuous @ T <sub>c</sub>	25°C	I <sub>D</sub>	19	-15	A
	70°C		17	-13	A
-Pulsed <sup>a</sup>		I <sub>DM</sub>	50	-50	A
Drain-Source Diode Forward Current		I <sub>S</sub>	10	-6	A
Maximum Power Dissipation	T <sub>c</sub> = 25°C	P <sub>D</sub>	11		W
	T <sub>c</sub> = 70°C		7.7		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 175		°C

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	13.6	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	120	°C/W

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N-Channel ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V			1	uA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±10	uA
ON CHARACTERISTICS <sup>a</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1	1.8	3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A		14	20	m ohm
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A		20	28	m ohm
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> =5V, V <sub>GS</sub> =4.5V	20			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =10A		16.5		S
DYNAMIC CHARACTERISTICS <sup>b</sup>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V f=1.0MHz		635		pF
Output Capacitance	C <sub>OSS</sub>			170		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			97		pF
SWITCHING CHARACTERISTICS <sup>b</sup>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =15V I <sub>D</sub> =1A V <sub>GS</sub> =10V R <sub>GEN</sub> =6 ohm		12.5		ns
Rise Time	t <sub>r</sub>			12		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			45		ns
Fall Time	t <sub>f</sub>			10		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V		12		nC
		V <sub>DS</sub> =15V, I <sub>D</sub> =20A, V <sub>GS</sub> =4.5V		6		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =20A V <sub>GS</sub> =10V		1.8		nC
Gate-Drain Charge	Q <sub>gd</sub>			3.5		nC

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P-Channel ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V			-1	uA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±10	uA
ON CHARACTERISTICS <sup>a</sup>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1	-1.8	-3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-6A		24	30	m ohm
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A		35	44	m ohm
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> =-5V, V <sub>GS</sub> =-10V	-20			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-6A		12.5		S
DYNAMIC CHARACTERISTICS <sup>b</sup>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V f=1.0MHz		1120		pF
Output Capacitance	C <sub>OSS</sub>			280		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			190		pF
SWITCHING CHARACTERISTICS <sup>b</sup>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =-15V I <sub>D</sub> =-1A V <sub>GS</sub> =-10V R <sub>GEN</sub> =6 ohm		15		ns
Rise Time	t <sub>r</sub>			28		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			110		ns
Fall Time	t <sub>f</sub>			30		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-15V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V		22		nC
		V <sub>DS</sub> =-15V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-4.5V		10.5		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-15V, I <sub>D</sub> =-20 A		2.5		nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =-10V		6.5		nC

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ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit	
<b>DRAIN-SOURCE DIODE CHARACTERISTICS<sup>b</sup></b>							
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_s = 10A$	N-Ch		0.9	1.3	V
		$V_{GS} = 0V, I_s = -6A$	P-Ch		-0.9	-1.3	

## Notes

a. Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle  $\leq 2\%$ .

b. Guaranteed by design, not subject to production testing.

N-Channel

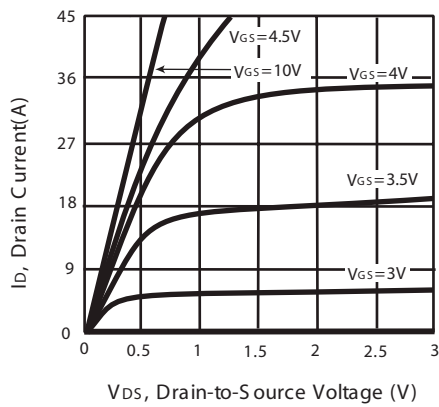


Figure 1. Output Characteristics

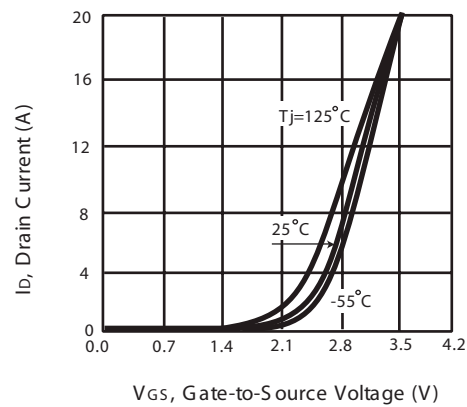


Figure 2. Transfer Characteristics

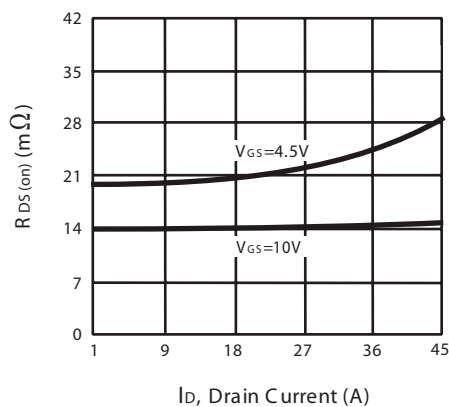


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

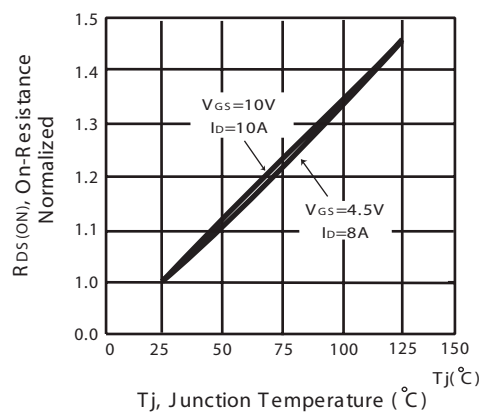


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STU310DH

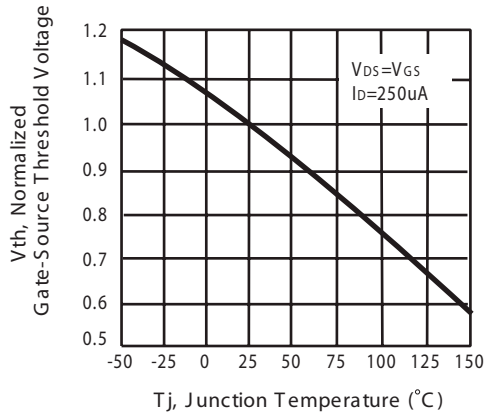


Figure 5. Gate Threshold Variation with Temperature

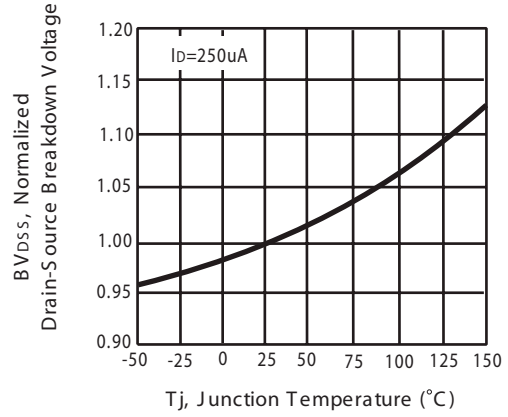


Figure 6. Breakdown Voltage Variation with Temperature

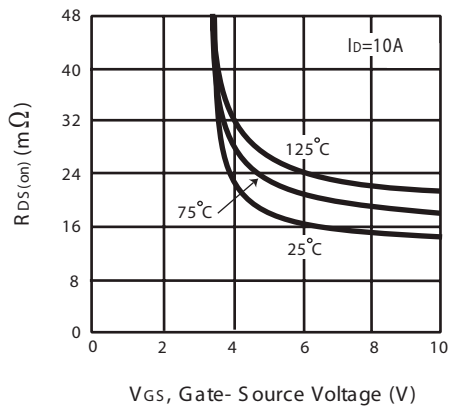


Figure 7. On-Resistance vs. Gate-Source Voltage

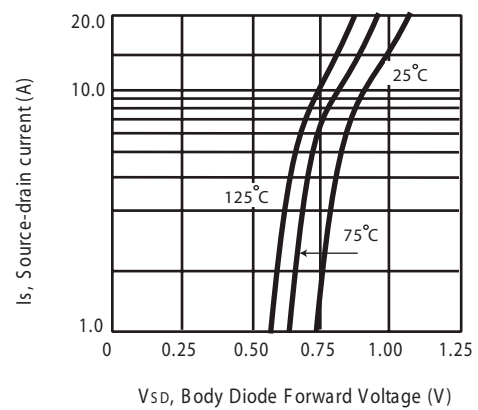


Figure 8. Body Diode Forward Voltage Variation with Source Current

# STU310DH

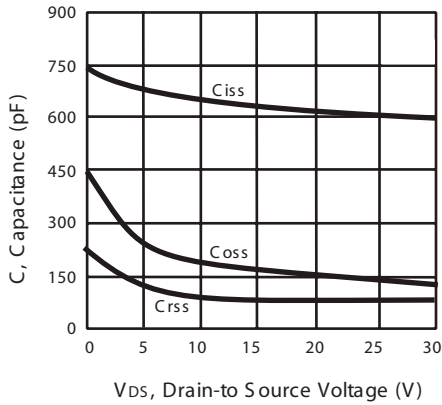


Figure 10. Capacitance

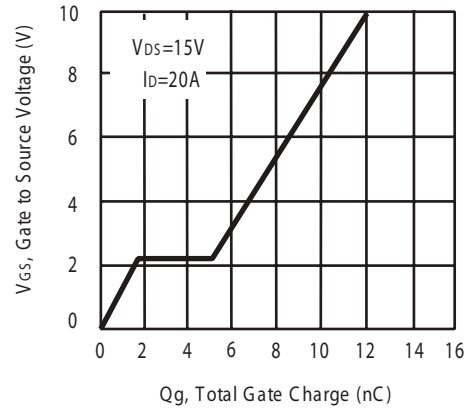


Figure 11. Gate Charge

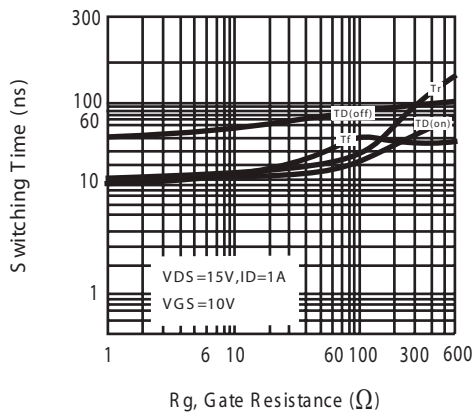


Figure 12. switching characteristics

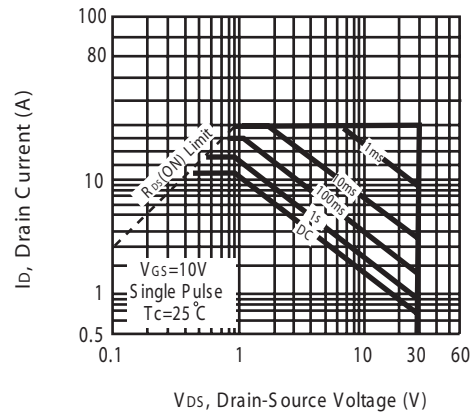


Figure 13. Maximum Safe Operating Area

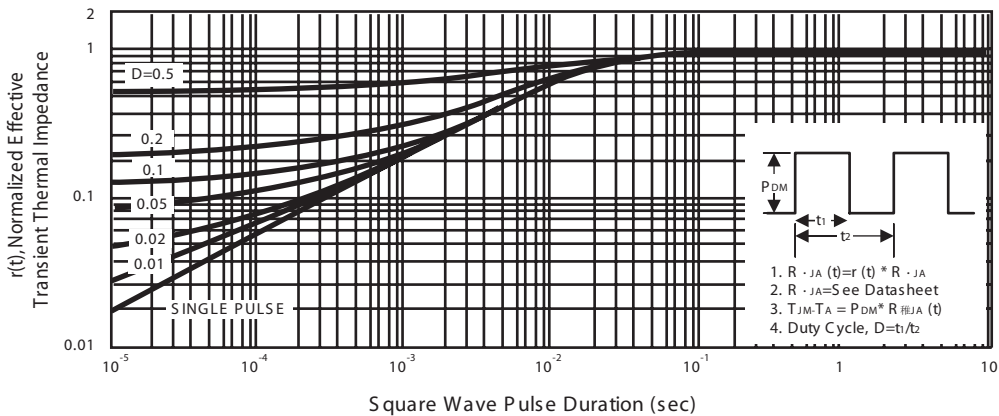


Figure 14. Normalized Thermal Transient Impedance Curve

# STU310DH

## P-Channel

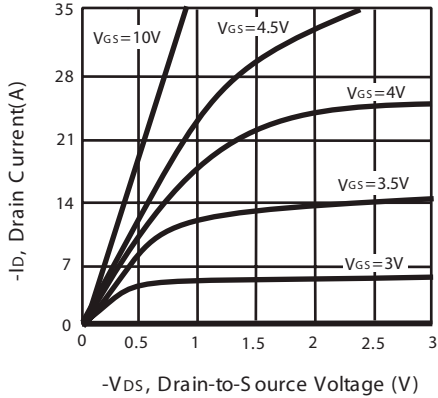


Figure 1. Output Characteristics

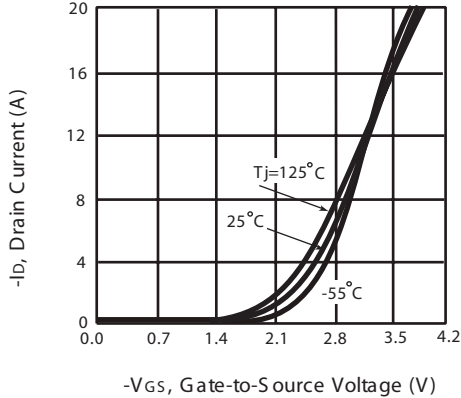


Figure 2. Transfer Characteristics

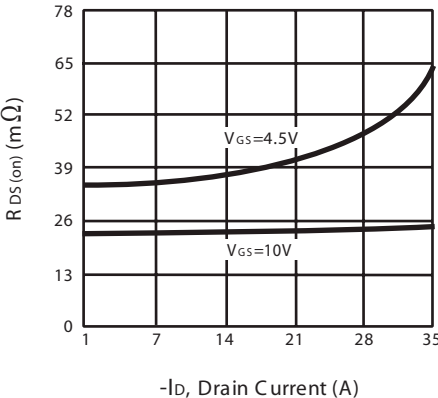


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

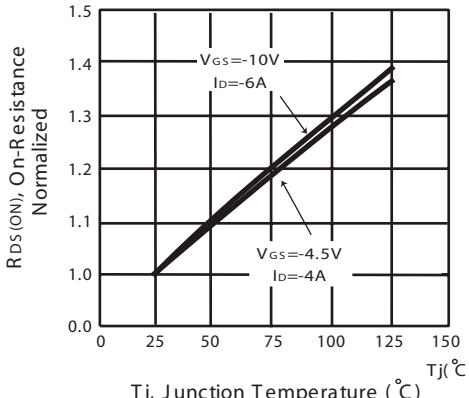


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STU310DH

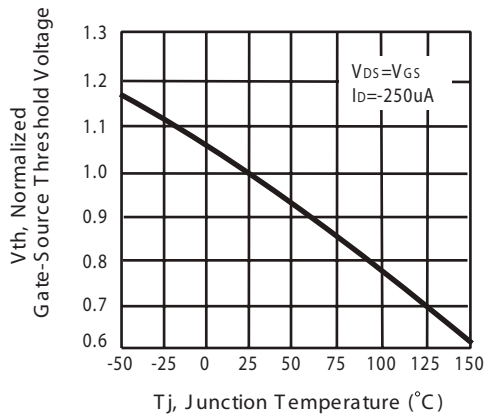


Figure 5. Gate Threshold Variation with Temperature

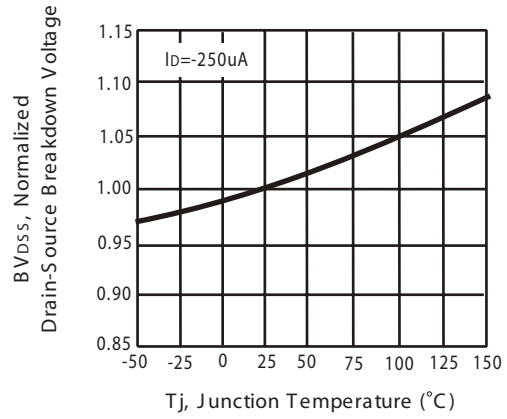


Figure 6. Breakdown Voltage Variation with Temperature

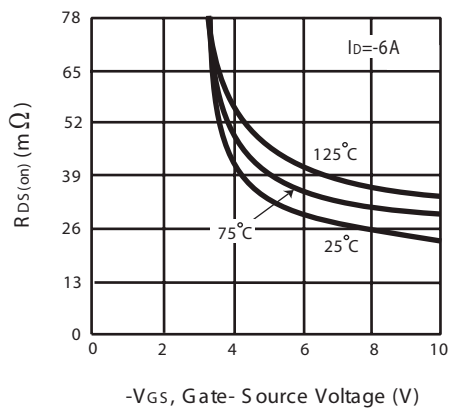


Figure 7. On-Resistance vs. Gate-Source Voltage

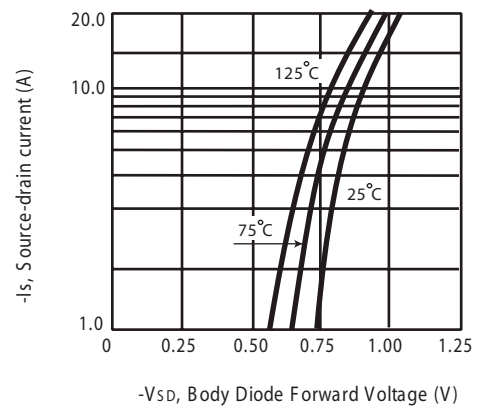


Figure 8. Body Diode Forward Voltage Variation with Source Current



# STU310DH

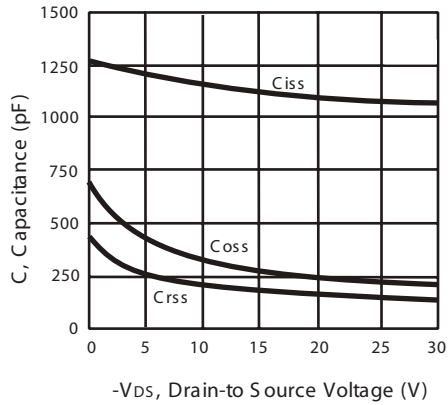


Figure 10. Capacitance

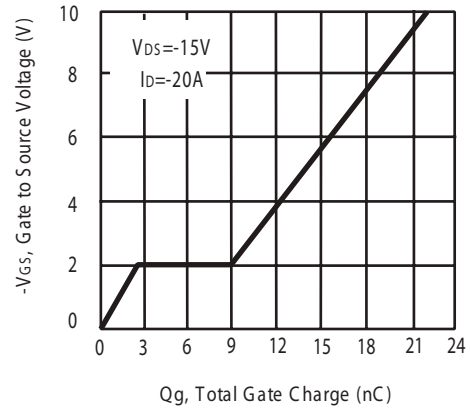


Figure 11. Gate Charge

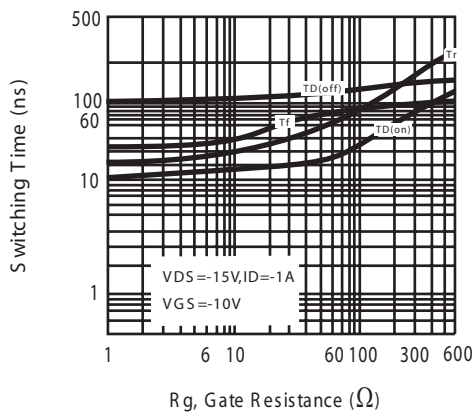


Figure 12. switching characteristics

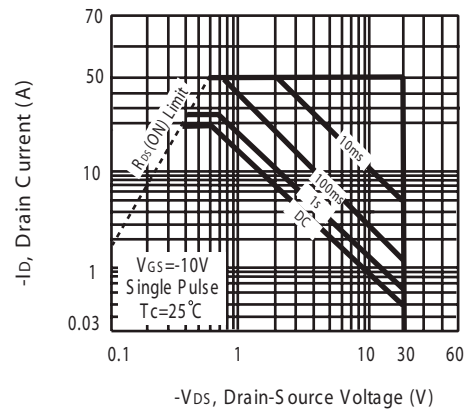


Figure 13. Maximum Safe Operating Area

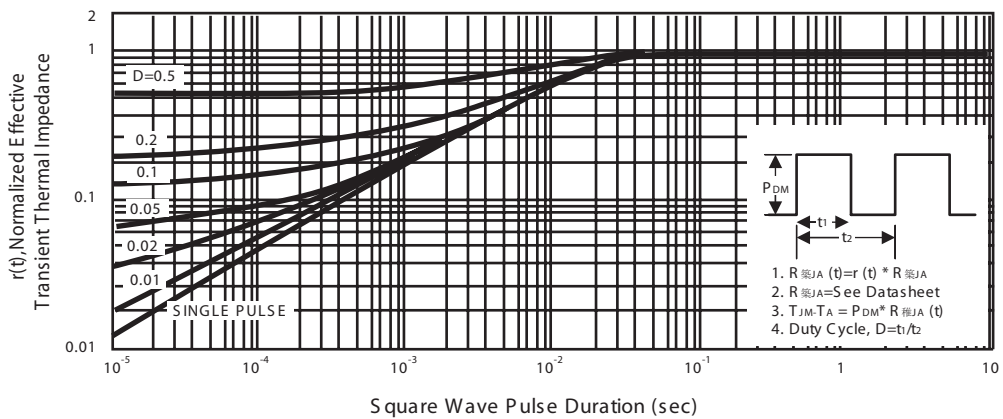
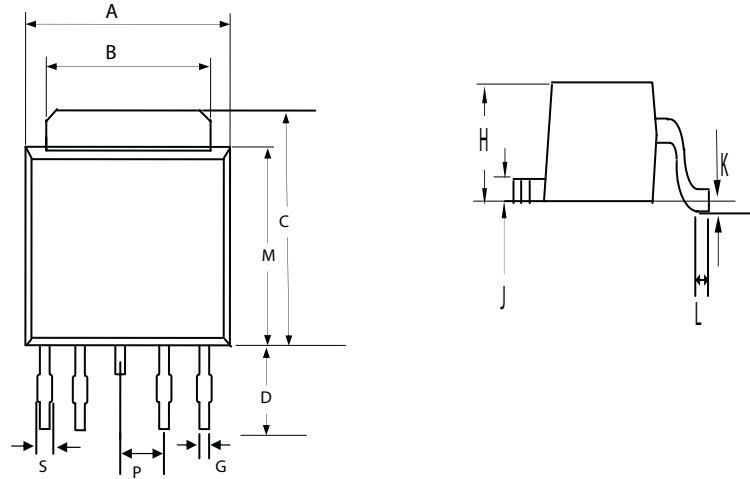


Figure 14. Normalized Thermal Transient Impedance Curve

# STU310DH

## PACKAGE OUTLINE DIMENSIONS

TO-252-4L

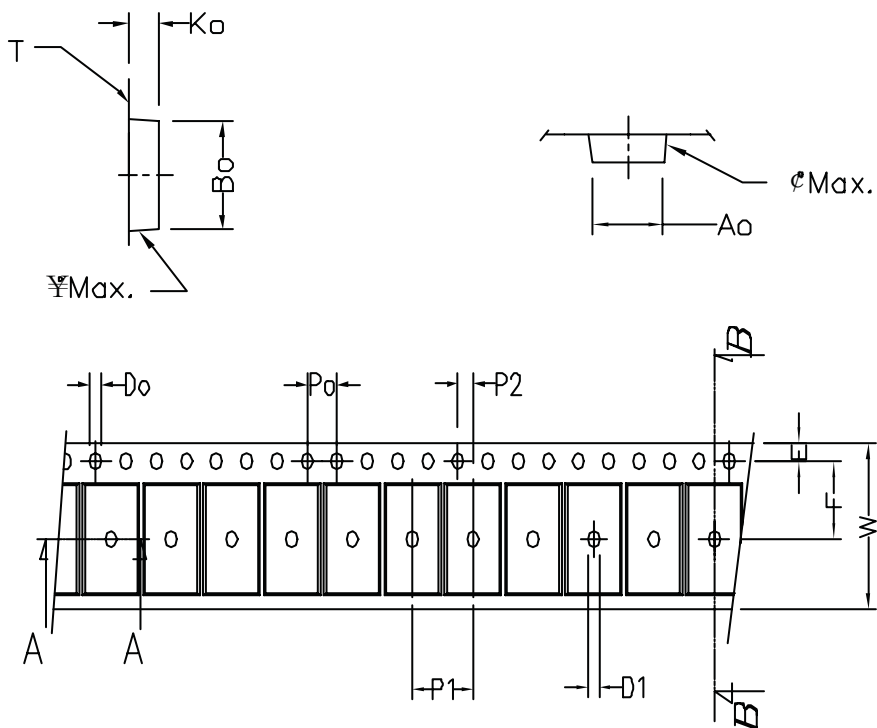


REF .	Millimeters	
	MIN	MAX
A	6.40	6.80
B	5.2	5.50
C	6.80	10.20
D	2.20	3.00
P	1.27 REF .	
S	0.50	0.80
G	0.40	0.60
H	2.20	2.40
J	0.45	0.60
K	0	0.15
L	0.90	1.50
M	5.40	5.80

# STU310DH

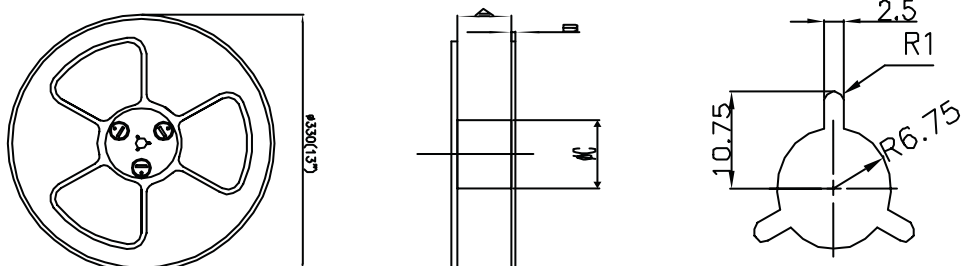
## TO-252-4L Tape and Reel Data

### TO-252-4L Carrier Tape



symbol	Ao	Bo	Ko	Po	P1	P2	T
Spec	6.96±0.1	10.49±0.1	2.79±0.1	4.0±0.1	8.0±0.10	2.0±0.05	0.33±0.013
symbol	E	F	Do	D1	W	10Po	
Spec	1.75±0.1	7.5±0.05	1.55±0.05	1.5±0.25	16.0 <sup>+0.3</sup> <sub>-0.1</sub>	40.0±0.2	

### TO-252-4L Reel



UNIT: mm

Width of carrier tape	8	12	16	24	32	44	56
A±0.1	9.4	13.4	17.4	25.4	33.4	45.4	57.4
B	2.3	2.3	2.3	2.3	2.3	2.3	2.3
∅C	100	100	100	100	100	100	100