



STU314D

SamHop Microelectronics Corp.

Ver 1.0

Dual Enhancement Mode Field Effect Transistor (N and P Channel)

PRODUCT SUMMARY (N-Channel)		
V _{DSS}	ID	R _{D(S(ON))} (mΩ) Max
30V	16A	28 @ V _{G(S)} =10V
		40 @ V _{G(S)} =4.5V

PRODUCT SUMMARY (P-Channel)		
V _{DSS}	ID	R _{D(S(ON))} (mΩ) Max
-30V	-14A	34 @ V _{G(S)} =-10V
		55 @ V _{G(S)} =-4.5V



ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units	
V _{DS}	Drain-Source Voltage	30	-30	V	
V _{GS}	Gate-Source Voltage	± 20	± 20	V	
I _D	Drain Current-Continuous ^a	T _C =25°C	16	-14	A
		T _C =70°C	13	-11	A
I _{DM}	-Pulsed ^b	47	-42	A	
E _{AS}	Sigle Pulse Avalanche Energy ^d	16	64	mJ	
P _D	Maximum Power Dissipation ^a	T _C =25°C	10.4	W	
		T _C =70°C	6.7	W	
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150		°C	

THERMAL CHARACTERISTICS

R _{θJC}	Thermal Resistance, Junction-to-Case ^a	12	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient ^a	60	°C/W

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N-Channel ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V , V _{GS} =0V			1	uA
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V , V _{DS} =0V			±10	uA
ON CHARACTERISTICS						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1	1.9	3	V
R _{D(S(ON))}	Drain-Source On-State Resistance	V _{GS} =10V , I _D =16A		22	28	m ohm
		V _{GS} =4.5V , I _D =13.7A		31	40	m ohm
g _{FS}	Forward Transconductance	V _{DS} =5V , I _D =16A		25		S
DYNAMIC CHARACTERISTICS ^c						
C _{iss}	Input Capacitance	V _{DS} =15V,V _{GS} =0V f=1.0MHz		505		pF
C _{oss}	Output Capacitance			105		pF
C _{rss}	Reverse Transfer Capacitance			65		pF
SWITCHING CHARACTERISTICS ^c						
t _{D(ON)}	Turn-On Delay Time	V _{DD} =15V I _D =1A V _{GS} =10V R _{GEN} =6 ohm		12		ns
t _r	Rise Time			11		ns
t _{D(OFF)}	Turn-Off Delay Time			16.6		ns
t _f	Fall Time			14		ns
Q _g	Total Gate Charge	V _{DS} =15V,I _D =16A,V _{GS} =10V		8.8		nC
		V _{DS} =15V,I _D =16A,V _{GS} =4.5V		4.3		nC
Q _{gs}	Gate-Source Charge	V _{DS} =15V,I _D =16A, V _{GS} =10V		1.5		nC
Q _{gd}	Gate-Drain Charge			2.3		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
I _s	Maximum Continuous Drain-Source Diode Forward Current			1.7		A
V _{SD}	Diode Forward Voltage ^b	V _{GS} =0V,I _s =1.7A		0.8	1.2	V

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P-Channel ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$			-1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}= \pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$			± 10	μA
ON CHARACTERISTICS						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1	-1.8	-3	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-14\text{A}$		26	34	m ohm
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-11\text{A}$		41	55	m ohm
g_{FS}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-14\text{A}$		17		S
DYNAMIC CHARACTERISTICS ^c						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		815		pF
C_{oss}	Output Capacitance			215		pF
C_{rss}	Reverse Transfer Capacitance			125		pF
SWITCHING CHARACTERISTICS ^c						
$t_{\text{D}(\text{ON})}$	Turn-On Delay Time	$V_{\text{DD}}=-15\text{V}$ $I_{\text{D}}=-1\text{A}$ $V_{\text{GS}}=-10\text{V}$ $R_{\text{GEN}}=6\text{ ohm}$		13		ns
t_{r}	Rise Time			15		ns
$t_{\text{D}(\text{OFF})}$	Turn-Off Delay Time			62		ns
t_{f}	Fall Time			13		ns
Q_g	Total Gate Charge	$V_{\text{DS}}=-15\text{V}$, $I_{\text{D}}=-14\text{A}$, $V_{\text{GS}}=-10\text{V}$		15.5		nC
		$V_{\text{DS}}=-15\text{V}$, $I_{\text{D}}=-14\text{A}$, $V_{\text{GS}}=-4.5\text{V}$		7.3		nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-15\text{V}$, $I_{\text{D}}=-14\text{A}$, $V_{\text{GS}}=-10\text{V}$		1.7		nC
Q_{gd}	Gate-Drain Charge			4.7		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
I_s	Maximum Continuous Drain-Source Diode Forward Current			-1.7		A
V_{SD}	Diode Forward Voltage ^b	$V_{\text{GS}}=0\text{V}$, $I_s=-1.7\text{A}$		-0.77	-1.3	V

Notes

- a.Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.
- b.Pulse Test:Pulse Width $\leq 300\text{us}$, Duty Cycle $\leq 2\%$.
- c.Guaranteed by design, not subject to production testing.
- d.Starting $T_J=25^\circ\text{C}$, $L=0.5\text{mH}$, $V_{\text{DD}}=20\text{V}$, $V_{\text{GS}}=10\text{V}$. (See Figure13)

N-Channel

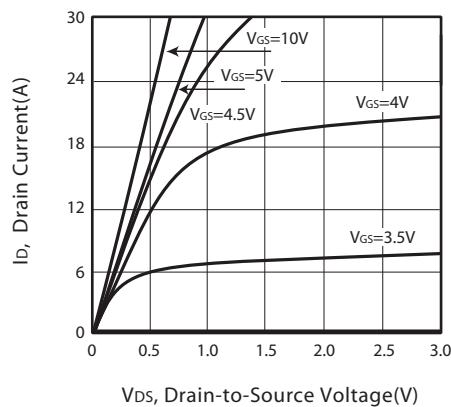


Figure 1. Output Characteristics

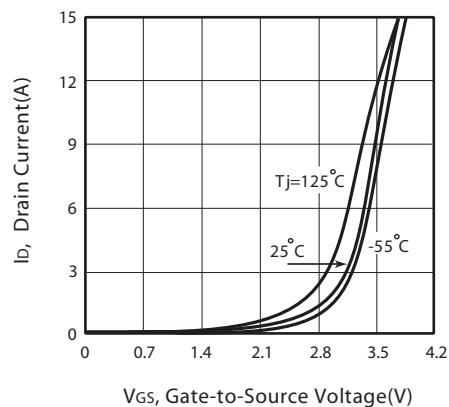


Figure 2. Transfer Characteristics

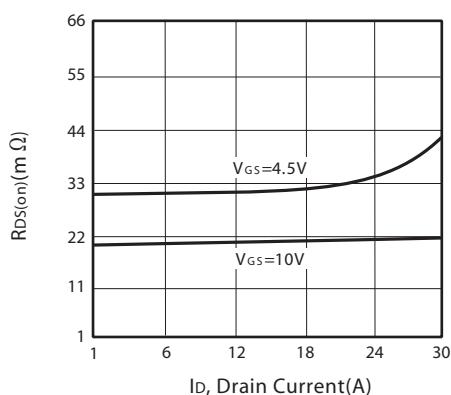


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

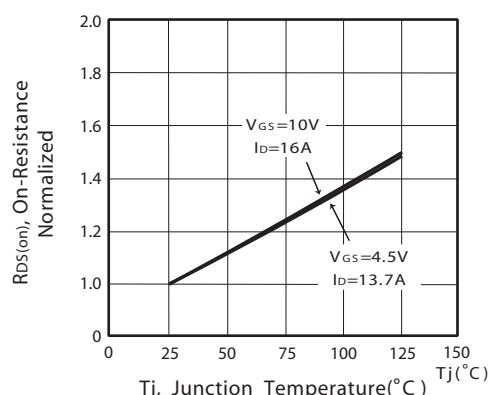


Figure 4. On-Resistance Variation with Drain Current and Temperature

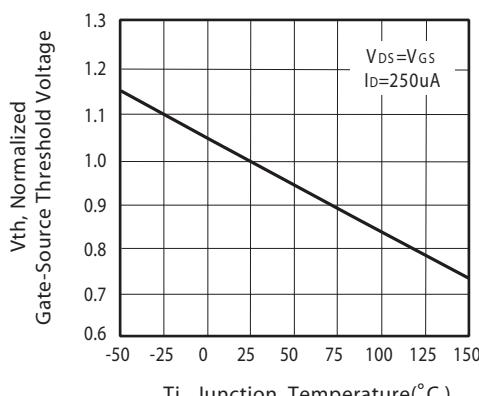


Figure 5. Gate Threshold Variation with Temperature

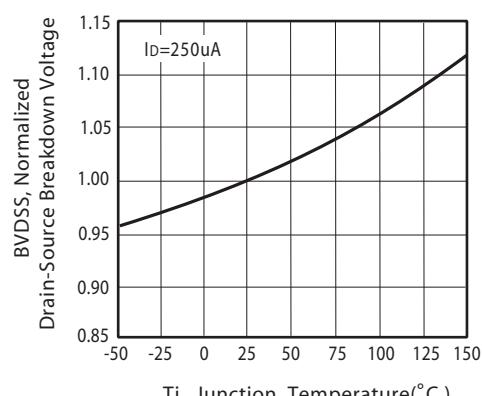
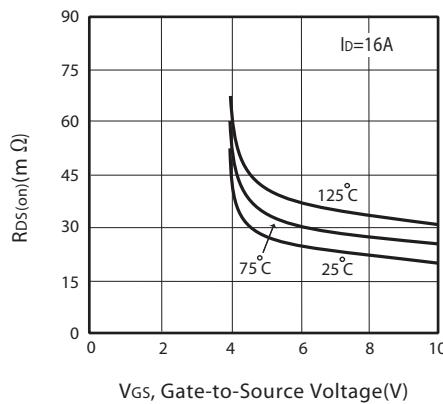


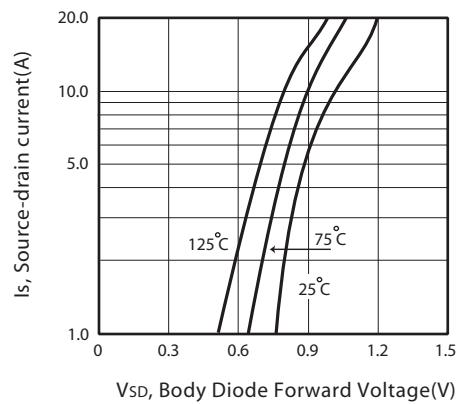
Figure 6. Breakdown Voltage Variation with Temperature

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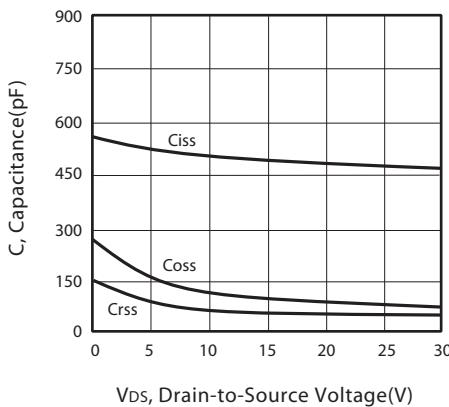
V_{GS}, Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs. Gate-Source Voltage



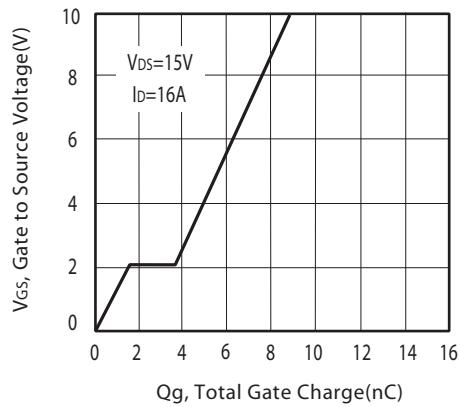
V_{SD}, Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



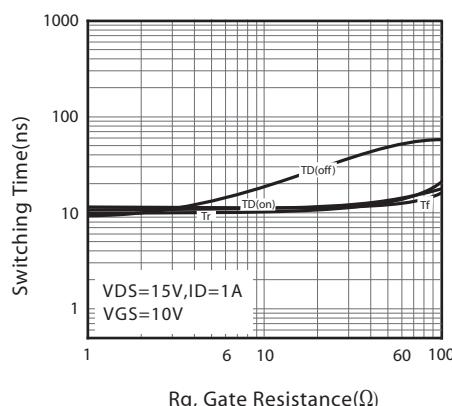
V_{DS}, Drain-to-Source Voltage(V)

Figure 9. Capacitance



Q_g, Total Gate Charge(nC)

Figure 10. Gate Charge



R_g, Gate Resistance(Ω)

Figure 11. switching characteristics

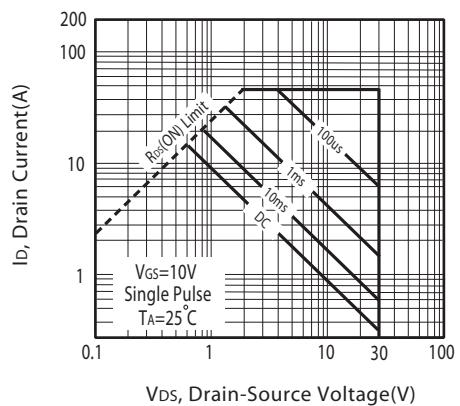
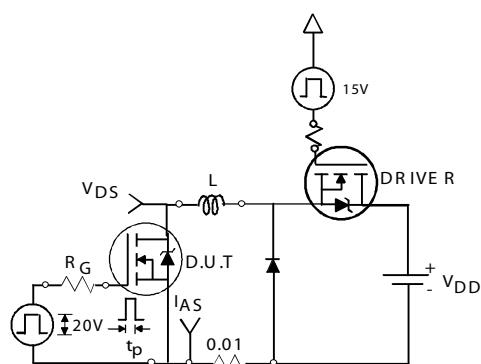
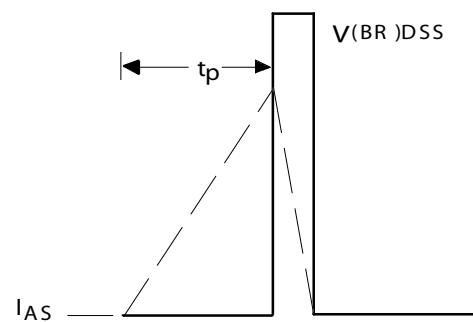


Figure 12. Maximum Safe Operating Area



Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.

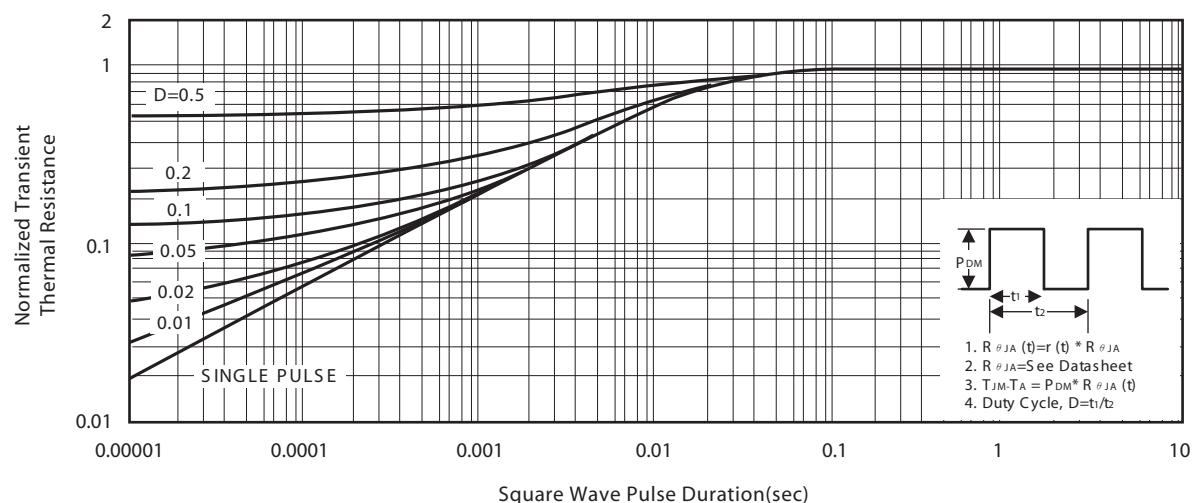


Figure 14. Normalized Thermal Transient Impedance Curve

P-Channel

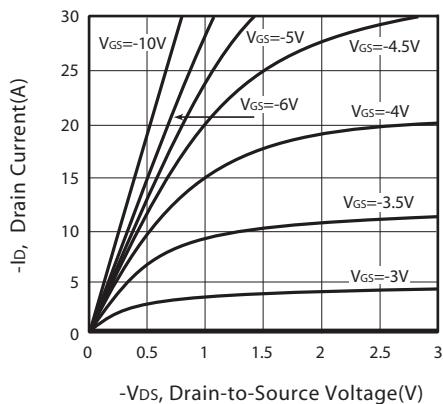


Figure 1. Output Characteristics

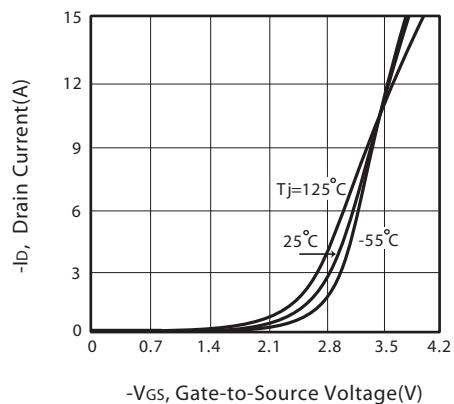


Figure 2. Transfer Characteristics

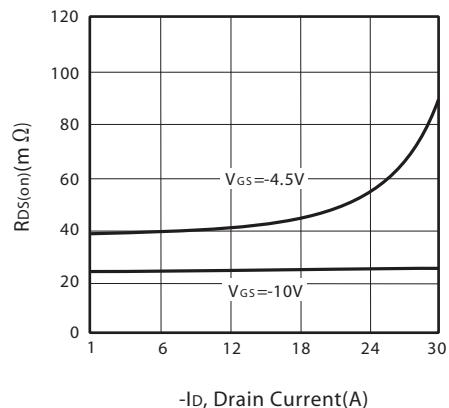


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

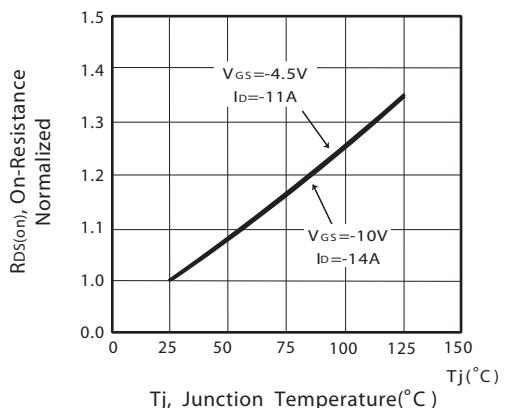


Figure 4. On-Resistance Variation with Drain Current and Temperature

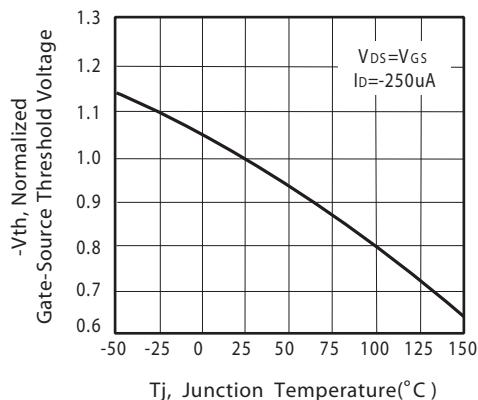


Figure 5. Gate Threshold Variation with Temperature

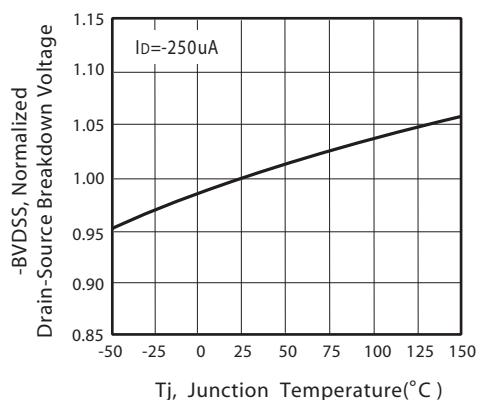


Figure 6. Breakdown Voltage Variation with Temperature

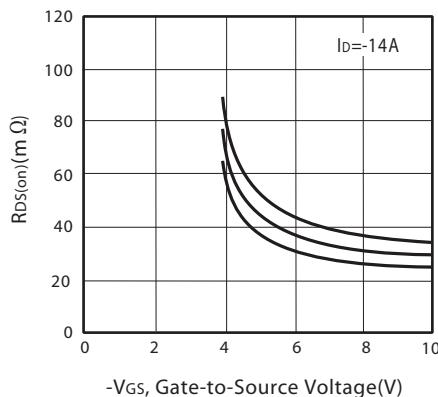


Figure 7. On-Resistance vs.
Gate-Source Voltage

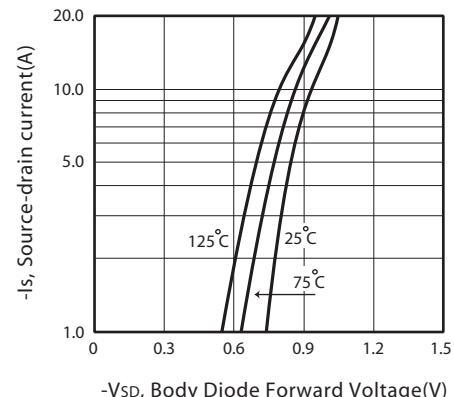


Figure 8. Body Diode Forward Voltage
Variation with Source Current

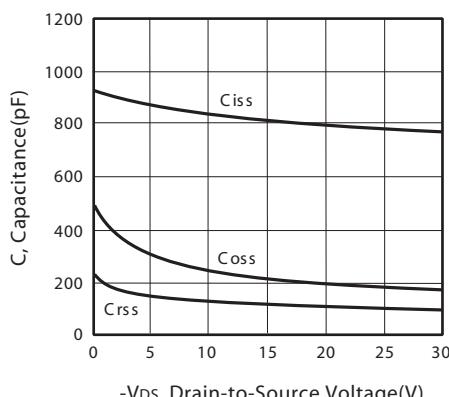


Figure 9. Capacitance

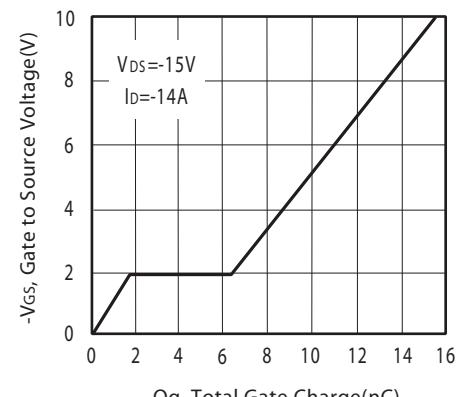


Figure 10. Gate Charge

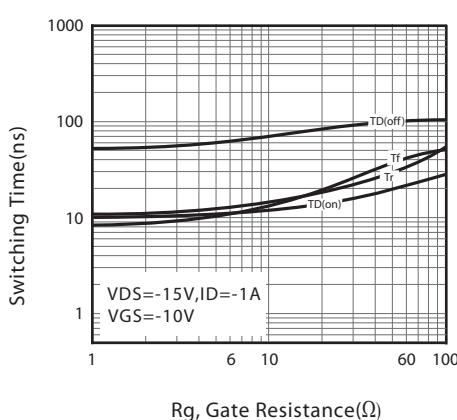


Figure 11. switching characteristics

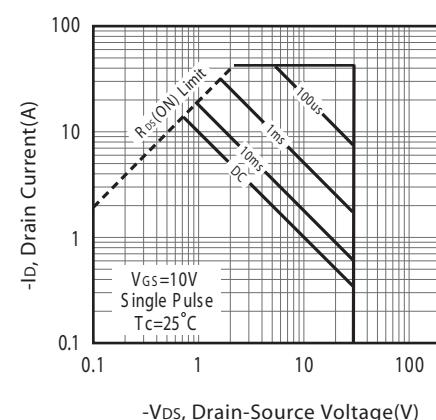
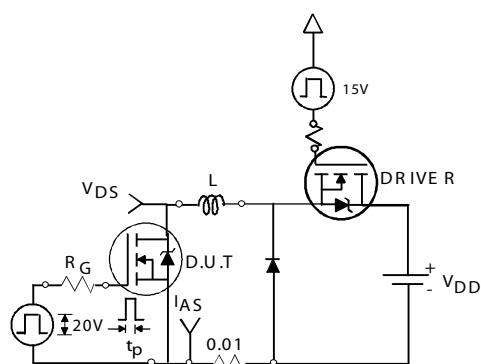


Figure 12. Maximum Safe Operating Area

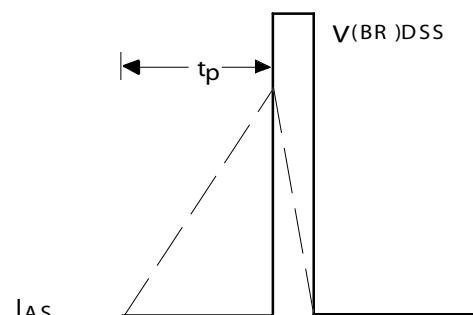
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Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.

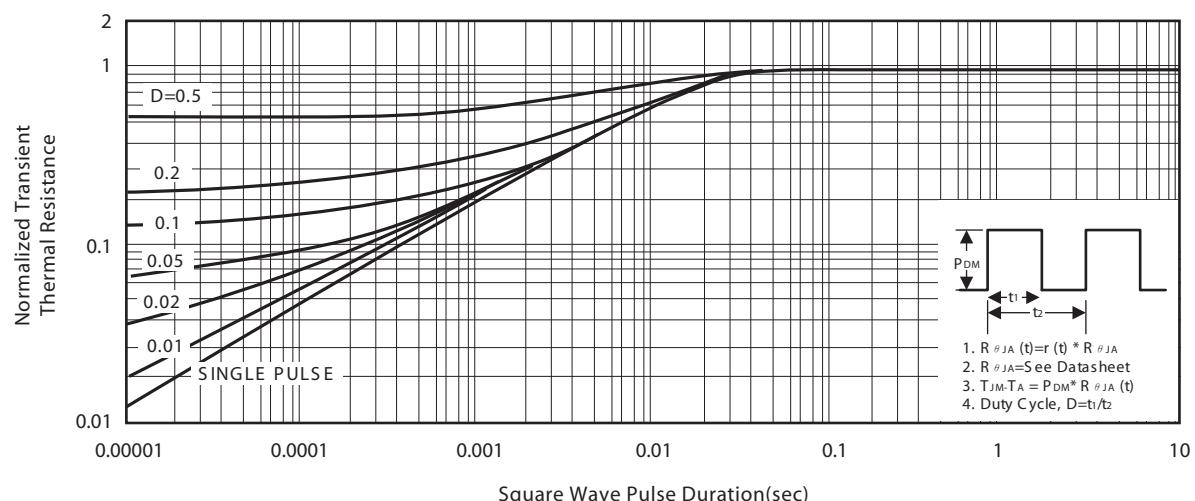
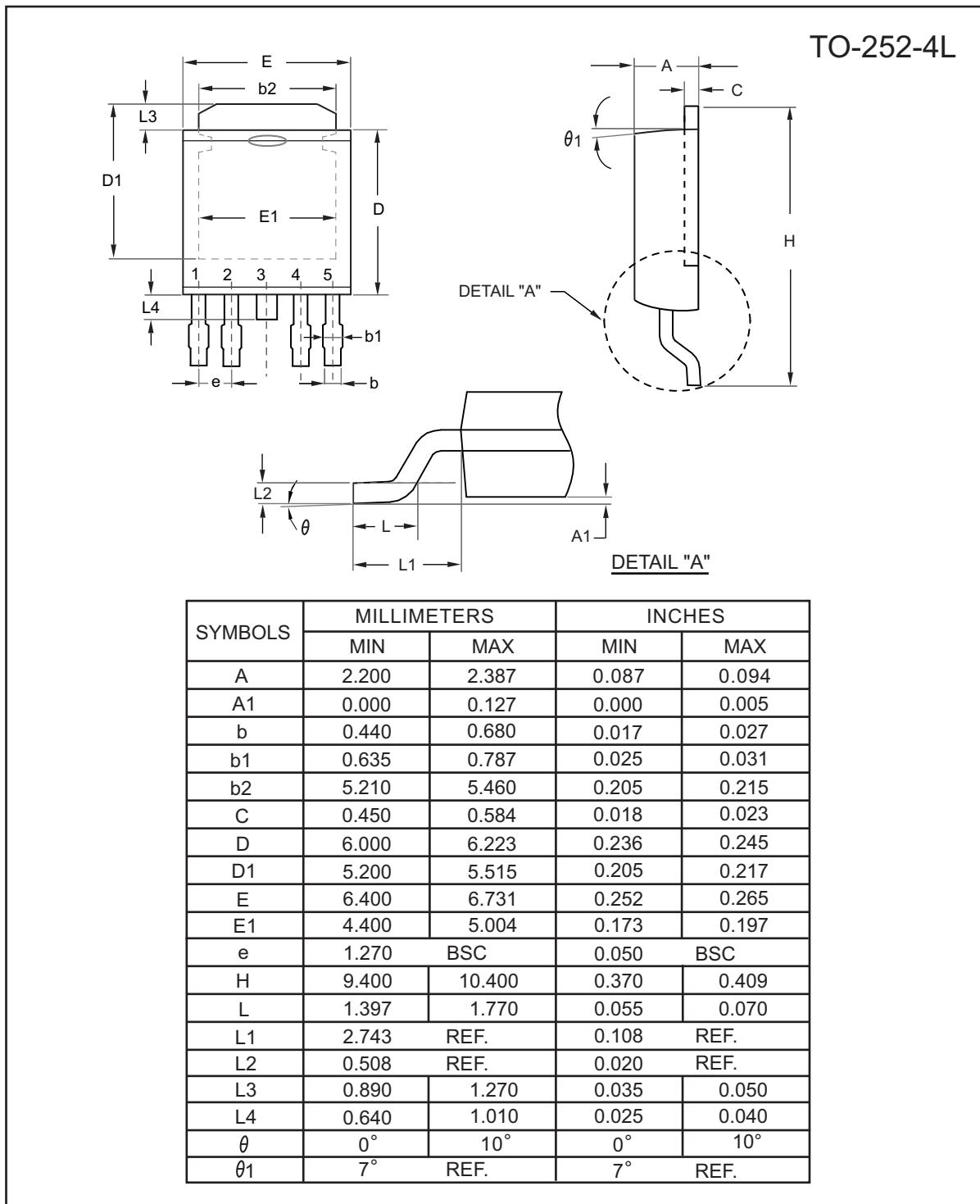


Figure 14. Normalized Thermal Transient Impedance Curve

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PACKAGE OUTLINE DIMENSIONS

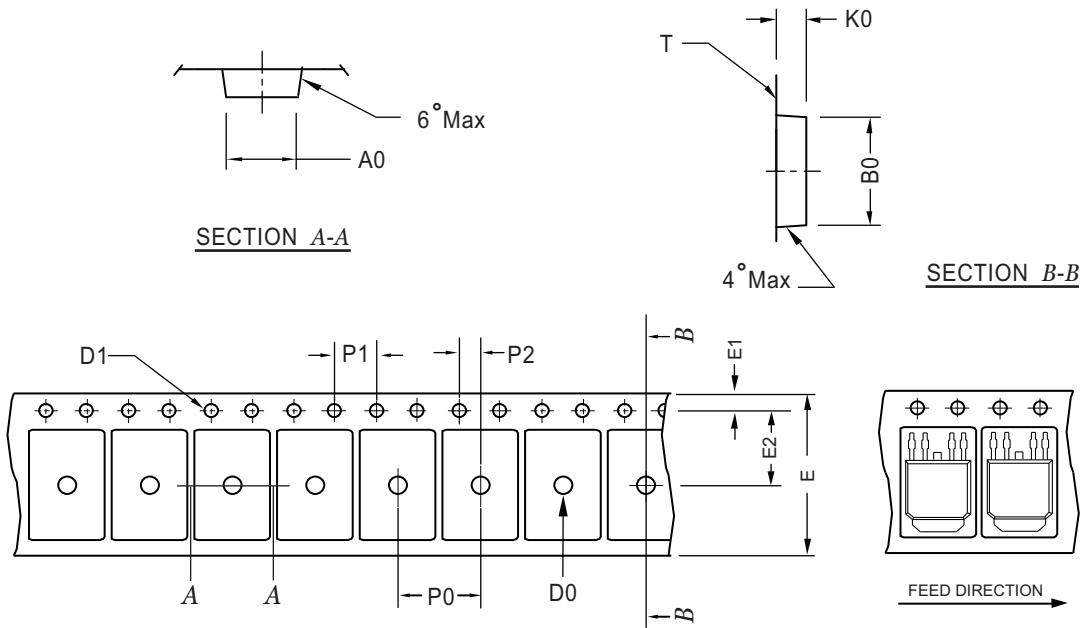


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TO-252-4L Tape and Reel Data

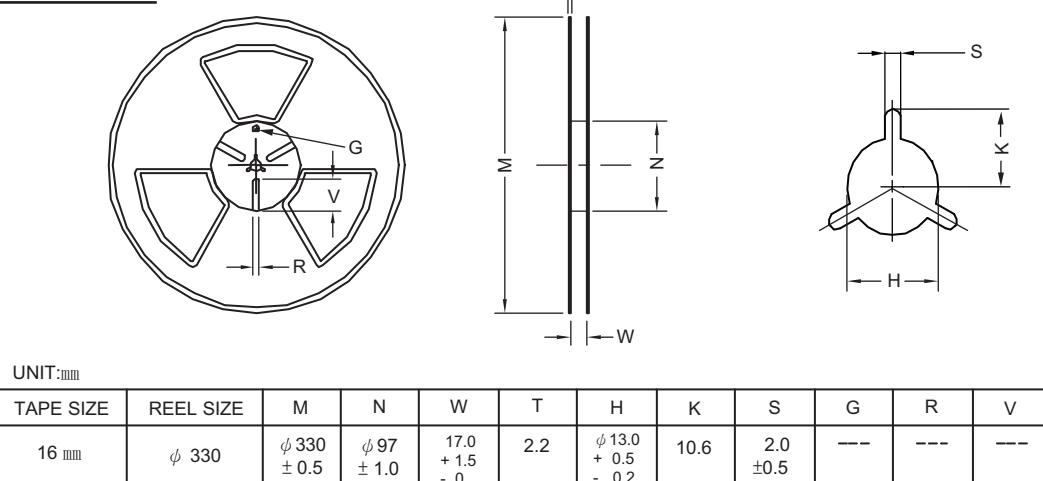
TO-252-4L Carrier Tape



UNIT:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
TO-252 (16 mm)	6.96 ±0.1	10.49 ±0.1	2.79 ±0.1	ψ 2	ψ 1.5 + 0.1 - 0	16.0 ±0.3	1.75 ±0.1	7.5 ±0.15	8.0 ±0.1	4.0 ±0.1	2.0 ±0.15	0.3 ±0.05

TO-252-4L Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	T	H	K	S	G	R	V
16 mm	ψ 330	ψ 330 ± 0.5	ψ 97 ± 1.0	17.0 + 1.5 - 0	2.2	ψ 13.0 + 0.5 - 0.2	10.6	2.0 ± 0.5	---	---	---

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