



SamHop Microelectronics Corp.

## STU/D410S

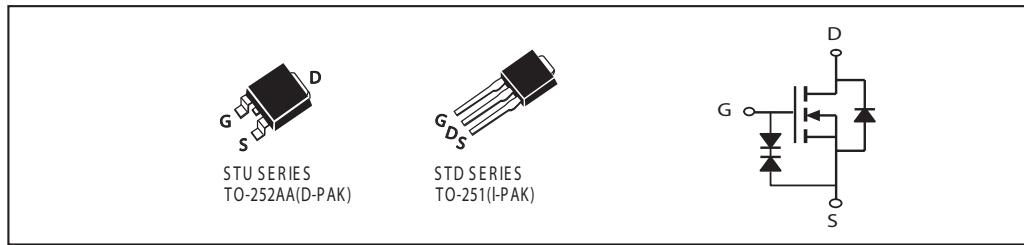
Mar. 26 2007

## N-Channel Logic Level Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DSON</sub> (mΩ) Max
40V	30A	20 @ V <sub>GS</sub> = 10V
		30 @ V <sub>GS</sub> = 4.5V

## FEATURES

- Super high dense cell design for low R<sub>DSON</sub>.
- Rugged and reliable.
- Surface Mount Package.
- ESD Protected.

ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	40	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous <sup>a</sup> @ T <sub>c</sub> =25°C -Pulsed <sup>b</sup>	I <sub>D</sub>	30	A
	I <sub>DM</sub>	100	A
Drain-Source Diode Forward Current	I <sub>S</sub>	8	A
Maximum Power Dissipation @ T <sub>c</sub> =25°C	P <sub>D</sub>	50	W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 175	°C

## THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	3	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	50	°C/W

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ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=32\text{V}, V_{\text{GS}}=0\text{V}$		1		$\mu\text{A}$
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$		$\pm10$		$\mu\text{A}$
ON CHARACTERISTICS <sup>a</sup>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	1.9	3	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$		16	20	$\text{m ohm}$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$		23	30	$\text{m ohm}$
On-State Drain Current	$I_{\text{D}(\text{ON})}$	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=10\text{V}$	30			A
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=10\text{A}$		17		S
DYNAMIC CHARACTERISTICS <sup>b</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}$ $f=1.0\text{MHz}$		690		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			140		$\text{pF}$
Reverse Transfer Capacitance	$C_{\text{rss}}$			95		$\text{pF}$
SWITCHING CHARACTERISTICS <sup>b</sup>						
Turn-On Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=15\text{V}$ $I_{\text{D}}=1\text{ A}$ $V_{\text{GS}}=10\text{V}$ $R_{\text{GEN}}=3\text{ ohm}$		13.1		ns
Rise Time	$t_{\text{r}}$			13.5		ns
Turn-Off Delay Time	$t_{\text{D}(\text{OFF})}$			45.7		ns
Fall Time	$t_{\text{f}}$			11.8		ns
Total Gate Charge	$Q_g$	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}, V_{\text{GS}}=10\text{V}$		13.5		$\text{nC}$
		$V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}, V_{\text{GS}}=4.5\text{V}$		6.7		$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}$		1.65		$\text{nC}$
				3.85		$\text{nC}$

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ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>a</sup>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0\text{V}, I_s = 8\text{A}$		0.84	1.3	V

## Notes

a.Pulse Test:Pulse Width  $\leq 300\text{us}$ , Duty Cycle  $\leq 2\%$ .

b.Guaranteed by design, not subject to production testing.

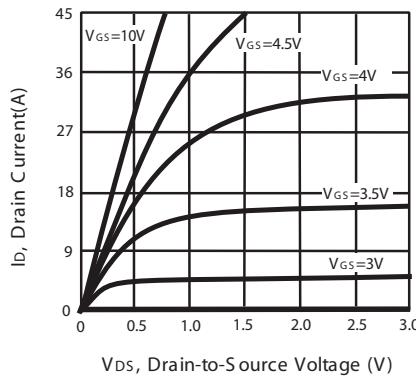


Figure 1. Output Characteristics

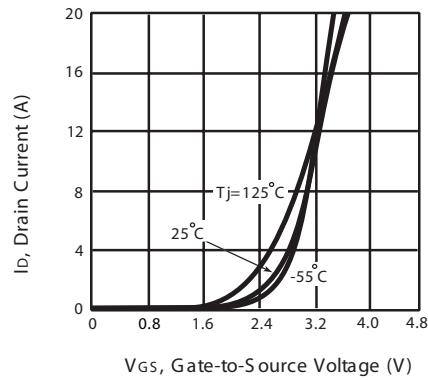


Figure 2. Transfer Characteristics

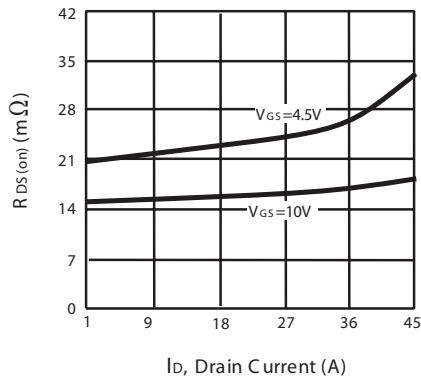


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

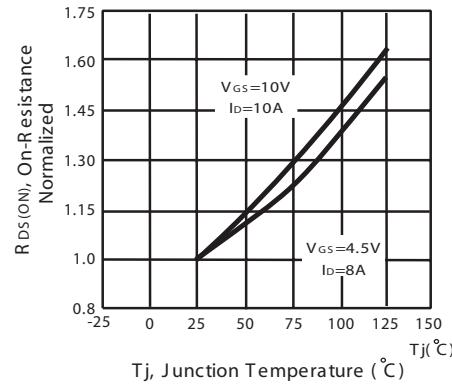


Figure 4. On-Resistance Variation with Drain Current and Temperature

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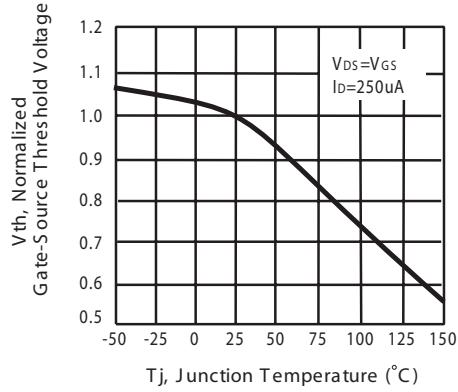


Figure 5. Gate Threshold Variation with Temperature

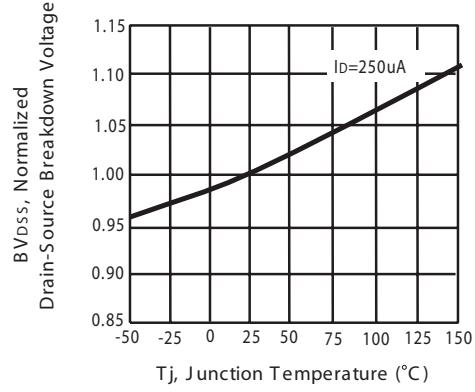


Figure 6. Breakdown Voltage Variation with Temperature

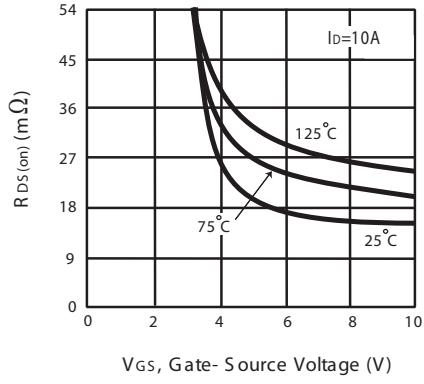


Figure 7. On-Resistance vs. Gate-Source Voltage

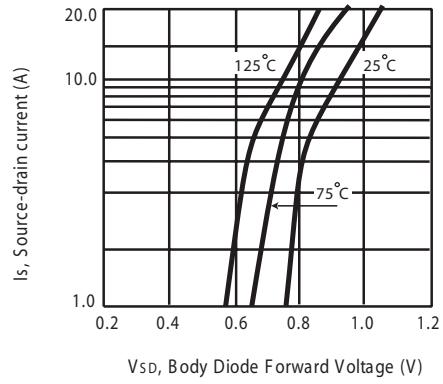


Figure 8. Body Diode Forward Voltage Variation with Source Current

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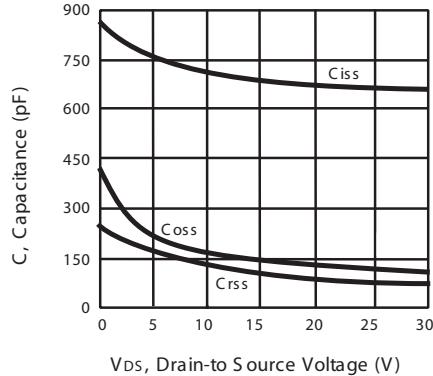


Figure 9. Capacitance

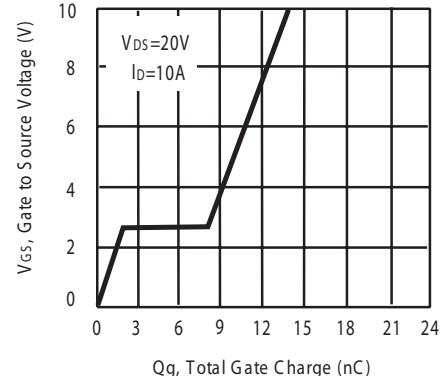


Figure 10. Gate Charge

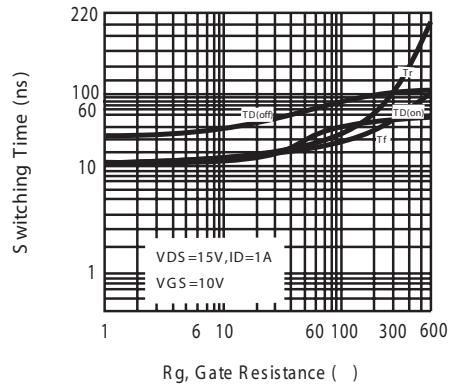


Figure 11. switching characteristics

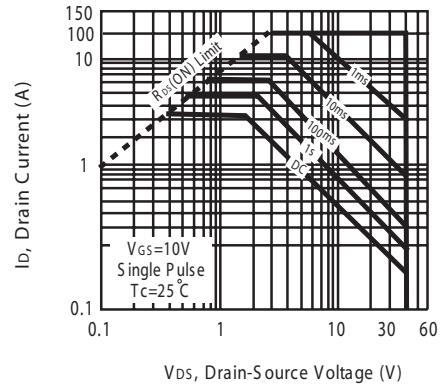


Figure 12. Maximum Safe Operating Area

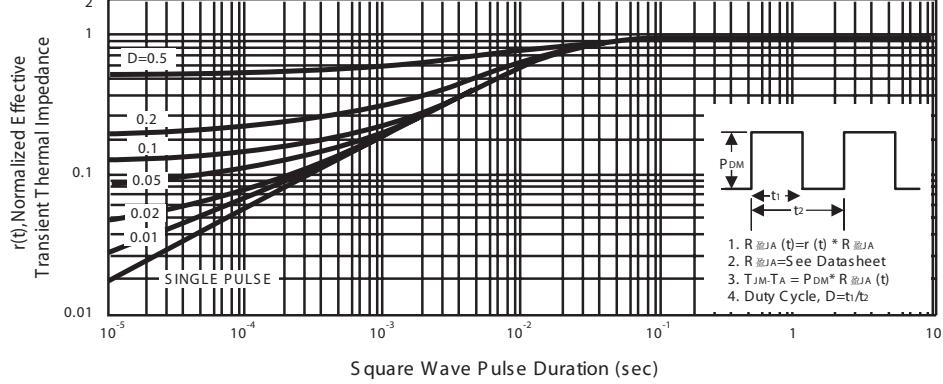
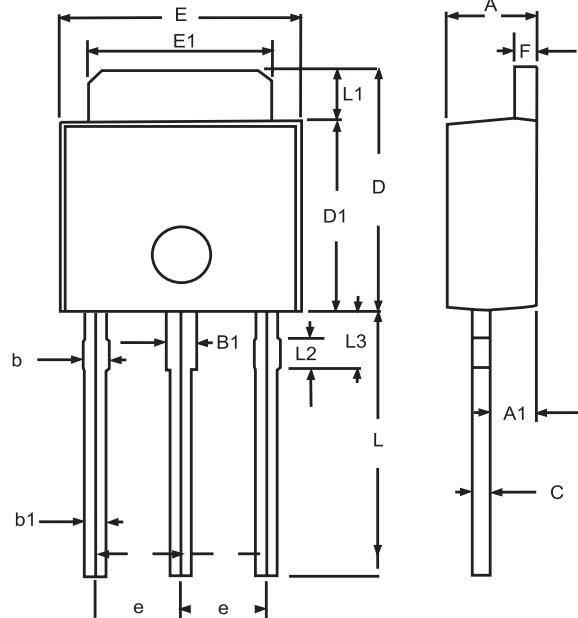


Figure 13. Normalized Thermal Transient Impedance Curve

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## PACKAGE OUTLINE DIMENSIONS

TO-251

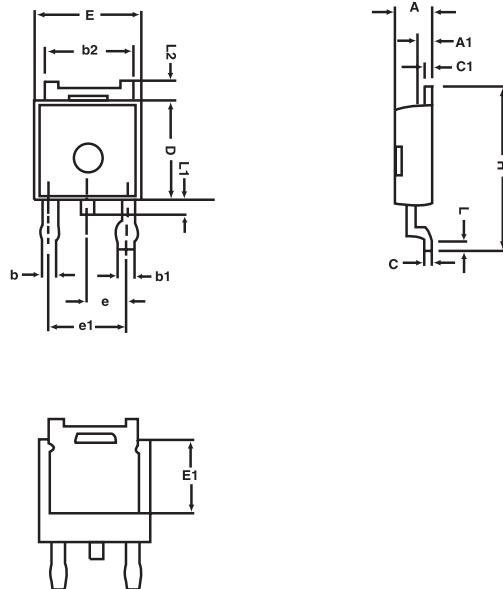


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.20	2.40	0.087	0.095
A1	1.100	1.300	0.043	0.051
B1	0.650	1.050	0.026	0.041
b	0.500	0.900	0.020	0.035
b1	0.400	0.800	0.016	0.32
C	0.400	0.600	0.016	0.024
D	6.700	7.300	0.264	0.287
D1	5.400	5.650	0.213	0.222
E	6.40	6.650	0.252	0.262
e	2.100	2.500	0.083	0.098
F	0.400	0.600	0.016	0.024
L	7.000	8.000	0.276	0.315
L1	1.300	1.700	0.051	0.067
L2	0.700	0.900	0.028	0.035
L3	1.400	1.800	0.055	0.071

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## PACKAGE OUTLINE DIMENSIONS

TO-252

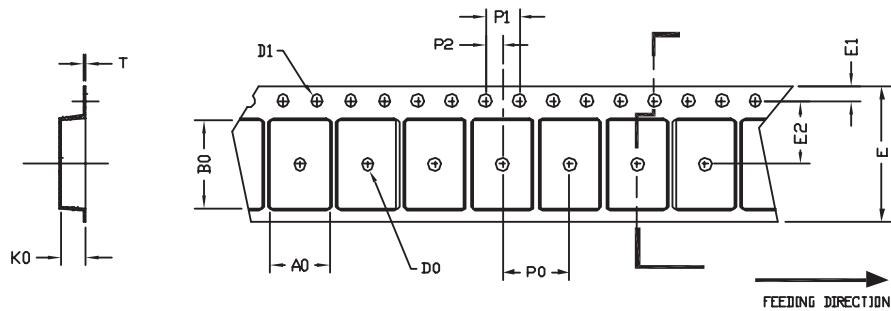


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.25	2.35	0.089	0.093
A1	0.95	1.05	0.037	0.041
b	0.77	0.85	0.030	0.033
b1	0.84	0.94	0.033	0.037
b2	5.30	5.45	0.209	0.215
C	0.49	0.53	0.019	0.021
D	6.00	6.20	0.236	0.244
E	6.40	6.60	0.252	0.260
E1	3.18	3.67	0.125	0.145
e	2.29	BSC	0.090	BSC
H	9.70	10.10	0.382	0.398
L	1.425	1.625	0.056	0.064
L1	0.650	0.850	0.026	0.033
L2	0.600	REF .	0.024	REF .

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## TO-252 Tape and Reel Data

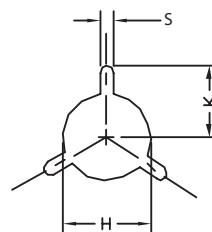
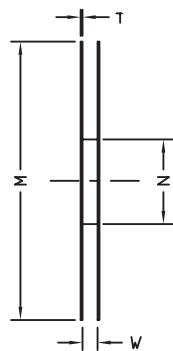
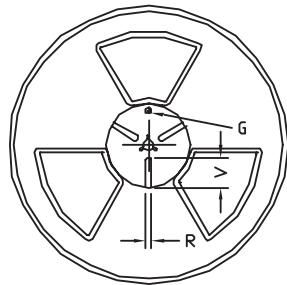
### TO-252 Carrier Tape



UNIT: b

PACKAGE	A0	B0	K0	D0	D1	E	E 1	E 2	P0	P1	P2	T
TO-252 (16 b I)	6.80 0.1	10.3 0.1	2.50 0.1	2.50 0.1	1.5 + 0.1 - 0	16.0 0.3	1.75 0.1	7.5 0.15	8.0 0.1	4.0 0.1	2.0 0.15	0.3 0.05

### TO-252 Reel



UNIT: b

TAPE SIZE	REEL SIZE	M	N	W	T	H	K	S	G	R	V
16 b	330	0.5	97	1.0	17.0 + 1.5 - 0	2.2	13.0 + 0.5 - 0.2	10.6	2.0 0.5	---	---