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SGTH

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SGS-THOMSON
MICROELECTRONICS

STH15N50/FI
STW15N50

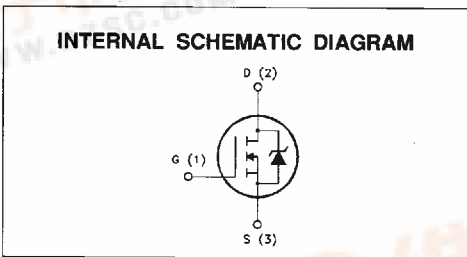
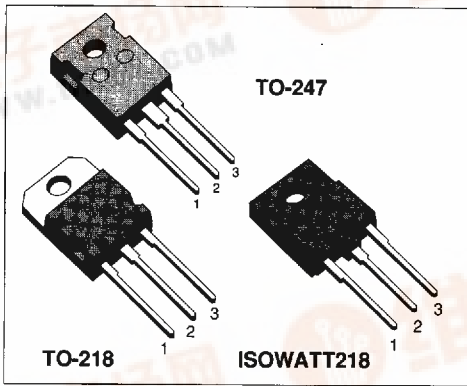
N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTOR

TYPE	V _{DSS}	R _{DS(on)}	I _D
STH15N50	500 V	< 0.4 Ω	15 A
STH15N50FI	500 V	< 0.4 Ω	9.3 A
STW15N50	500 V	< 0.4 Ω	15 A

- TYPICAL R_{DS(on)} = 0.34 Ω
- AVALANCHE RUGGED TECHNOLOGY
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- REDUCED GATE CHARGE
- APPLICATION ORIENTED CHARACTERIZATION

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- CHOPPER REGULATORS, CONVERTERS, MOTOR CONTROL, LIGHTING FOR INDUSTRIAL AND CONSUMER ENVIRONMENT



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STH/STW15N50	STH15N50FI	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	500		V
V _{DGR}	Drain- gate Voltage (R _{GS} = 20 kΩ)	500		V
V _{GS}	Gate-source Voltage	± 20		V
I _D	Drain Current (continuous) at T _c = 25 °C	15	9.3	A
I _D	Drain Current (continuous) at T _c = 100 °C	9.4	5.8	A
I _{DM} (*)	Drain Current (pulsed)	60	60	A
P _{tot}	Total Dissipation at T _c = 25 °C	180	70	W
	Derating Factor	1.44	0.56	W/°C
	Insulation Withstand Voltage (DC)	—	4000	V
	Storage Temperature	-65 to 150		°C
	Max. Operating Junction Temperature	150		°C

(*) Pulse width limited by safe operating area

THERMAL DATA

			TO-218/TO-247	ISOWATT218	
R _{thj-case}	Thermal Resistance Junction-case	Max	0.69	1.78	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient	Max	30		°C/W
R _{thc-sink}	Thermal Resistance Case-sink	Typ	0.1		°C/W
T _I	Maximum Lead Temperature For Soldering Purpose		300		°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max, δ < 1%)	15	A
E _{AS}	Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 25 V)	900	mJ
E _{AR}	Repetitive Avalanche Energy (pulse width limited by T _j max, δ < 1%)	30	mJ
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (T _c = 100 °C, pulse width limited by T _j max, δ < 1%)	9.4	A

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 250 μA V _{GS} = 0	500			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = Max Rating V _{DS} = Max Rating x 0.8 T _c = 125 °C			250 1000	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 20 V			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250 μA	2	3	4	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10V I _D = 7.5 A V _{GS} = 10V I _D = 7.5 A T _c = 100°C		0.34	0.4 0.8	Ω Ω
I _{D(on)}	On State Drain Current	V _{DS} > I _{D(on)} x R _{DS(on)max} V _{GS} = 10 V	15			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g _{fs} (*)	Forward Transconductance	V _{DS} > I _{D(on)} x R _{DS(on)max} I _D = 7.5 A	8.5	11.2		S
C _{iss}	Input Capacitance	V _{DS} = 25 V f = 1 MHz V _{GS} = 0		2350	3000	pF
C _{oss}	Output Capacitance			340	440	pF
C _{rss}	Reverse Transfer Capacitance			75	100	pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	$V_{DD} = 210\text{ V}$ $I_D = 7\text{ A}$		60	80	ns
t_r	Rise Time	$R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 3)		55	70	ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 400\text{ V}$ $I_D = 15\text{ A}$ $R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 5)		270		A/ μ s
Q_g	Total Gate Charge	$V_{DD} = 400\text{ V}$ $I_D = 15\text{ A}$ $V_{GS} = 10\text{ V}$		90	110	nC
Q_{gs}	Gate-Source Charge			12		nC
Q_{gd}	Gate-Drain Charge			48		nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$	Off-voltage Rise Time	$V_{DD} = 400\text{ V}$ $I_D = 15\text{ A}$		35	45	ns
t_f	Fall Time	$R_G = 4.7\ \Omega$ $V_{GS} = 10\text{ V}$		45	60	ns
t_c	Cross-over Time	(see test circuit, figure 5)		65	85	ns

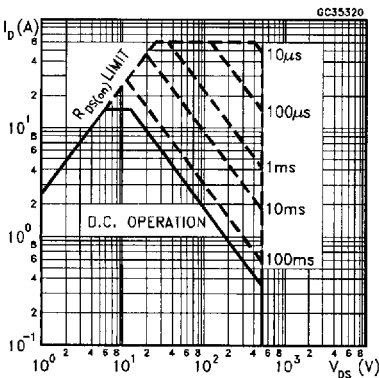
SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain Current				15	A
$I_{SDM}(\bullet)$	Source-drain Current (pulsed)				60	A
$V_{SD}(\ast)$	Forward On Voltage	$I_{SD} = 15\text{ A}$ $V_{GS} = 0$			2	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 15\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 100\text{ V}$ $T_j = 150\text{ }^\circ\text{C}$ (see test circuit, figure 5)		630		ns
Q_{rr}	Reverse Recovery Charge			10.7		μ C
I_{RRM}	Reverse Recovery Current			34		A

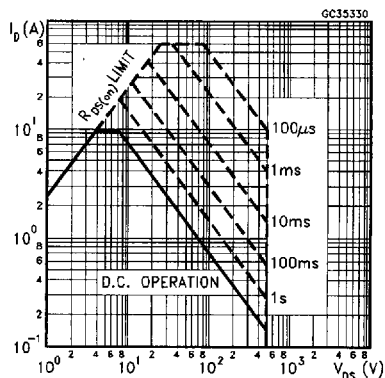
(*) Pulsed: Pulse duration = 300 μ s, duty cycle 1.5 %

(\bullet) Pulse width limited by safe operating area

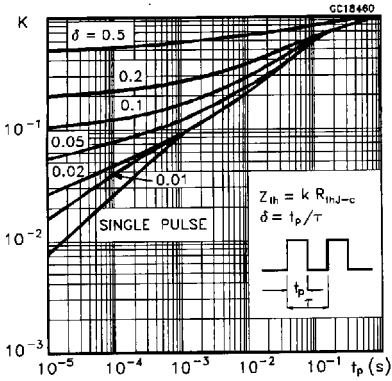
Safe Operating Areas For TO-218 and TO-247



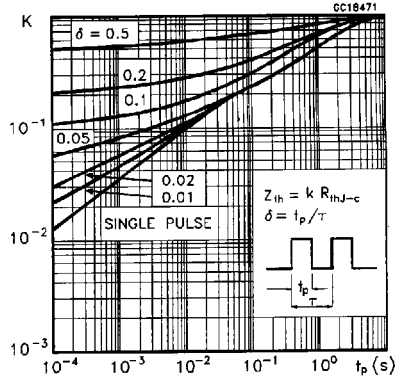
Safe Operating Areas For ISOWATT218



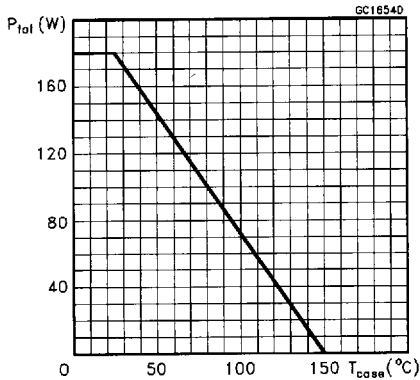
Thermal Impedance For TO-218 and TO-247



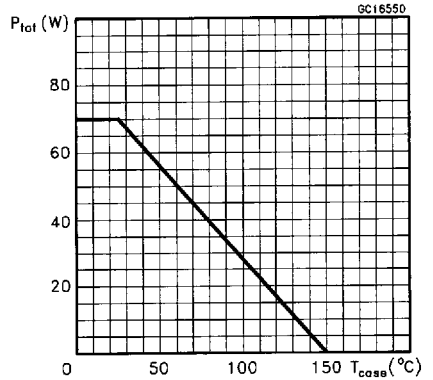
Thermal Impedance For ISOWATT218



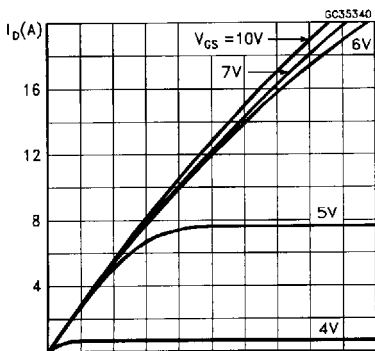
Derating Curve For TO-218 and TO-247



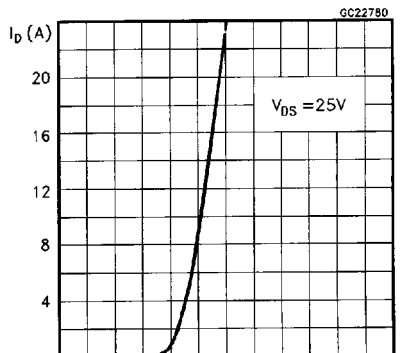
Derating Curve For ISOWATT218



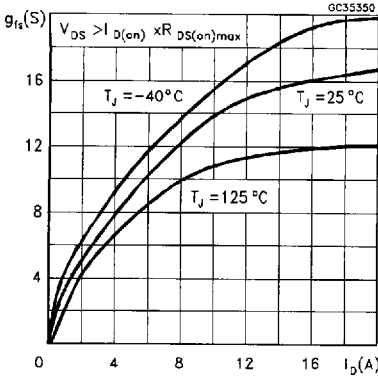
Output Characteristics



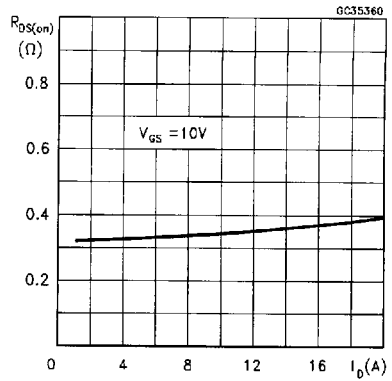
Transfer Characteristics



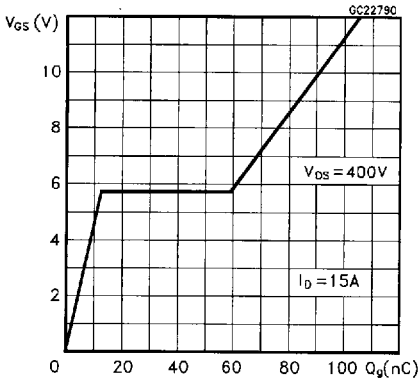
Transconductance



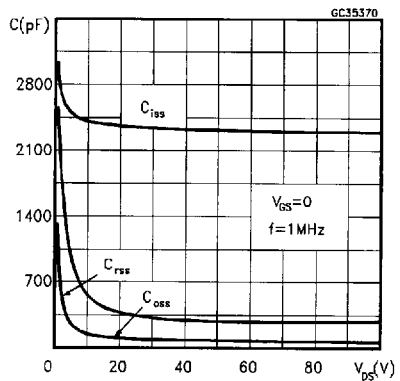
Static Drain-source On Resistance



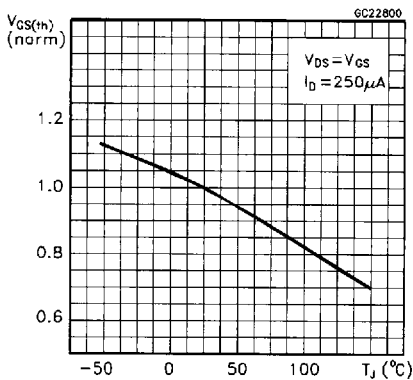
Gate Charge vs Gate-source Voltage



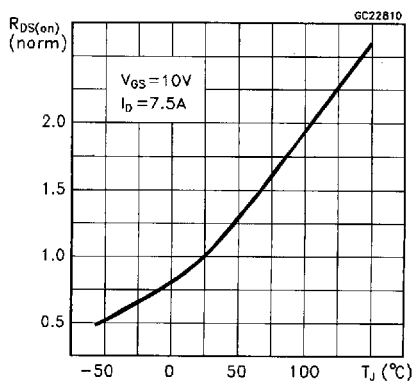
Capacitance Variations



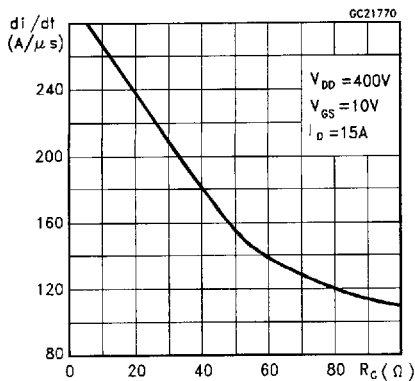
Normalized Gate Threshold Voltage vs Temperature



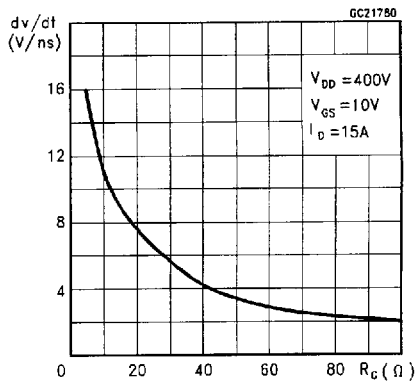
Normalized On Resistance vs Temperature



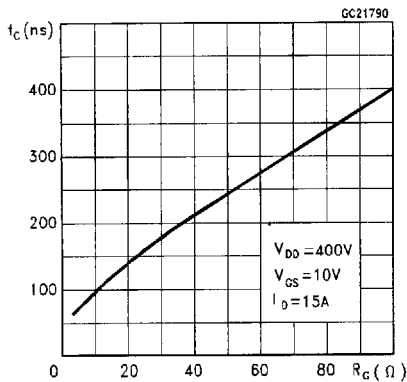
Turn-on Current Slope



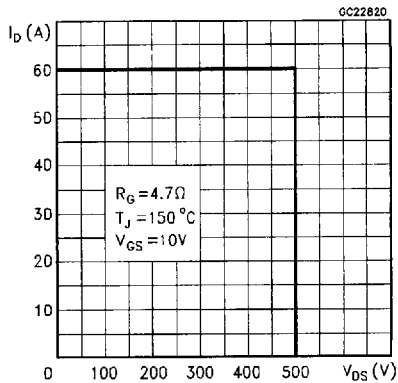
Turn-off Drain-source Voltage Slope



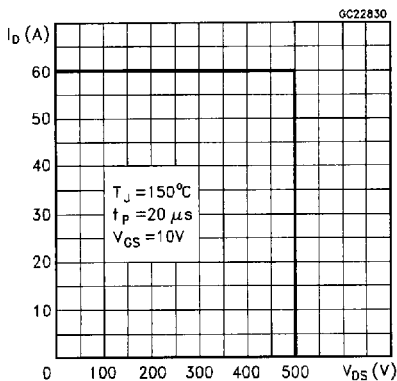
Cross-over Time



Switching Safe Operating Area



Accidental Overload Area



Source-drain Diode Forward Characteristics

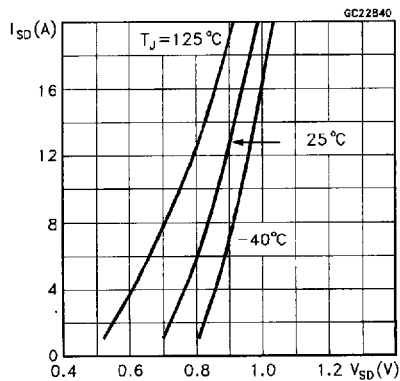


Fig. 1: Unclamped Inductive Load Test Circuits

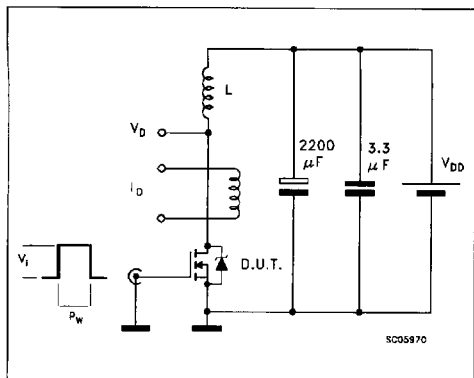


Fig. 2: Unclamped Inductive Waveforms

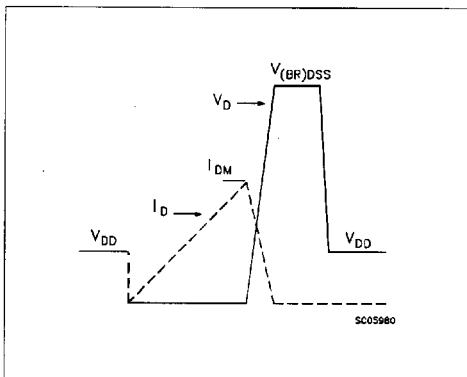


Fig. 3: Switching Times Test Circuits For Resistive Load

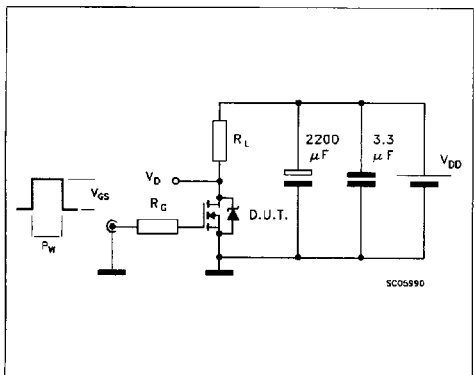


Fig. 4: Gate Charge Test Circuit

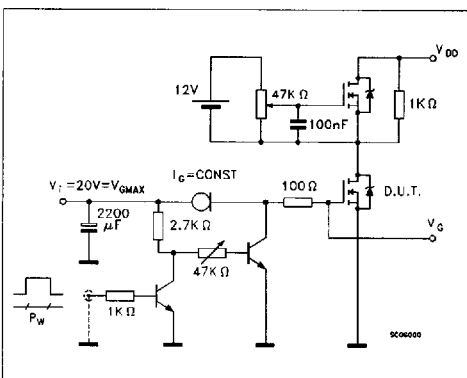


Fig. 5: Test Circuit For Inductive Load Switching And Diode Reverse Recovery Time

