

## **INCHANGE SEMICONDUCTOR**

# **Isc N-Channel MOSFET Transistor**

# STW45NM50

### FEATURES

- · Low input capacitance and gate charge
- · Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

• The MDmesh<sup>™</sup> family is very suitable for increasing power density of high voltage converters allowing system miniaturization and higher efficiencies.

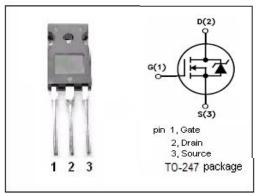
* ABSOLUTE MAXIMUM RATINGS(Ta=25 C)						
SYMBOL	PARAMETER	VALUE	UNIT			
V <sub>DSS</sub>	Drain-Source Voltage 550		V			
V <sub>GSS</sub>	Gate-Source Voltage ±30		V			
ID	Drain Current-Continuous@Tc=25°C 45   Tc=100°C 28.4		А			
I <sub>DM</sub>	Drain Current-Single Pulsed 180		А			
PD	Total Dissipation	417	W			
T <sub>ch</sub>	Max. Operating Junction Temperature 150		°C			
T <sub>stg</sub>	Storage Temperature	-65~150	°C			

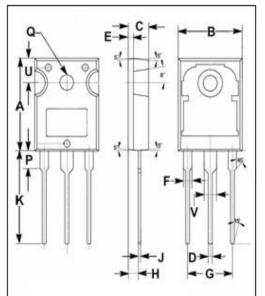
### • ABSOLUTE MAXIMUM RATINGS(T<sub>a</sub>=25℃)

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER		UNIT	
Rth(ch-c)	Channel-to-case thermal resistance 0.3		°C/W	
Rth(ch-a)	Channel-to-ambient thermal resistance	30	°C/W	

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### **ELECTRICAL CHARACTERISTICS**

 $T_{C}\text{=}25^{\circ}\!\!\!\mathrm{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 0.25mA	500			V
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>DS</sub> =±30V; I <sub>D</sub> =0.25mA	3		5	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> =22.5A		80	100	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±30V;V <sub>DS</sub> =0V			±0.1	μA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 550V; V <sub>GS</sub> = 0V; TJ=25℃ TJ=125℃			10 100	μA
V <sub>SDF</sub>	Diode forward voltage	I <sub>SD</sub> =45A, V <sub>GS</sub> = 0 V			1.5	v

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