

Vishay Siliconix

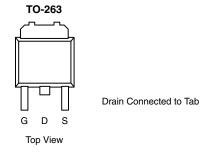
P-Channel 80-V (D-S) MOSFET

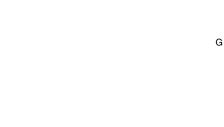
PRODUCT SUMMARY					
V _{DS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A) ^b	Q _g (Typ)		
- 80	0.0111 at V _{GS} = - 10 V	- 110	113 nC		

FEATURES

• TrenchFET® Power MOSFET







Ordering Information: SUM110P08-11 (Lead (Pb)-free)

P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 80	V		
Gate-Source Voltage	V _{GS}	± 20			
	T _C = 25 °C		110 ^a		
Continuous Drain Current (T = 150 °C)	T _C = 125 °C		71		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	23.5 ^{b, c}		
	T _A = 125 °C		13.6 ^{b, c}		
Pulsed Drain Current		I _{DM}	- 120	Α	
0 11 0 0 0 1	T _C = 25 °C		110 ^a		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 9 ^{b, c}		
Avalanche Current	L = 0.1 mH	I _{AS}	- 75		
Single-Pulse Avalanche Energy	L = U.1 Min	E _{AS}	281	mJ	
	T _C = 25 °C		375		
Maximum Power Dissipation	T _C = 125 °C	P _D	125	w	
	T _A = 25 °C	LD	13.6 ^{b, c}	vv	
	T _A = 125 °C		4.5 ^{b, c}		
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 sec	R_{thJA}	8	11	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	0.33	0.4	C/VV		

a. Package limited.b. Surface Mounted on 1" x 1" FR4 board.

d. Maximum under Steady State conditions is °C/W.

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Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static	-					
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 80			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250 A		- 85		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		7.0		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 2		- 4	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 80 V, V _{GS} = 0 V			- 1	
		V _{DS} = - 80 V, V _{GS} = 0 V, T _J = 175 °C			- 500	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = -10 \text{ V}$	120			Α
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		0.092	0.0111	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 20 A		80		S
Dynamic ^b				•		
Input Capacitance	C _{iss}			11500		pF
Output Capacitance	C _{oss}	V _{DS} = - 40 V, V _{GS} = 0 V, f = 1 MHz		790		
Reverse Transfer Capacitance	C _{rss}			700		
Total Gate Charge	Qg			185	280	nC
Gate-Source Charge	Q_{gs}	V _{DS} = - 40 V, V _{GS} = - 10 V, I _D = - 110 A		40		
Gate-Drain Charge	Q _{gd}			45		
Gate Resistance	R_{g}	f = 1 MHz		3.6		Ω
Turn-On Delay Time	t _{d(on)}			25	40	ns ns
Rise Time	t _r	$V_{DD} = -40 \text{ V}, R_{L} = 0.36 \Omega$		410	620	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 110 A, V_{GEN} = - 10 V, R_g = 1 Ω		145	220	
Fall Time	t _f			470	710	
Drain-Source Body Diode Characteristic	s			•		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 110	Α
Pulse Diode Forward Current ^a	I _{SM}				- 120	
Body Diode Voltage	V_{SD}	I _S = - 20 A		- 0.8	- 1.5	V
Body Diode Reverse Recovery Time	t _{rr}			65	100	ns
Body Diode Reverse Recovery Charge	Q _{rr}	a I _F = -20 A, di/dt = 100 A/μs, 1 _J = 25 °C		135	205	nC
Reverse Recovery Fall Time	t _a			43		ns
Reverse Recovery Rise Time	t _b			22		

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

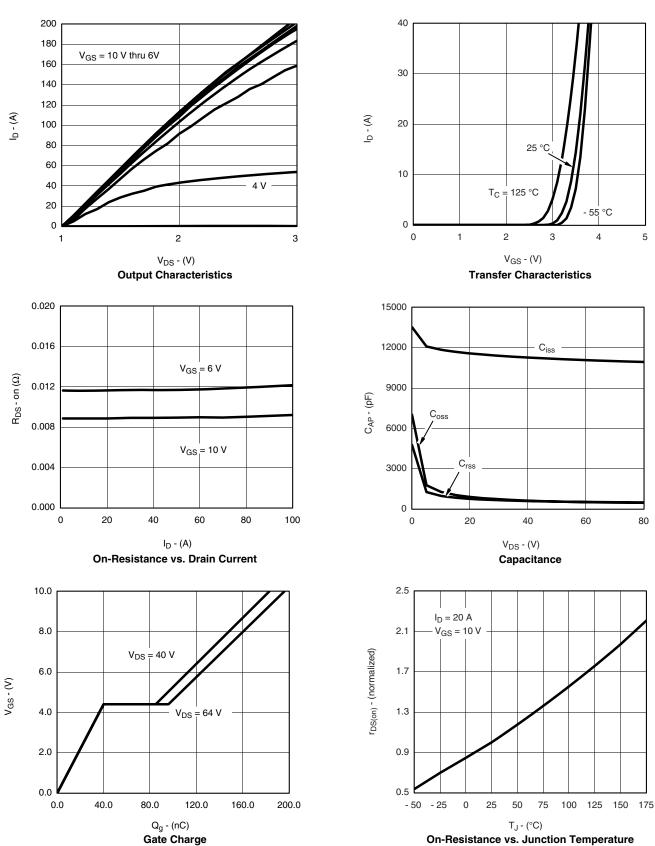
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

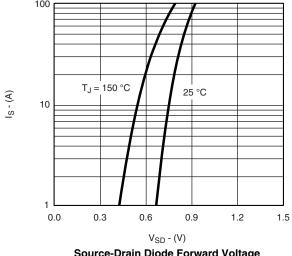


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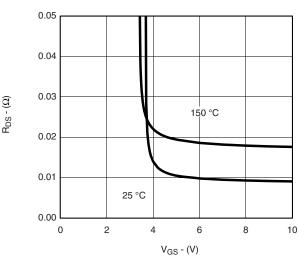
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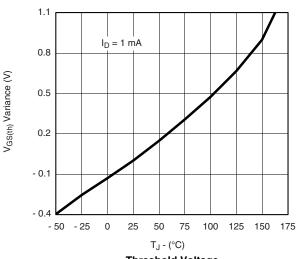




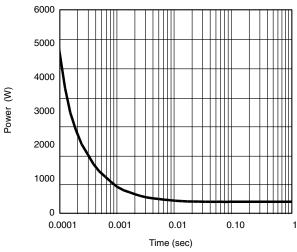
Source-Drain Diode Forward Voltage



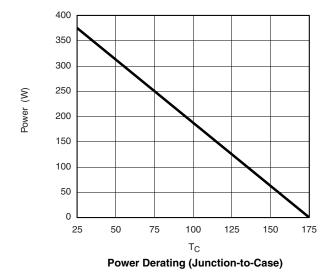
On-Resistance vs. Gate-to-Source Voltage

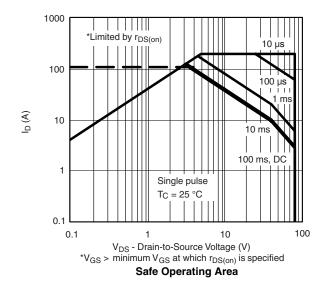


Threshold Voltage



Single Pulse Power, Junction-to-Case ($T_C = 25$ °C)

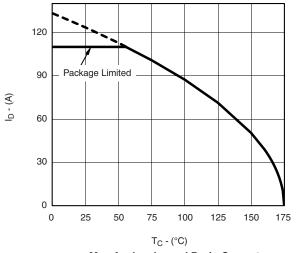


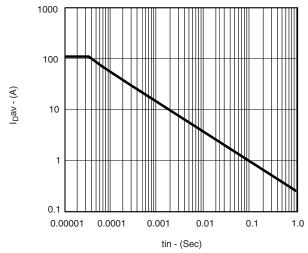




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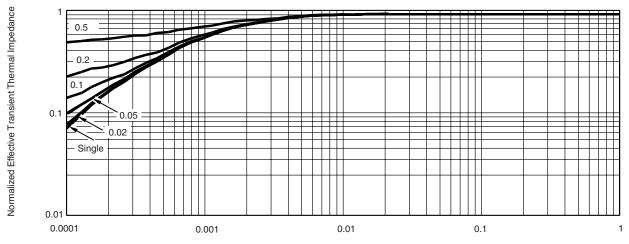
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Max Avalanche and Drain Current vs. Case Temperature

Avalanche Current vs. Time



Normalized Thermal Transient Impedance, Junction-to-Case

*The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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