

# N-Channel 150 V (D-S) 175 °C MOSFET

## PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
150	0.073 at $V_{GS} = 10$ V	23
	0.077 at $V_{GS} = 6$ V	22.5

## FEATURES

- TrenchFET® Power MOSFETS
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC

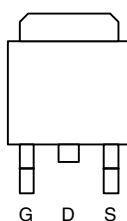


**RoHS**  
COMPLIANT

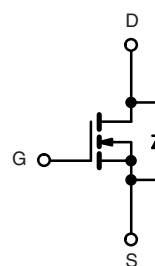
## APPLICATIONS

- Primary Side Switch

TO-263



Top View



N-Channel MOSFET

Ordering Information: SUM23N15-73-E3 (Lead (Pb) free)

## ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	150	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175$ °C)	$I_D$	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Pulsed Drain Current	$I_{DM}$	35	A
Avalanche Current	$I_{AR}$	25	
Repetitive Avalanche Energy <sup>a</sup>	$E_{AR}$	31	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_C = 25$ °C	W
		$T_A = 25$ °C <sup>c</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175	°C

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	$R_{thJA}$	40	°C/W
Junction-to-Case (Drain)	$R_{thJC}$	1.5	

Notes:

a. Duty cycle  $\leq 1$  %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	150			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	35			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A		0.059	0.073	Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C			0.140	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 175 °C			0.168	
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 10 A		0.062	0.077	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 25 A	10			S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		1290		pF
Output Capacitance	C <sub>oss</sub>			160		
Reverse Transfer Capacitance	C <sub>rss</sub>			70		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 23 A		22	35	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			6		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			7.5		
Gate Resistance	R <sub>G</sub>			4		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 75 V, R <sub>L</sub> = 3.26 Ω I <sub>D</sub> ≅ 23 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 2.5 Ω		10	15	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			60	90	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			30	43	
Fall Time <sup>c</sup>	t <sub>f</sub>			45	70	
Source-Drain Diode Ratings and Characteristics (T <sub>C</sub> = 25 °C) <sup>b</sup>						
Continuous Current	I <sub>S</sub>				35	A
Pulsed Current	I <sub>SM</sub>				23	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 23 A, V <sub>GS</sub> = 0 V		1	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 23 A, dI/dt = 100 A/μs		100	150	ns
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>			5	8	A
Reverse Recovery Charge	Q <sub>rr</sub>				0.25	0.6

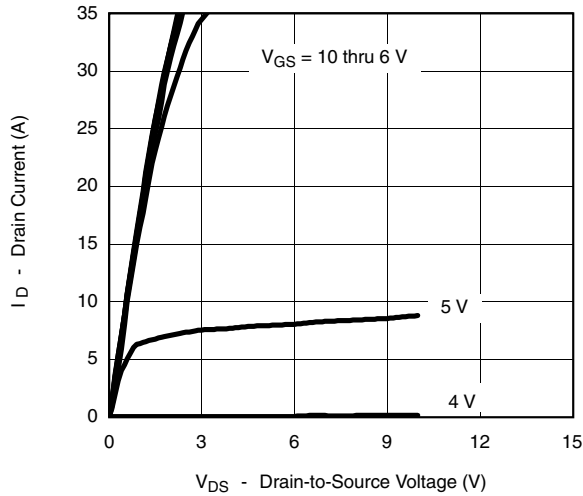
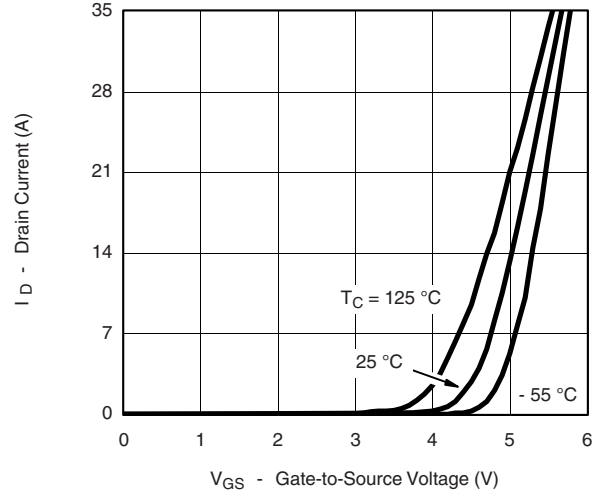
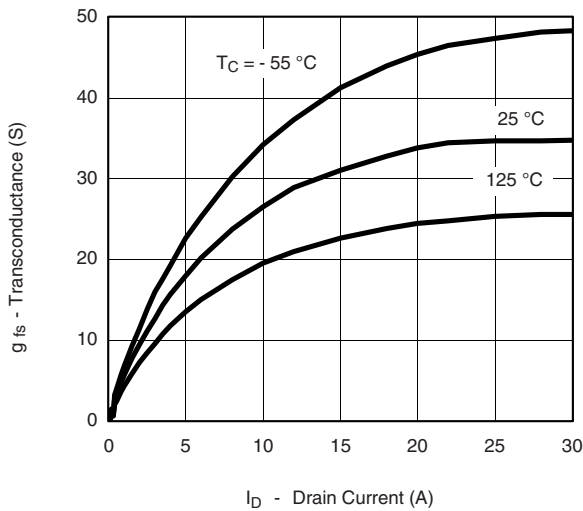
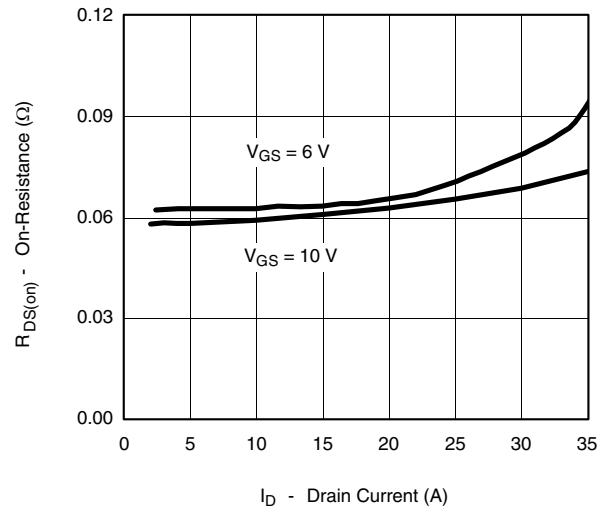
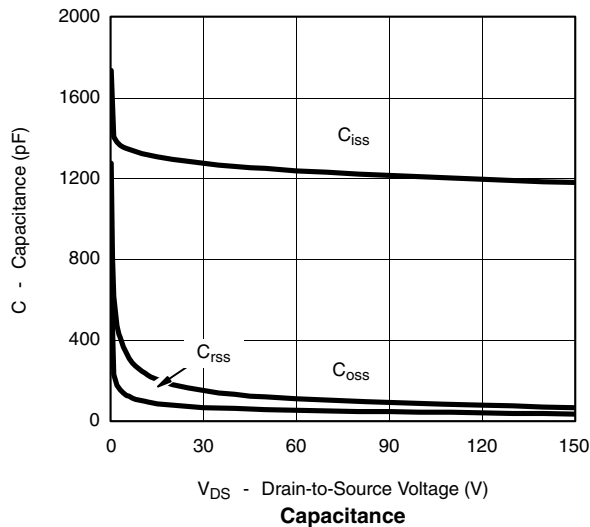
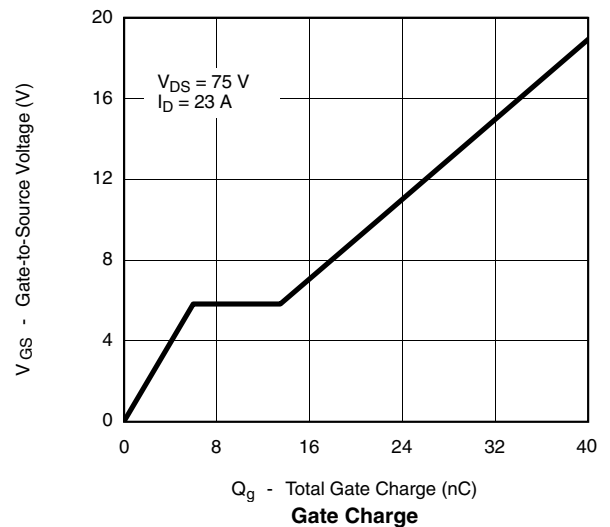
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

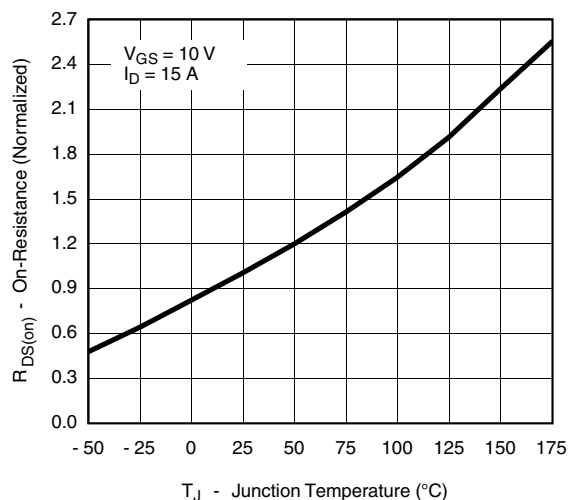
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

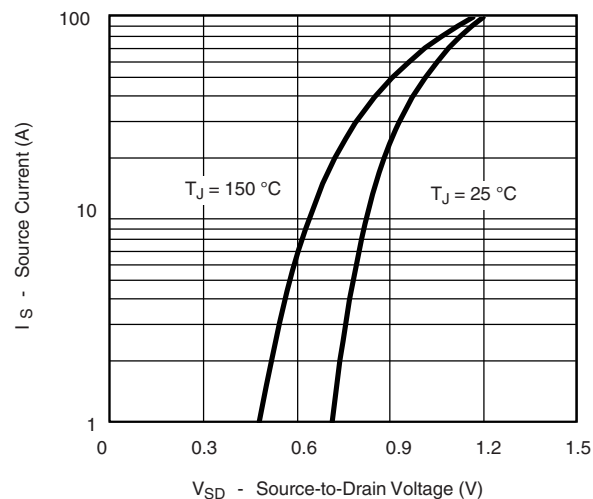
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** (25 °C unless noted)

**Output Characteristics**

**Transfer Characteristics**

**Transconductance**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

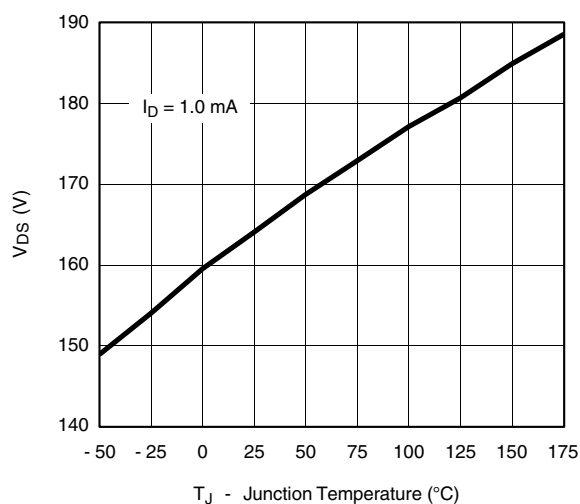
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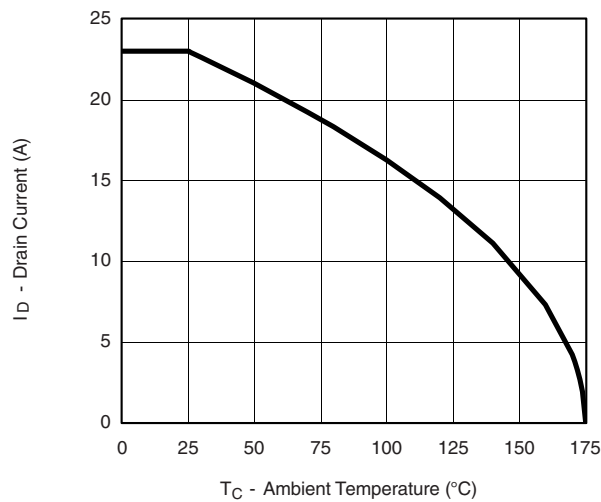
On-Resistance vs. Junction Temperature



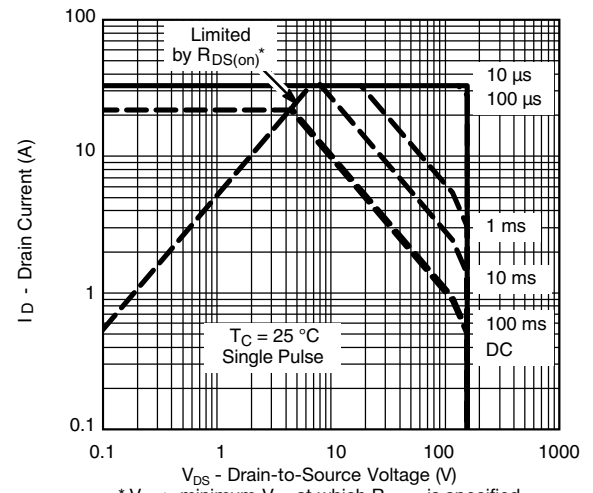
Source-Drain Diode Forward Voltage



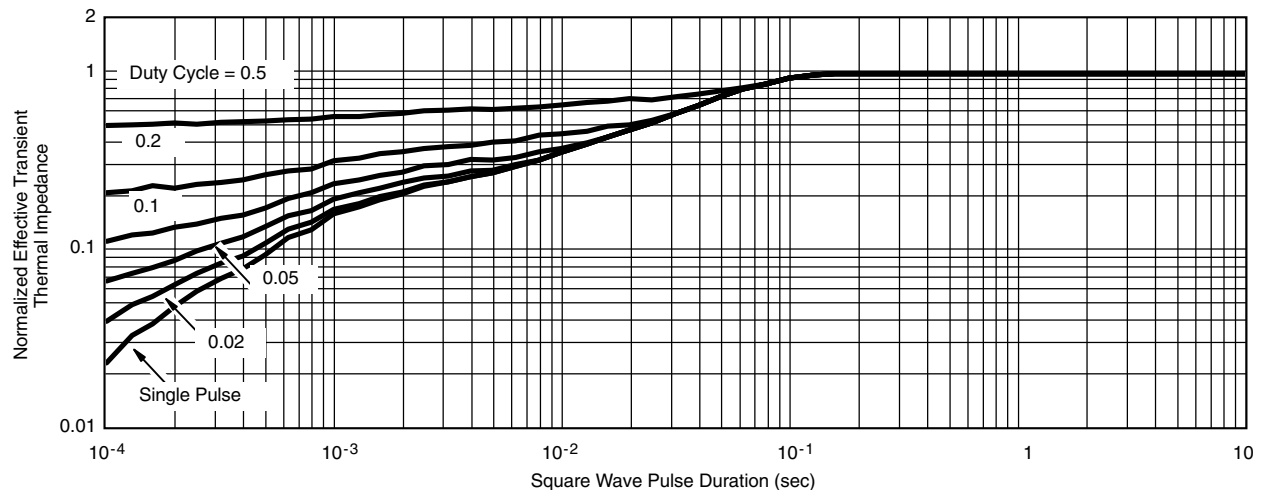
Drain Source Breakdown vs. Junction Temperature

**THERMAL RATINGS**

**Maximum Avalanche and Drain Current  
vs. Case Temperature**



**Safe Operating Area**  
\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



**Normalized Thermal Transient Impedance, Junction-to-Case**

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