N-Channel 100-V (D-S) 175°C MOSFET

PRODUCT SUMMARY		
$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)
100	0.030 @ V_{GS} = 10 V	40
	0.034 @ V_{GS} = 6 V	37.5

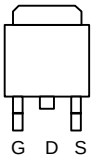
FEATURES

- TrenchFET® Power MOSFETS
- 175°C Junction Temperature
- New Low Thermal Resistance Package

APPLICATIONS

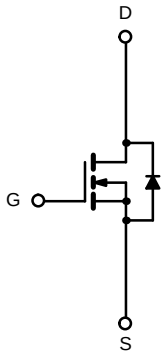
- Automotive
  - Motor Drives
  - 12-V Switches

TO-263



Top View

Ordering Information: SUM40N10-30



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current (T_J = 175°C)	T_C = 25°C	I_D	40	A
	T_C = 125°C		23	
Pulsed Drain Current		I_{DM}	75	
Avalanche Current		I_{AR}	35	
Repetitive Avalanche Energy[a]	L = 0.1 mH	E_{AR}	61	mJ
Maximum Power Dissipation[a]	T_C = 25°C	P_D	107[b]	W
	T_A = 25°C[c]		3.75	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount)[c]	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	1.4	

Notes
a. Duty cycle ≤ 1%.
b. See SOA curve for voltage derating.
c. When mounted on 1" square PCB (FR-4 material).

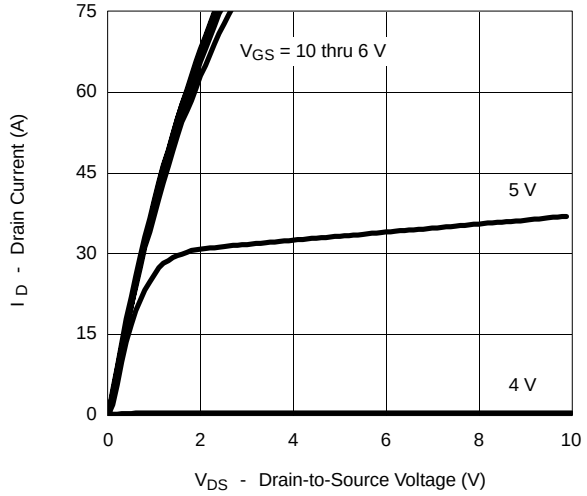
SPECIFICATIONS (T_J =25°C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{DS} = 0 V, I_D = 250 μA	100			V
Gate-Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 μA	2		4	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 80 V, V_{GS} = 0 V			1	μA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 125°C			50	
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 175°C			250	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 10 V	75			A
Drain-Source On-State Resistance[a]	$r_{DS(on)}$	V_{GS} = 10 V, I_D = 15 A		0.024	0.030	Ω
		V_{GS} = 6 V, I_D = 10 A		0.026	0.034	
		V_{GS} = 10 V, I_D = 15 A, T_J = 125°C			0.054	
		V_{GS} = 10 V, I_D = 15 A, T_J = 175°C			0.067	
Forward Transconductance[a]	g_{fs}	V_{DS} = 15 V, I_D = 15 A	10			S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		2400		pF
Output Capacitance	C_{oss}			270		
Reverse Transfer Capacitance	C_{rss}			90		
Total Gate Charge[c]	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 40 A		35	60	nC
Gate-Source Charge[c]	Q_{gs}			11		
Gate-Drain Charge[c]	Q_{gd}			9		
Gate Resistance	R_G			1.7		Ω
Turn-On Delay Time[c]	$t_{d(on)}$	V_{DD} = 50 V, R_L = 1.25 Ω I_D ≅ 40 A, V_{GEN} = 10 V, R_G = 2.5 Ω		11	20	ns
Rise Time[c]	t_r			12	20	
Turn-Off Delay Time[c]	$t_{d(off)}$			30	45	
Fall Time[c]	t_f			12	20	
Source-Drain Diode Ratings and Characteristics (T_C = 25°C)[b]						
Continuous Current	I_S				40	A
Pulsed Current	I_{SM}				75	
Forward Voltage[a]	V_{SD}	I_F = 30 A, V_{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t_{rr}	I_F = 30 A, di/dt = 100 A/μs		60	100	ns
Peak Reverse Recovery Current	$I_{RM(REC)}$			5	8	A
Reverse Recovery Charge	Q_{rr}			0.15	0.4	μC

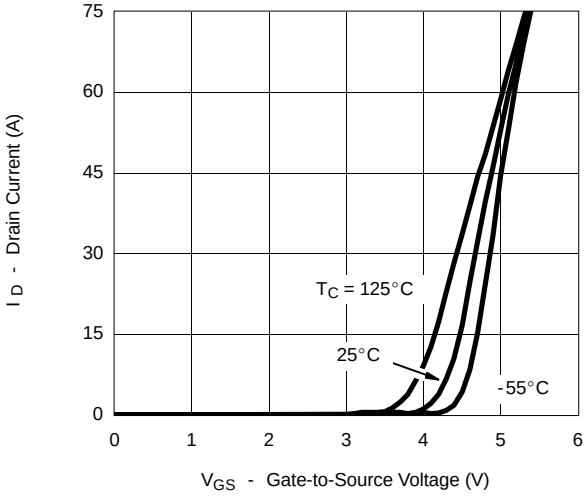
Notes
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

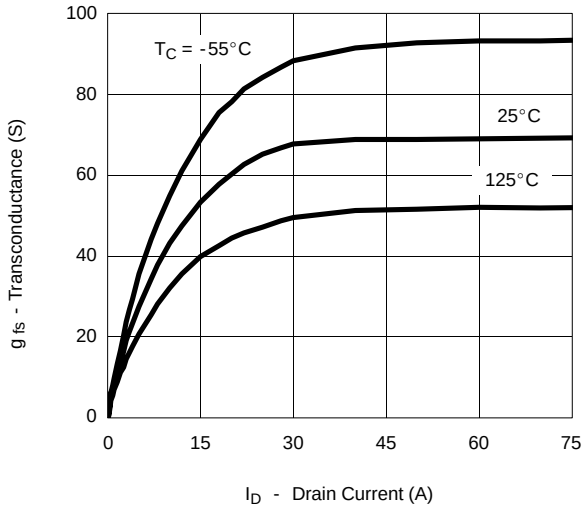
Output Characteristics



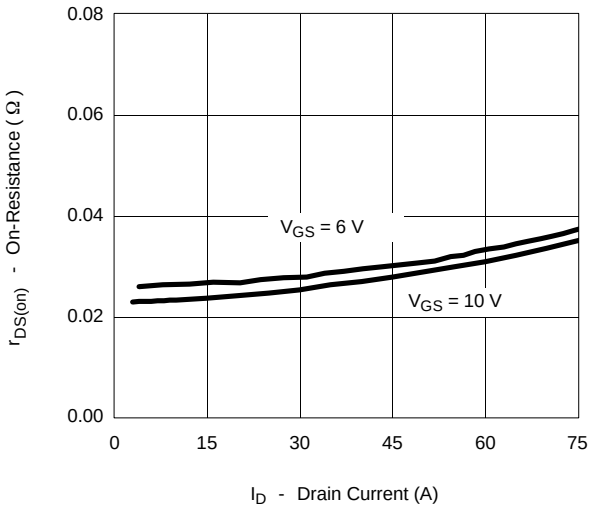
Transfer Characteristics



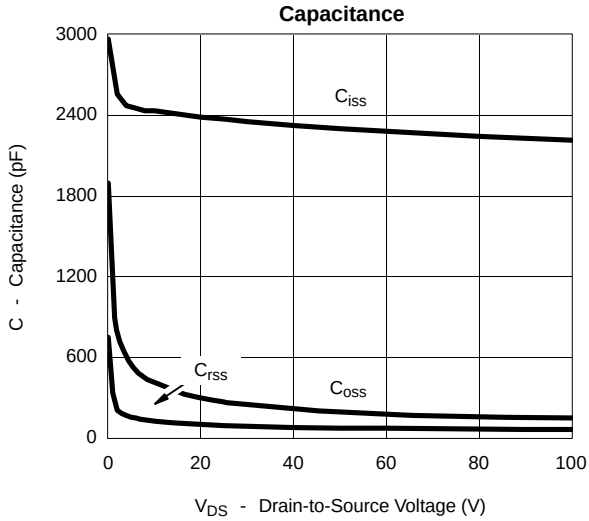
Transconductance



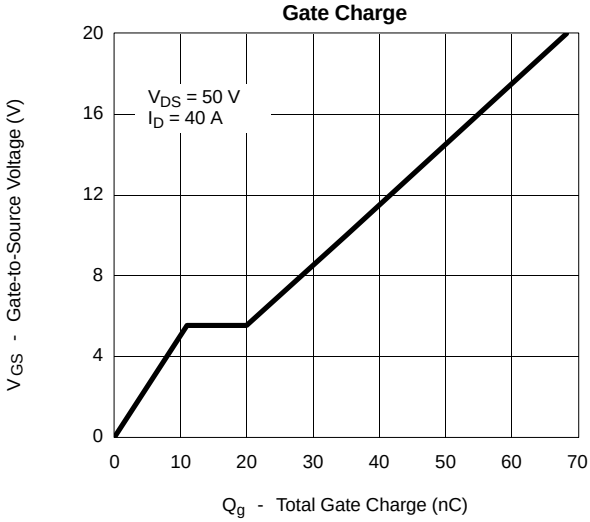
On-Resistance vs. Drain Current



Capacitance

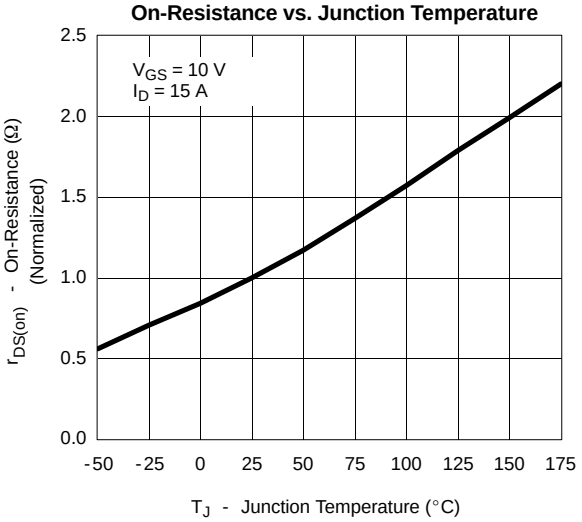


Gate Charge

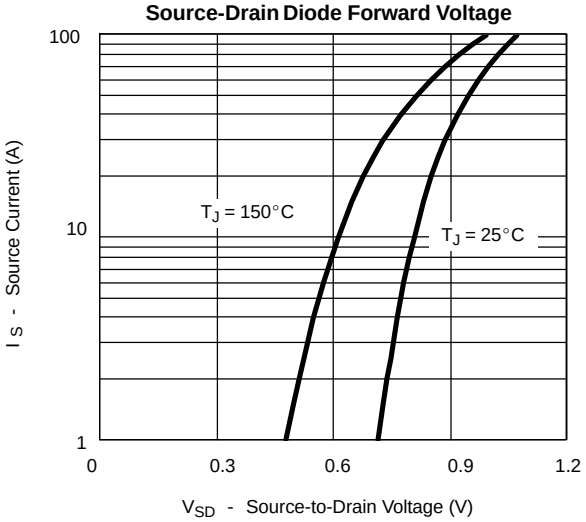


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

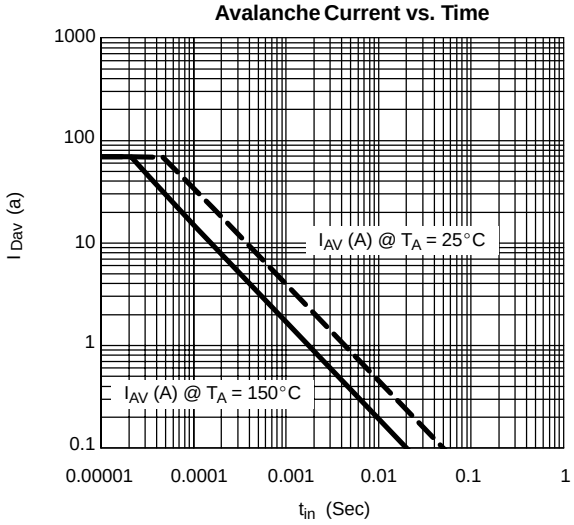
On-Resistance vs. Junction Temperature



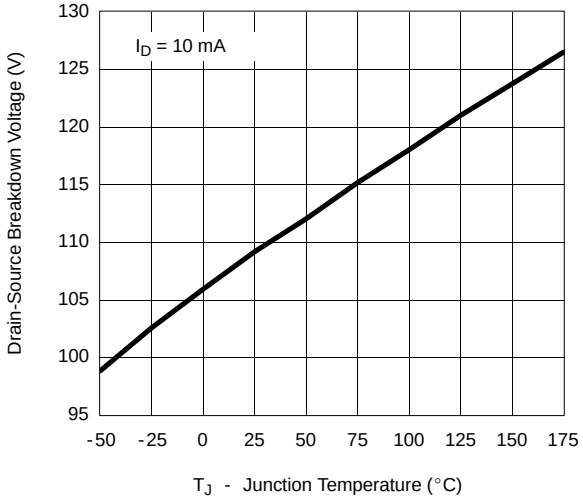
Source-Drain Diode Forward Voltage



Avalanche Current vs. Time

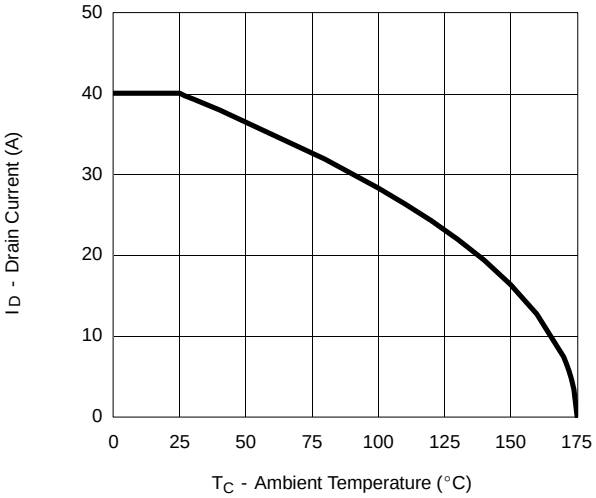


Drain-Source Breakdown Voltage vs. Junction Temperature

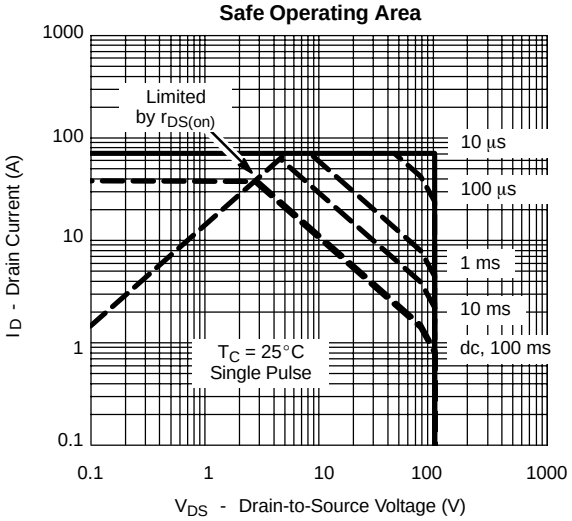


THERMAL RATINGS

Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case