



N-Channel 150-V (D-S) 175°C MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)
150	0.020 @ $V_{GS} = 10$ V	80
	0.022 @ $V_{GS} = 4.5$ V	76

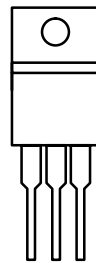
FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- PWM Optimized With Low Threshold

APPLICATIONS

- Primary Side Switch

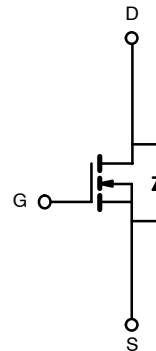
TO-220AB



G D S

Top View

DRAIN connected to TAB



Ordering Information: SUP80N15-20L

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ($T_J = 175^\circ\text{C}$)	$T_C = 25^\circ\text{C}$	I_D	80	A
	$T_C = 125^\circ\text{C}$		46	
Pulsed Drain Current		I_{DM}	180	
Avalanche Current		I_{AR}	45	
Repetitive Avalanche Energy ^a	$L = 0.1$ mH	E_{AR}	101	mJ
Maximum Power Dissipation ^a	$T_C = 25^\circ\text{C}$	P_D	300 ^b	W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (Free Air)	R_{thJA}	62.5	$^\circ\text{C/W}$
Junction-to-Case	R_{thJC}	0.5	

Notes

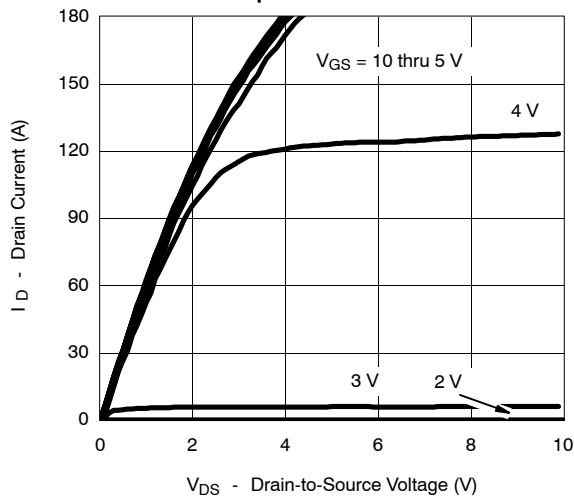
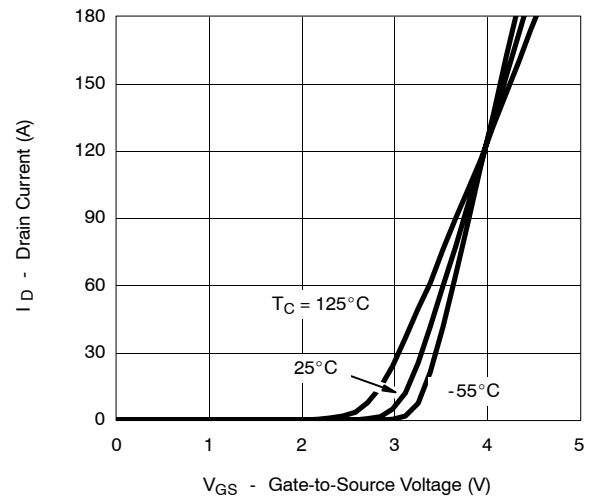
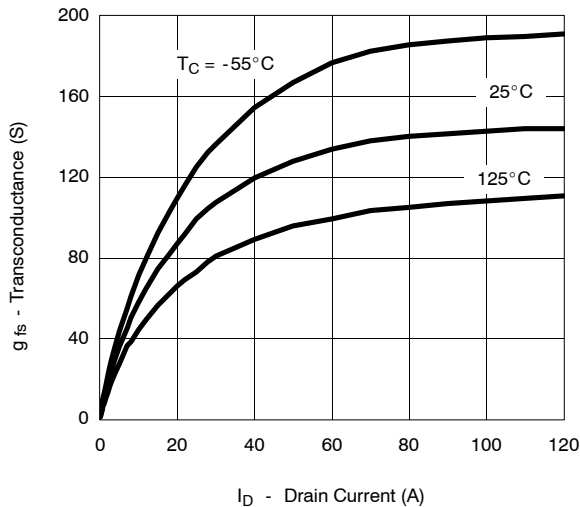
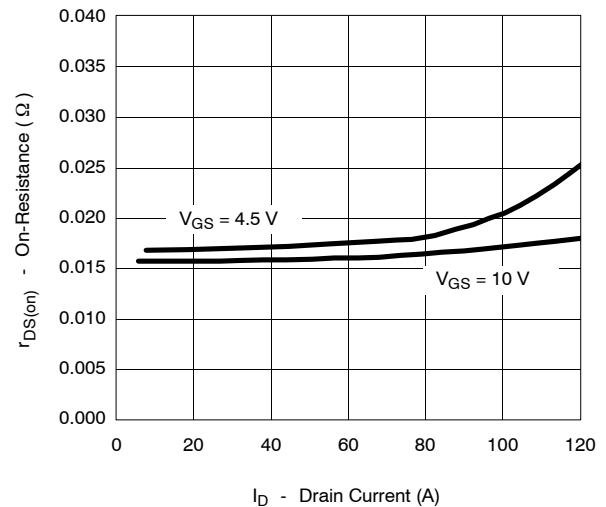
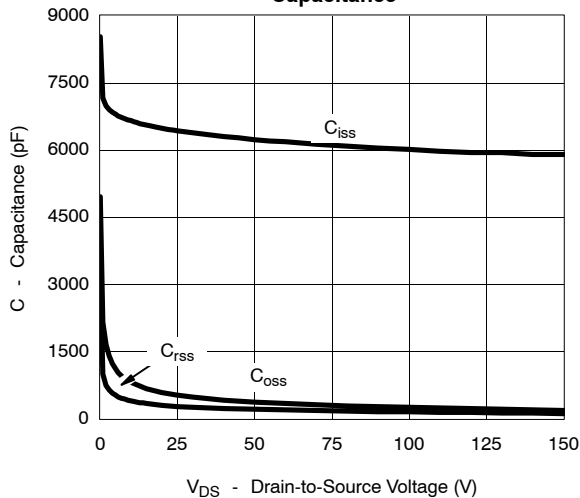
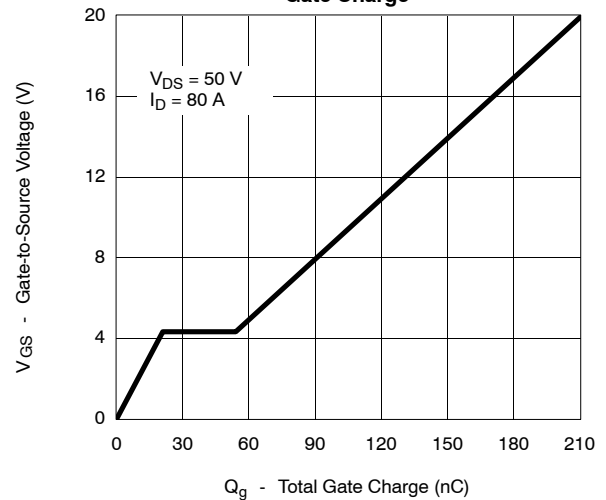
- a. Duty cycle $\leq 1\%$.
b. See SOA curve for voltage derating.

SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

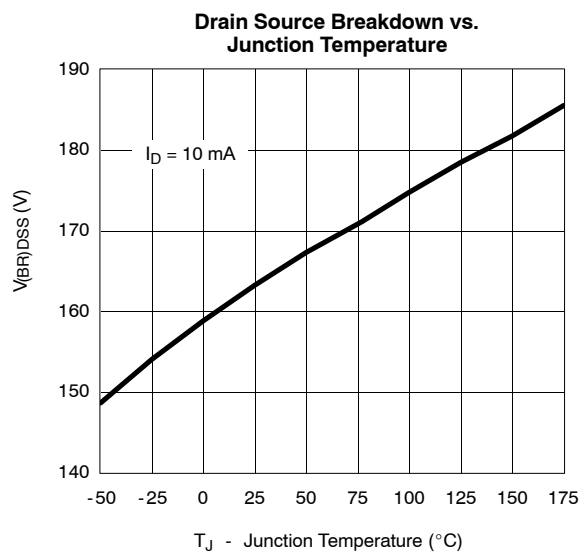
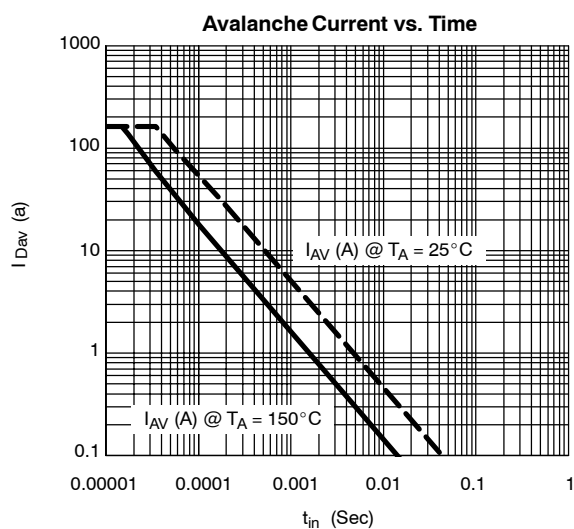
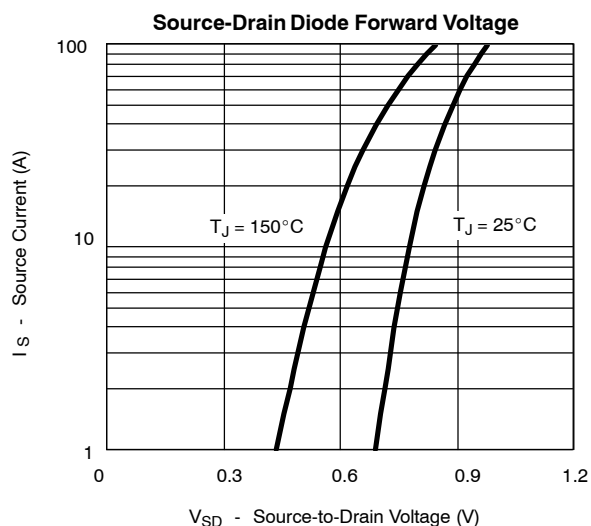
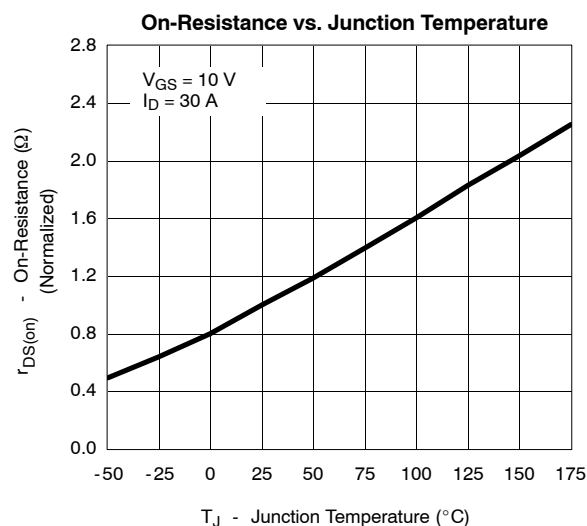
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	150			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 120 V, V _{GS} = 0 V			1	μA
		V _{DS} = 120 V, V _{GS} = 0 V, T _J = 125°C			50	
		V _{DS} = 120 V, V _{GS} = 0 V, T _J = 175°C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	120			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 30 A		0.016	0.020	Ω
		V _{GS} = 4.5 V, I _D = 20 A		0.017	0.022	
		V _{GS} = 10 V, I _D = 30 A, T _J = 125°C			0.037	
		V _{GS} = 10 V, I _D = 30 A, T _J = 175°C			0.046	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 30 A	25			S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		6500		pF
Output Capacitance	C _{oss}			520		
Reverse Transfer Capacitance	C _{rss}			270		
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 80 A		110	165	nC
Gate-Source Charge ^c	Q _{gs}			21		
Gate-Drain Charge ^c	Q _{gd}			33		
Gate Resistance	R _g			1.6		Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, R _L = 0.93 Ω I _D ≅ 80 A, V _{GEN} = 10 V, R _G = 2.5 Ω		20	30	ns
Rise Time ^c	t _r			100	150	
Turn-Off Delay Time ^c	t _{d(off)}			70	105	
Fall Time ^c	t _f			135	200	
Source-Drain Diode Ratings and Characteristics (T _C = 25°C) ^b						
Continuous Current	I _S					A
Pulsed Current	I _{SM}				180	
Forward Voltage ^a	V _{SD}	I _F = 80 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 50 A, di/dt = 100 A/μs		120	180	ns
Peak Reverse Recovery Current	I _{RM(REC)}			6	9	A
Reverse Recovery Charge	Q _{rr}			0.35	0.81	μC

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)****Output Characteristics****Transfer Characteristics****Transconductance****On-Resistance vs. Drain Current****Capacitance****Gate Charge**

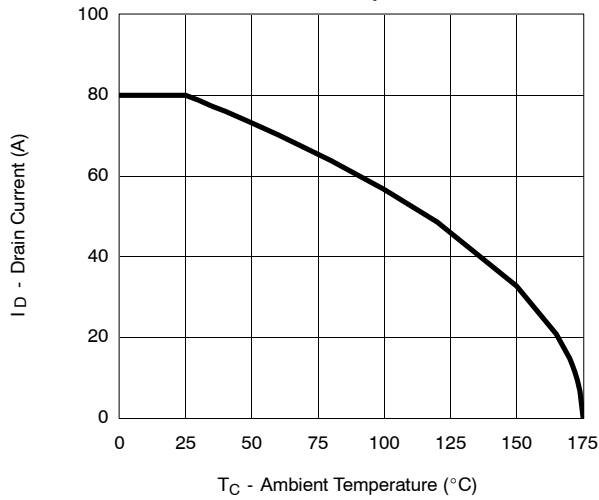
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



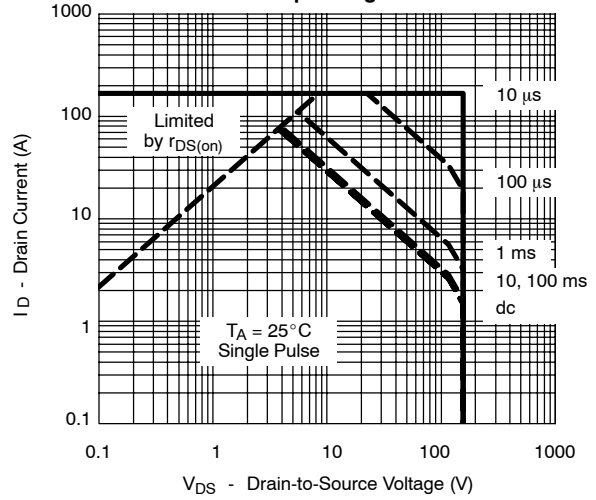


THERMAL RATINGS

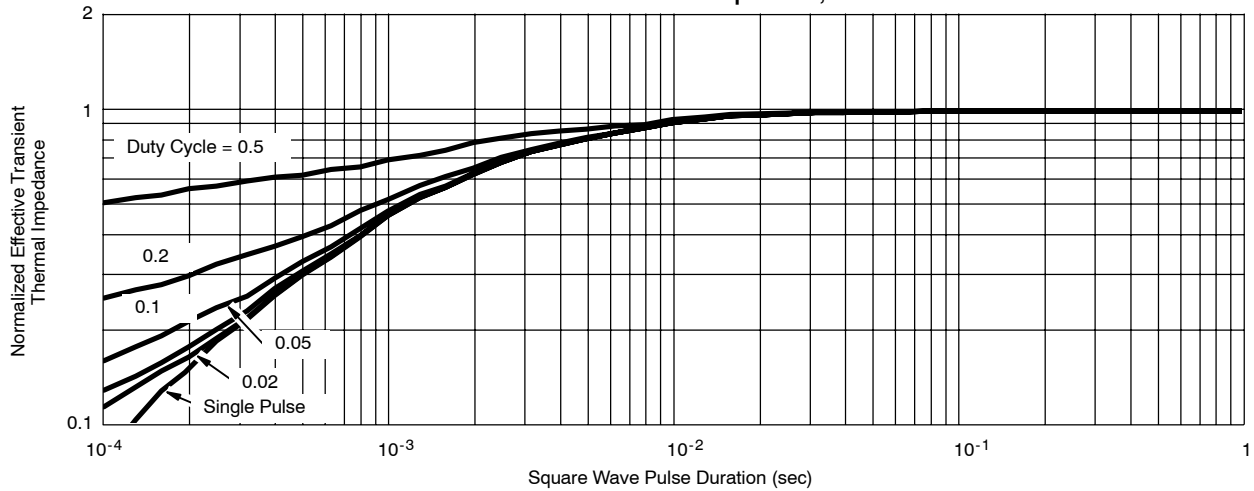
**Maximum Avalanche and Drain Current
vs. Case Temperature**



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case





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