

Vishay Siliconix

N-Channel 200 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY			
V _{DS} (V)	200		
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.0152		
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 7.5 \text{ V}$	0.0169		
Q _g typ. (nC)	58		
I _D (A)	90		
Configuration	Single		

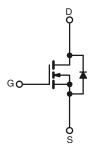
FEATURES

- ThunderFET® power MOSFET
- \bullet Tuned for the lowest R_{DS} Q_{oss} FOM
- Maximum 175 °C junction temperature
- 100 % R_a and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



APPLICATIONS

- · Power supplies:
 - Uninterruptible power supplies
- AC/DC switch-mode power supplies
- Lighting
- · Synchronous rectification
- DC/DC converter
- · Motor drive switch
- DC/AC inverter
- · Solar micro inverter
- Class D audio amplifier



N-Channel MOSFET

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free and halogen-free	SUP90142E-GE3

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	200	V
Gate-source voltage		V_{GS}	± 20	V
Continuous drain current	T _C = 25 °C		90	
	T _C = 125 °C	l _D	52	
Pulsed drain current (t = 100 μs)		I _{DM}	240	А
Continuous source-drain diode current		I _S	90	
Single pulse avalanche current ^a	. 0.411	I _{AS}	60	
Single pulse avalanche energy ^a	L = 0.1 mH	E _{AS}	180	mJ
Maximum power dissipation	T _C = 25 °C		375 ^b	14/
	T _C = 125 °C	P _D	125 ^b	w
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	90
Soldering recommendations (peak temperature) c			260	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	MAXIMUM	UNIT	
Maximum junction-to-ambient (PCB mount) c		R _{thJA}	40	°C/W	
Maximum junction-to-case (drain)	Steady state	R _{thJC}	0.4]	

Notes

- a. Duty cycle ≤ 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).
- d. Package limited.



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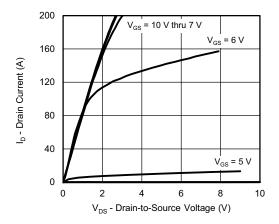
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200	-	-	V	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \ \mu A$	2	-	4	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	250	nA	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V	-	-	1	μА	
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 125 °C	-	-	150		
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 175 °C	-	-	5	mA	
On-state drain current a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	60	-	-	Α	
Drain-source on-state resistance a	` ′	V _{GS} = 10 V, I _D = 30 A	-	0.0126	0.0152	Ω	
	R _{DS(on)}	V _{GS} = 7.5 V, I _D = 30 A	-	0.0133	0.0169		
Forward transconductance a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	-	63	-	S	
Dynamic ^b					•	•	
Input capacitance	C _{iss}		-	3120	-	pF	
Output capacitance	C _{oss}	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	280	-		
Reverse transfer capacitance	C _{rss}		-	24	-		
Total gate charge	Qg		-	58	87		
Gate-source charge	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 60 \text{ A}$	-	17.6	-	nC	
Gate-drain charge	Q _{gd}		-	17.2	-		
Output charge	Q _{oss}	V _{DS} = 100 V, V _{GS} = 0 V	-	108	162		
Gate resistance	R_{g}	f = 1 MHz	1.5	3	5	Ω	
Turn-on delay time	t _{d(on)}		-	14	28		
Rise time	t _r	$V_{DD} = 100 \text{ V}, R_L = 1.66 \Omega, I_D \cong 60 \text{ A},$	-	125	250		
Turn-off delay time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	27	54	- ns	
Fall time	t _f		-	80	150		
Drain-Source Body Diode Characteristic	cs						
Pulse diode forward current (t = 100 μs)	I _{SM}		-	-	240	Α	
Body diode voltage	V_{SD}	I _F = 30 A, V _{GS} = 0 V	-	0.85	1.5	V	
Body diode reverse recovery time	t _{rr}		-	150	300	ns	
Body diode reverse recovery charge	Q _{rr}	1 20 4 41/-4 400 4/	-	0.9	1.8	nC	
Reverse recovery fall time	ta	I _F = 30 A, dl/dt = 100 A/μs	-	125	-		
Reverse recovery rise time	t _b		-	25	-	ns	
Body diode peak reverse recovery charge	I _{RM(REC)}		-	11.5	20	Α	

Notes

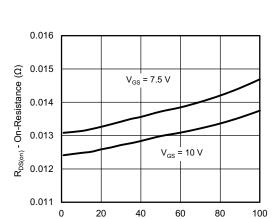
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



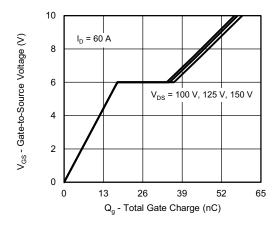


Output Characteristics

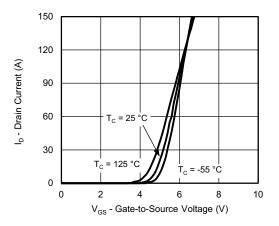


On-Resistance vs. Drain Current and Gate Voltage

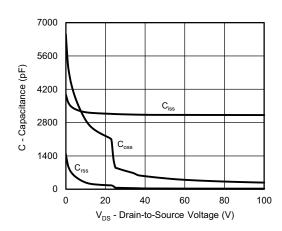
I_D - Drain Current (A)



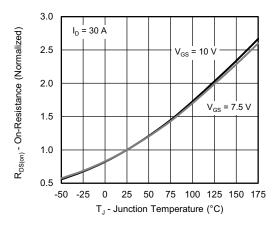
Gate Charge



Transfer Characteristics

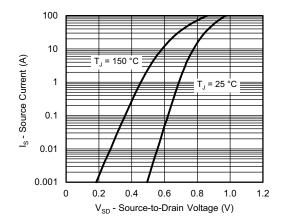


Capacitance

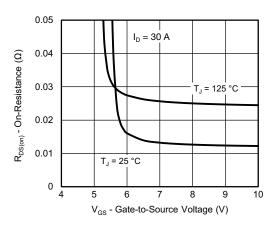


On-Resistance vs. Junction Temperature

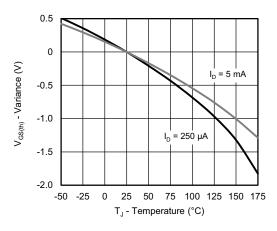




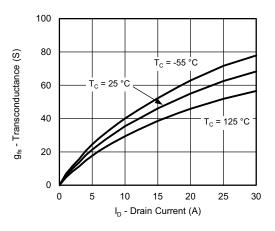
Source-Drain Diode Forward Voltage



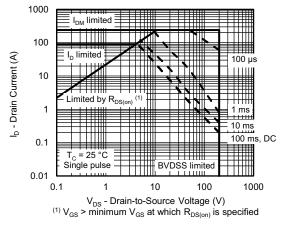
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

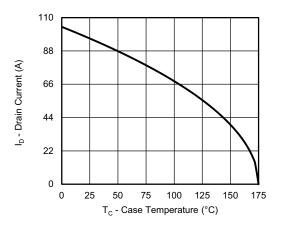


Transconductance

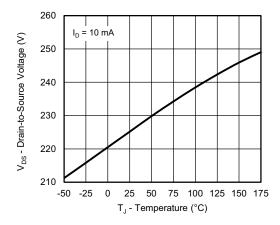


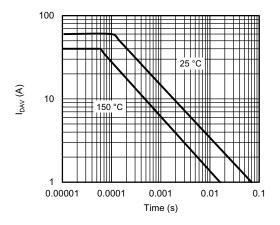
Safe Operating Area, Junction-to-Ambient





Current Derating a





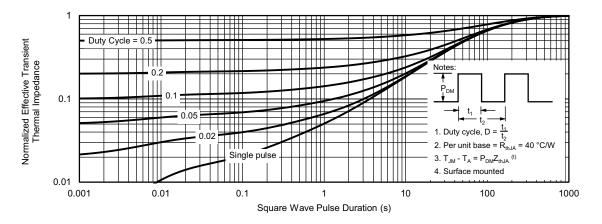
Drain Source Breakdown vs. Junction Temperature

 I_{DAV} vs. Time

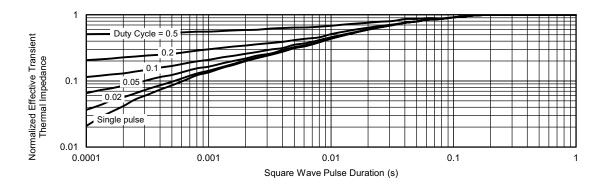
Note

a. The power dissipation P_D is based on T_J max. = 25 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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