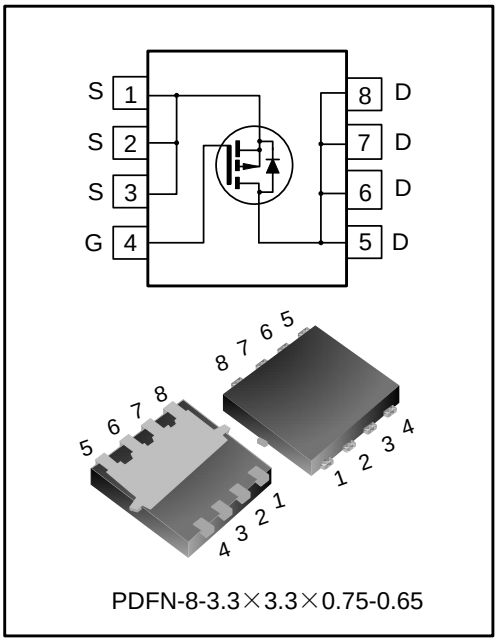
-46A, -30V P CHANNEL MOSFET

DESCRIPTION

SVT03110PL3 is a P channel enhancement mode power MOS field effect transistor which is produced using Silan's LVMOS technology. The improved process and cell structure have been especially tailored to minimize on-state resistance, provide superior switching performance and high avalanche breakdown tolerance.

This device is widely used in UPS, Power Management for Inverter Systems.



PDFN-8-3.3×3.3×0.75-0.65

FEATURES

- -46A, -30V, $R_{DS(on)(typ.)}$ =7.0mΩ@V_{GS}=-10V
- Low gate charge
- Low Crss
- Fast switching
- Improved dv/dt capability
- 100% avalanche tested
- Pb-free lead plating
- RoHS compliant

KEY PERFORMANCE PARAMETERS

Characteristics	Ratings	Unit
V_{DS}	-30	V
$V_{GS(th)}$	-1.0~-3.0	V
$R_{DS(on),max}$	11	mΩ
I_D	-46	A
$Q_{g.typ}$	51	nC

ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
SVT03110PL3TR	PDFN-8-3.3x3.3x0.75-0.65	0311	Halogen free	Tape & Reel

ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED, T_J=25°C)

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Drain-Source Voltage	V_{DS}	--	-30	--	--	V
Gate-Source Voltage	V_{GS}	--	-20	--	20	V
Drain Current (Note 1)	I_D	T_C=25°C	--	--	-46	A
		T_C=100°C	--	--	-29	A
Drain Current Pulsed (Note 2)	I_{DM}	T_C=25°C	--	--	-184	A
Power Dissipation (Note 3)	P_D	T_C=25°C	--	--	35	W
Single Pulsed Avalanche Energy	E_{AS}	L=0.1mH, V_{DD}=-24V, R_G=25Ω, starting temperature T_J=25°C	--	--	105.8	mJ
Single Pulsed Current	I_{AS}	--	--	--	46	A
Operation Junction Temperature Range	T_J	--	-55	--	150	°C
Storage Temperature Range	T_{stg}	--	-55	--	150	°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Thermal Resistance, Junction-case, Bottom	$R_{\theta JC}$	--	--	--	3.57	°C/W
Thermal Resistance, Junction-ambient	$R_{\theta JA}$	--	--	--	59	°C/W
Soldering Temperature (SMD)	T_{sold}	Reflow soldering: 10±1 sec, 3times	--	--	260	°C

ELECTRICAL CHARACTERISTICS (UNLESS OTHERWISE NOTED, T_J=25°C)

Static characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Drain-source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250μA	-30	--	--	V
Drain-source Leakage Current	I_{DSS}	V_{DS}=-30V, V_{GS}=0V, T_J=25°C	--	--	-1.0	μA
		V_{DS}=-30V, V_{GS}=0V, T_J=125°C	--	-1.5	--	
Gate-source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	--	--	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_D=-250μA	-1.0	--	-3.0	V
Static Drain-source On State Resistance	$R_{DS(on)}$	V_{GS}=-10V, I_D=-15A	--	7.0	11	mΩ
		V_{GS}=-4.5V, I_D=-10A	--	10	13.8	
Gate Resistance	R_g	f=1MHz	--	3.2	--	Ω

Dynamic characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{iss}	f=1MHz, V_{GS}=0V, V_{DS}=-15V	--	2419	--	pF
Output Capacitance	C_{oss}		--	398	--	
Reverse Transfer Capacitance	C_{rss}		--	357	--	
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=-20V, V_{GS}=-10V, R_G=6.0Ω, I_D=-11.5A (Notes 4, 5)	--	9.3	--	ns
Turn-on Rise Time	t_r		--	39	--	
Turn-off Delay Time	$t_{d(off)}$		--	72	--	
Turn-off Fall Time	t_f		--	46	--	
Total Gate Charge	Q_g	V_{DD}=-15V, V_{GS}=-10V, I_D=-11.5A (Notes 4, 5)	--	51	--	nC
Gate-source Charge	Q_{gs}		--	9.4	--	
Gate-drain Charge	Q_{gd}		--	11	--	
Gate-plateau Voltage	$V_{plateau}$		--	3.4	--	V

Reverse diode characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Continuous Source Current	I_S	T_C=25°C, Integral Reverse P-N Junction Diode in the MOSFET	--	--	-46	A
Pulsed Source Current	$I_{S,pulse}$		--	--	-184	
Diode Forward Voltage	V_{SD}	I_S=-1.0A, V_{GS}=0V	--	--	-1.4	V
Reverse Recovery Time	T_{rr}	I_S=-11.5A, V_{GS}=0V, V_R=30V dI_F/dt=100A/μs (Note 4)	--	19	--	ns
Reverse Recovery Charge	Q_{rr}		--	11	--	nC

Notes:

1. The rated value only refers to the maximum absolute value at the case temperature of 25°C in the specification. If the case temperature is higher than 25°C, it should be derated according to the actual environmental conditions;
2. Pulse time 5μs, pulse width is limited by the maximum junction temperature;
3. The dissipation power will change with temperature, derating above 25°C: 0.28W/°C;
4. Pulse Test: Pulse width ≤300μs, Duty cycle≤2%;
5. Essentially independent of operating temperature.

TYPICAL CHARACTERISTICS

Figure 1. On-Region Characteristics

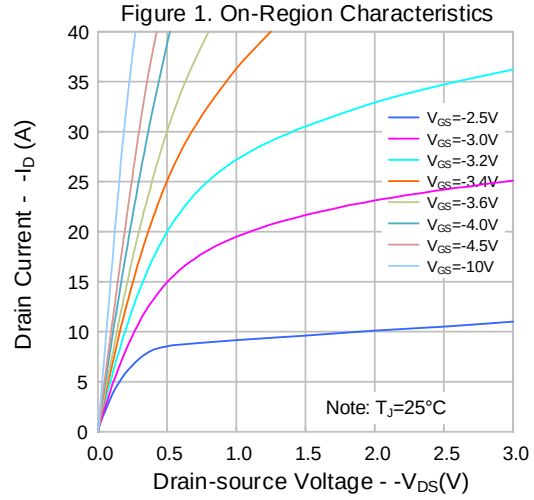


Figure 2. Transfer Characteristics

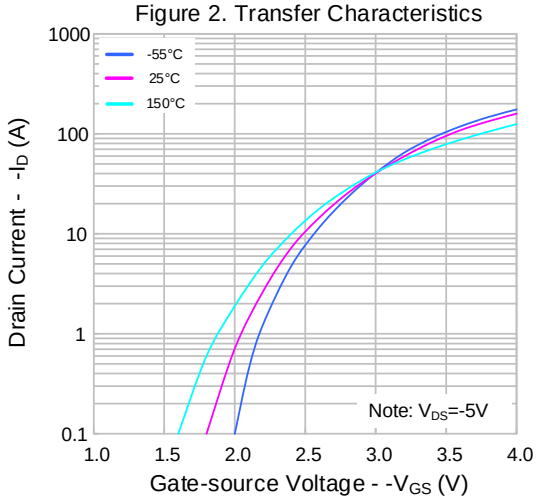


Figure 3. On-resistance vs. Drain Current

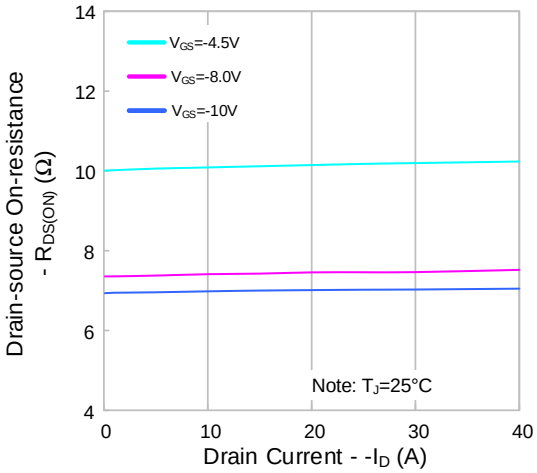


Figure 4. On-resistance vs. Gate-Source Voltage

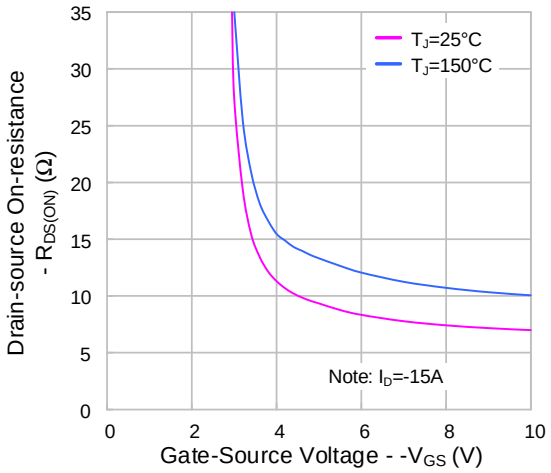


Figure 5. Gate-Source Threshold Voltage vs. Temperature

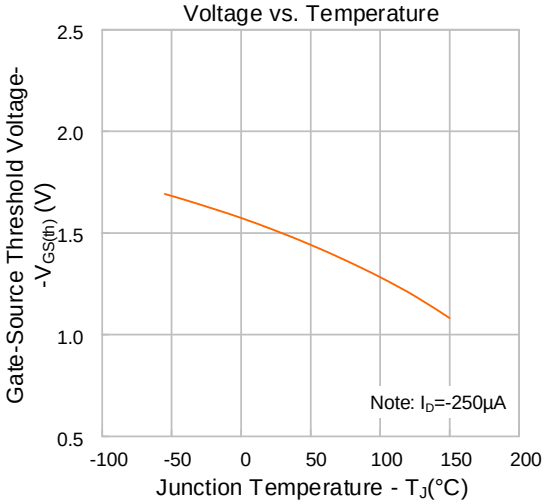
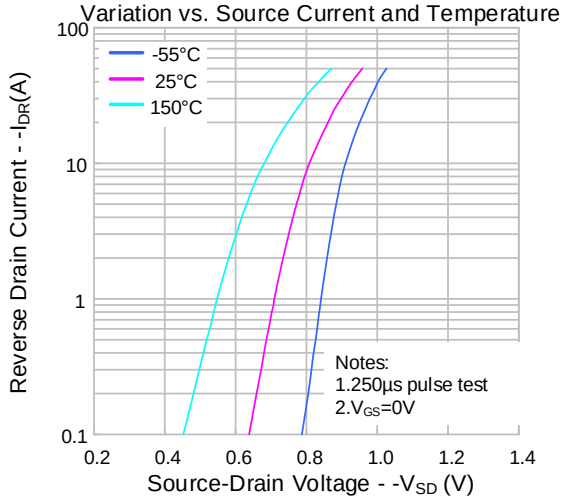


Figure 6. Body Diode Forward Voltage Variation vs. Source Current and Temperature



TYPICAL CHARACTERISTICS (CONTINUED)

Figure 7. Capacitance Characteristics

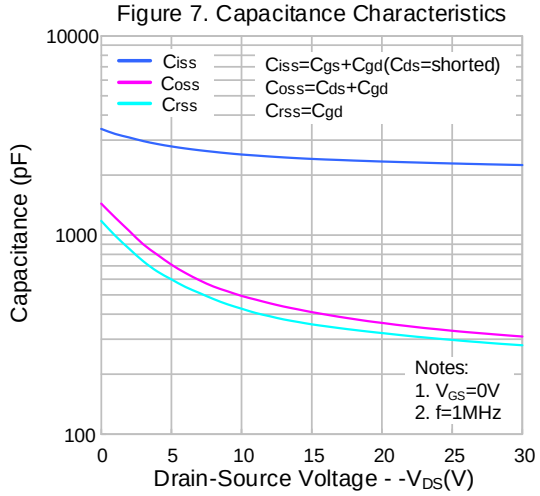


Figure 8. Gate Charge Characteristics

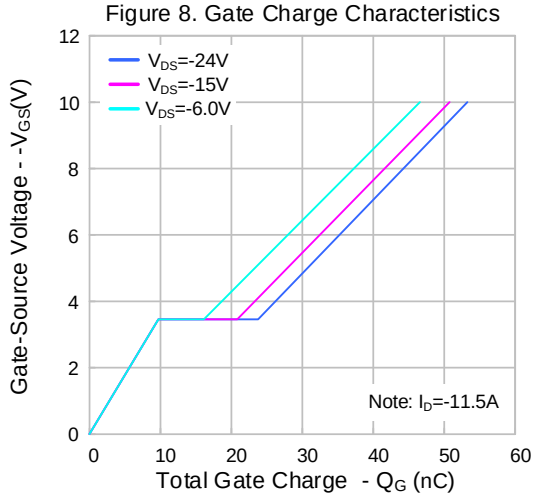


Figure 9. Breakdown Voltage Variation vs. Temperature

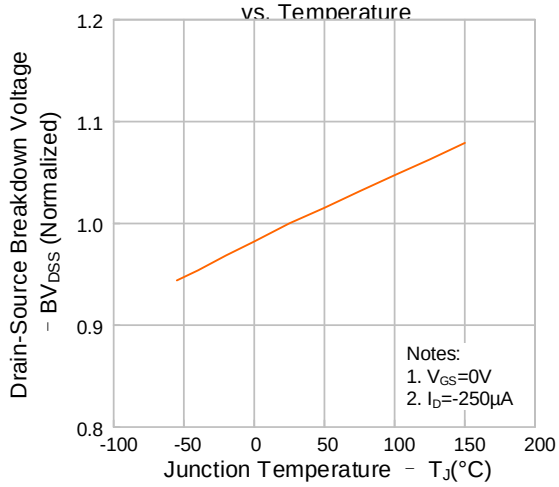


Figure 10. On-resistance Variation vs. Temperature

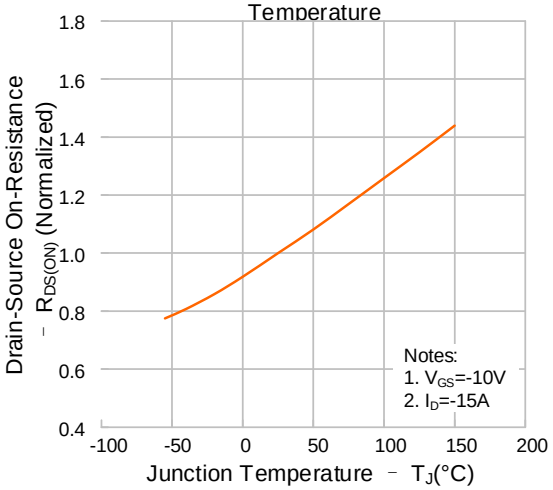


Figure 11. Max. Safe Operating Area

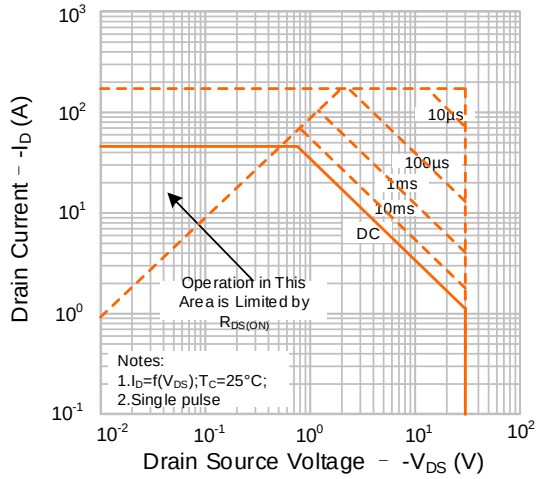
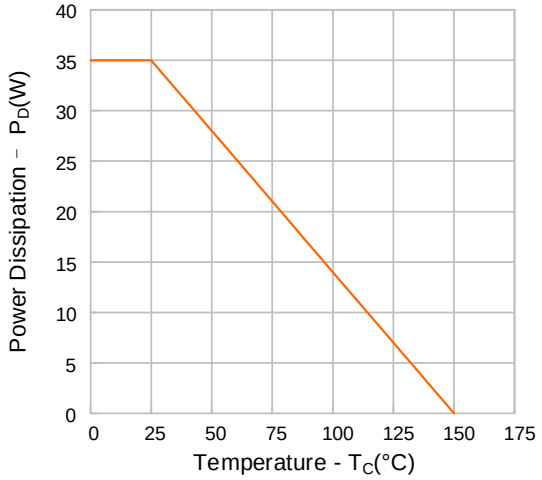
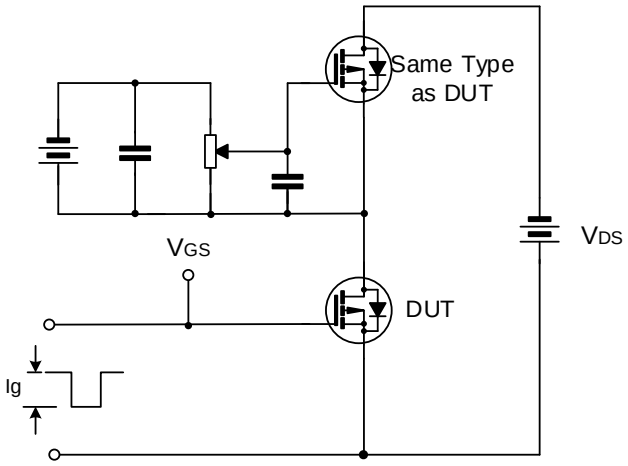


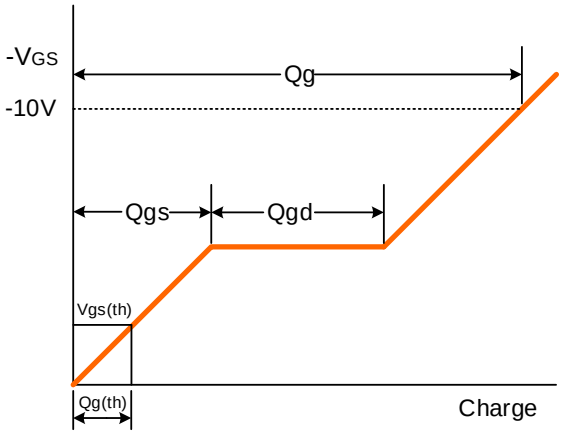
Figure 12. Power Dissipation vs. Temperature



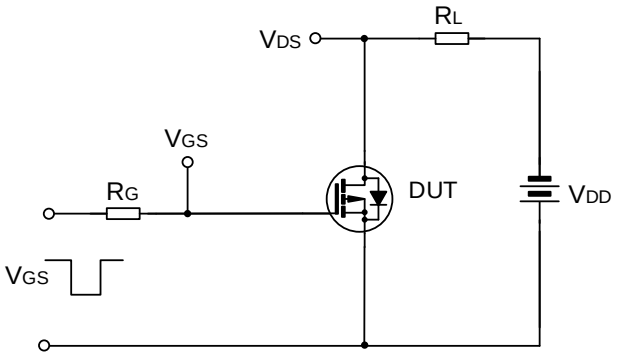
TYPICAL TEST CIRCUIT

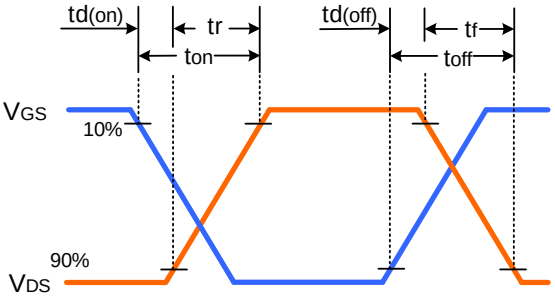
Gate Charge Test Circuit & Waveform



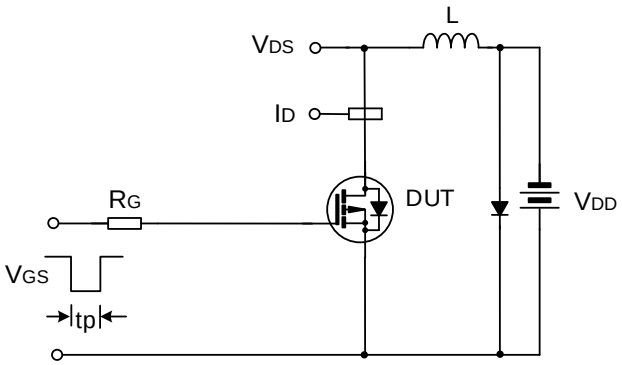


Resistive Switching Test Circuit & Waveform

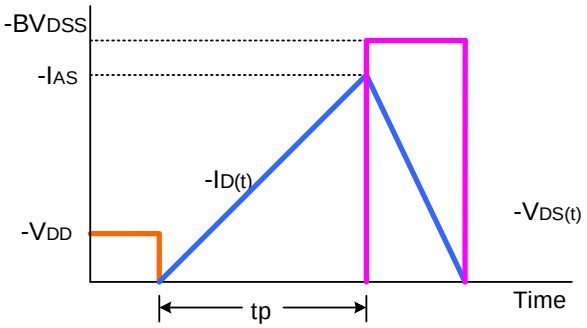




Unclamped Inductive Switching Test Circuit & Waveform

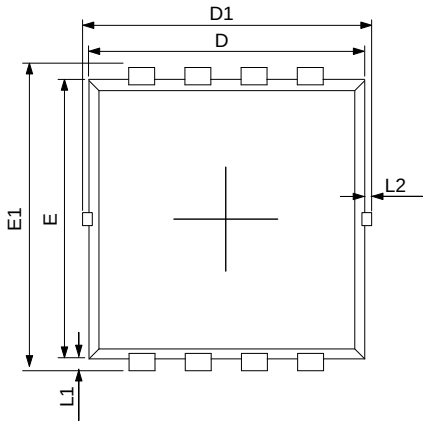


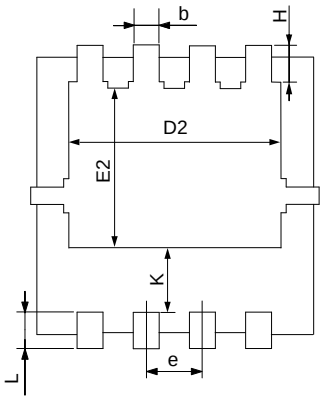
$$E_{AS} = \frac{1}{2} LI_{AS}^2$$



PACKAGE OUTLINE

PDFN-8-3.3x3.3x0.75-0.65 **UNIT: mm**





SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.80	0.90
A1	0.14	0.15	0.20
b	0.25	0.30	0.35
D	3.05	3.15	3.25
D1	3.10	3.30	3.50
D2	2.35	2.45	2.55
E	2.90	3.00	3.10
E1	3.10	3.30	3.50
E2	1.64	1.74	1.84
e	0.55	0.65	0.75
H	0.32	0.42	0.52
K	0.59	0.69	0.79
L	0.25	0.40	0.55
L1	0.10	0.15	0.20
L2	—	—	0.15

MOS DEVICES OPERATE NOTES:

Electrostatic charges may exist in many things. Please take following preventive measures to prevent effectively the MOS electric circuit as a result of the damage which is caused by discharge:

- The operator must put on wrist strap which should be earthed to against electrostatic.
- Equipment cases should be earthed.
- All tools used during assembly, including soldering tools and solder baths, must be earthed.
- MOS devices should be packed in antistatic/conductive containers for transportation.

Important notice:

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Rev.: 1.1

Revision History:

1. Delete wave soldering condition
2. Update the typical test circuit
3. Update the important notice

Rev.: 1.0

Revision History:

1. First release