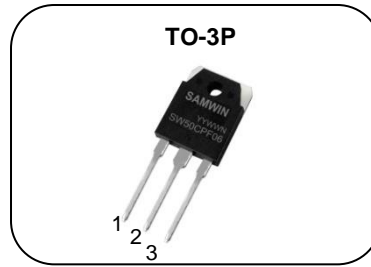


## DIODE

### Features

- Ultrafast recovery time
- Low forward voltage drop
- 150 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC-JESD47

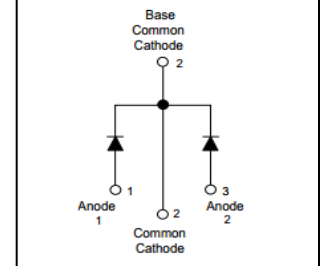


1. Anode 2. Gathode 3. Anode

**VR : 620V**

**IF(AV) : 50A**

**VF at IF : 1.5V**



### General Description

This FRED is designed with optimized performance of forward voltage drop and ultrafast recovery time. The platinum doped life time control, guarantee the best overall performance ruggedness and reliability characteristics. These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as freewheeling diode in low voltage inverters. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element.

### Order Codes

| Item | Sales Type   | Marking   | Package | Packaging |
|------|--------------|-----------|---------|-----------|
| 1    | SW W 50CPF06 | SW50CPF06 | TO-3P   | TUBE      |

### Absolute maximum ratings

| Symbol         | Parameter  | Value       | Unit             |
|----------------|--|-------------|------------------|
| $V_R$          | Repetitive peak reverse voltage                                | 620         | V                |
| $I_{F(AV)}$    | Average rectified forward current (@ $T_C=150^\circ\text{C}$ ) | 50          | A                |
| $T_J, T_{Stg}$ | Operating junction and storage temperatures                    | -55 ~ + 150 | $^\circ\text{C}$ |

### Electrical characteristic ( $T_C = 25^\circ\text{C}$ unless otherwise specified )

| Symbol | Parameter                           | Test conditions                          | Min. | Typ. | Max. | Unit          |
|--------|-------------------------------------|--|------|------|------|---------------|
| $V_R$  | Breakdown voltage, blocking voltage | $I_R=100\mu\text{A}$                     | 620  |      |      | V             |
| $V_F$  | Forward voltage                     | $I_F=3\text{A}$                          |      | 0.9  | 1.13 | V             |
|        |                                     | $I_F=50\text{A}$                         |      |      | 1.5  | V             |
|        |                                     | $I_F=50\text{A}, T_J=150^\circ\text{C}$  |      |      | 1.3  | V             |
| $I_R$  | Reverse leakage current             | $V_R=600\text{V}$                        |      |      | 500  | nA            |
|        |                                     | $V_R=600\text{V}, T_J=150^\circ\text{C}$ |      |      | 500  | $\mu\text{A}$ |
| CT     | Junction capacitance                | $V_R=600\text{V}$                        |      | 30   |      | pF            |

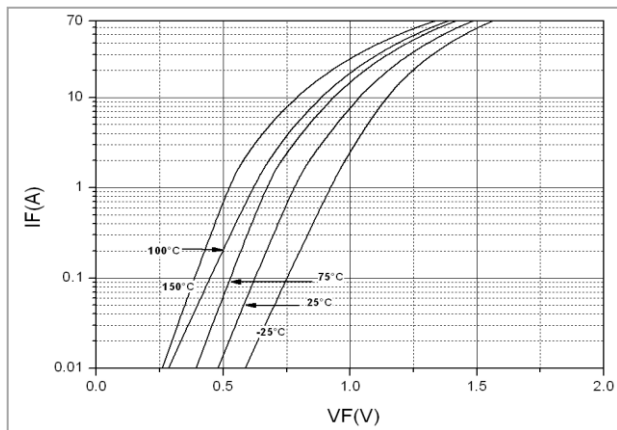
## DYNAMIC RECOVERY CHARACTERISTICS (T<sub>J</sub>= 25 °C unless otherwise specified)

| Symbol           | Parameter               | Test conditions   | Min. | Typ. | Max. | Unit |
|------------------|-------------------------|---|------|------|------|------|
| I <sub>rrm</sub> | Peak recovery current   | I <sub>F</sub> =50A, di/dt=200A/us,<br>V <sub>R</sub> =100V |      | 5.9  |      | A    |
| T <sub>rr</sub>  | Reverse recovery time   |   |      | 59   |      | ns   |
| Q <sub>rr</sub>  | Reverse recovery Charge |   |      | 190  |      | nC   |

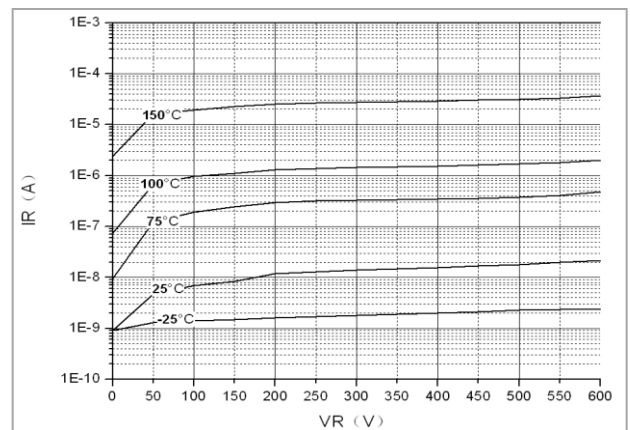
## THERMAL - MECHANICAL SPECIFICATIONS

| Symbol            | Parameter  | Value | Unit |
|-------------------|--|-------|------|
| R <sub>thjc</sub> | Thermal resistance, Junction to case (Per Leg)       | 0.57  | °C/W |
| R <sub>thjc</sub> | Thermal resistance, Junction to case (Per Package)   | 0.62  | °C/W |
| R <sub>thja</sub> | Thermal resistance, Junction to ambient(Per Leg)     | 35.5  | °C/W |
| R <sub>thja</sub> | Thermal resistance, Junction to ambient(Per Package) | 34.3  | °C/W |

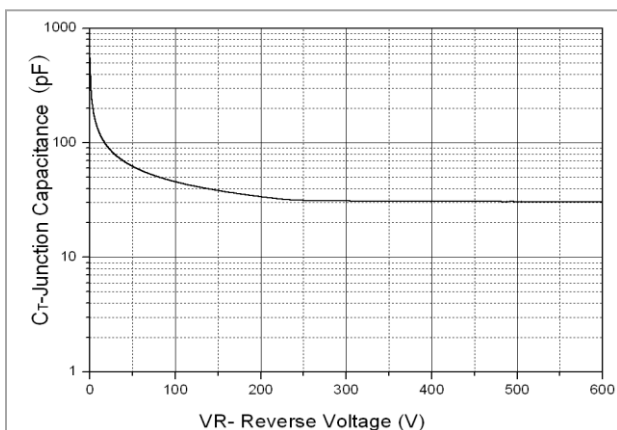
**Fig. 1. Typical Forward Voltage Drop Characteristics**



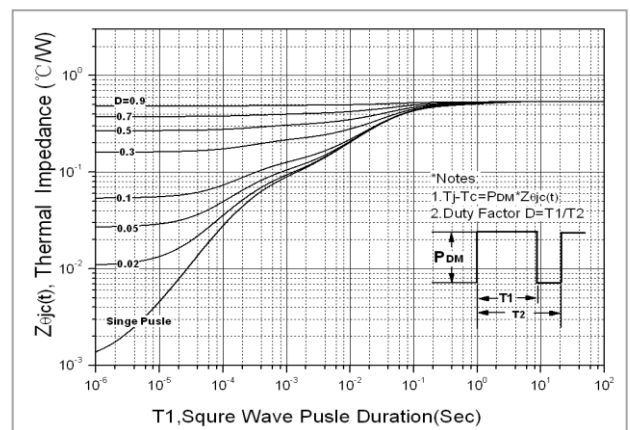
**Fig. 2. Typical Values of Reverse Current vs. Reverse Voltage**



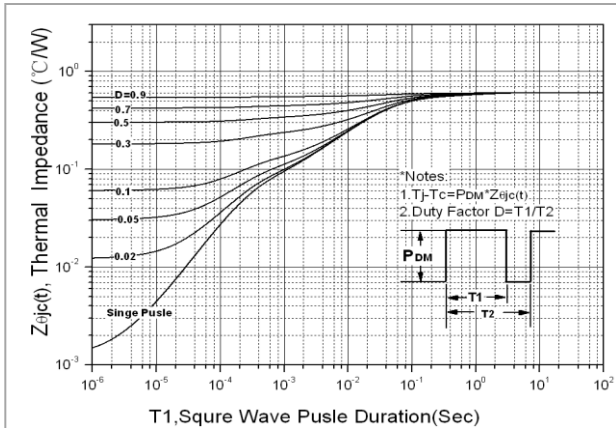
**Fig. 3. Typical Junction Capacitance vs. Reverse Voltage**



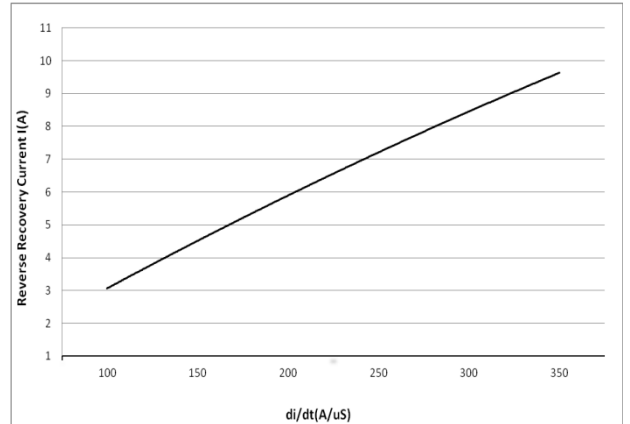
**Fig. 4. Max. Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)**



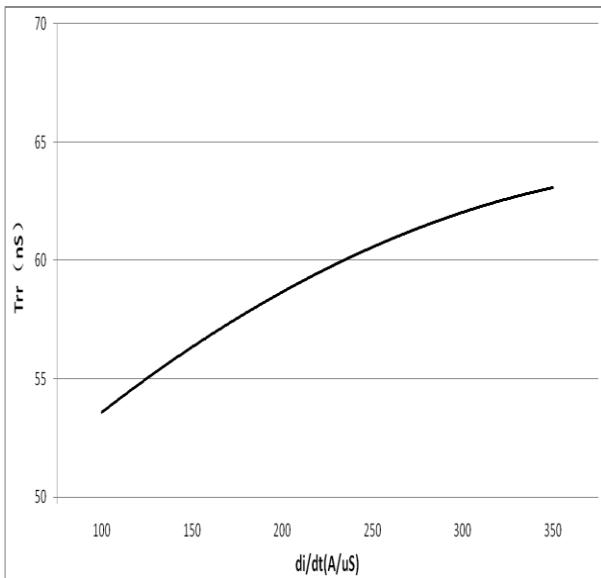
**Fig 5. Max. Thermal Impedance  $Z_{thJC}$  Characteristics (Per Package)**



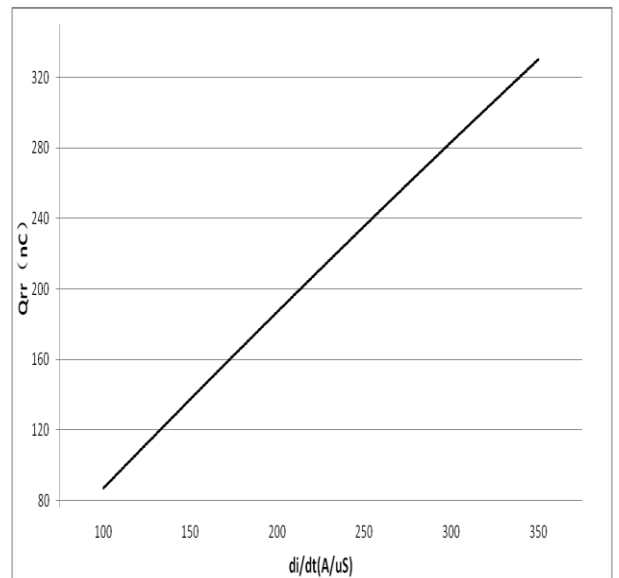
**Fig. 6. Typical Reverse Recovery Current vs. di/dt**



**Fig. 7. Typical Reverse Recovery Time vs. di/dt**



**Fig. 8. Typical Stored Charge vs. di/dt**



**Fig. 9. Reverse Recovery Parameter Test Circuit & waveform**

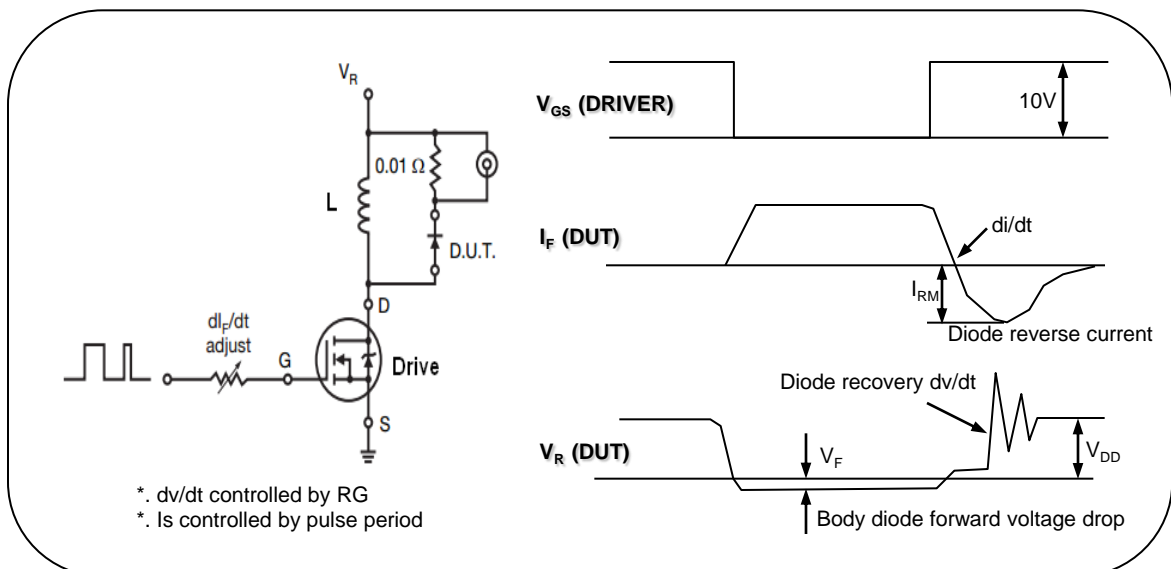


Fig. 10. Unclamped Inductive Test Circuit

