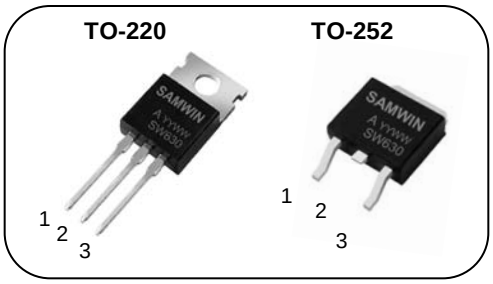
N-channel TO-220/D-PAK MOSFET

Features

- High ruggedness
- $R_{DS(ON)}$ (Max 0.4 **Ω**)@V_{GS}=10V
- Gate Charge (Typ 22nC)
- Improved dv/dt Capability
- 100% Avalanche Tested

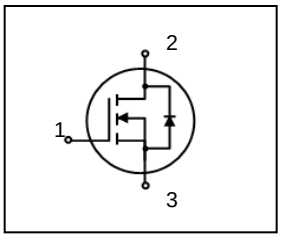


1. Gate 2. Drain 3. Source

BV_{DSS} : 200V

I_D : 10A

$R_{DS(ON)}$: 0.4ohm



General Description

This power MOSFET is produced with advanced VDMOS technology of SAMWIN. This technology enable power MOSFET to have better characteristics, such as fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics. This power MOSFET is usually used at high efficient DC to DC converter block and SMPS.
It's typical application is TV and monitor.

Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW P 630A	SW630	TO-220	TUBE
2	SW D 630A	SW630	TO-252	REEL

Absolute maximum ratings

Symbol	Parameter		Value		Unit
			TO-220	TO-252	
V_{DSS}	Drain to Source Voltage		200		V
I_D	Continuous Drain Current (@T_C=25ºC)		10*		A
	Continuous Drain Current (@T_C=100ºC)		6.3*		A
I_{DM}	Drain current pulsed	(note 1)	40		A
V_{GS}	Gate to Source Voltage		±30		V
E_{AS}	Single pulsed Avalanche Energy	(note 2)	600		mJ
E_{AR}	Repetitive Avalanche Energy	(note 1)	120		mJ
dv/dt	Peak diode Recovery dv/dt	(note 3)	5		V/ns
P_D	Total power dissipation (@T_C=25ºC)		132	148	W
	Derating Factor above 25ºC		1.06	1.18	W/ºC
T_{STG}, T_J	Operating Junction Temperature & Storage Temperature		-55 ~ + 150		ºC
T_L	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.		300		ºC

*. Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value		Unit
		TO-220	TO-252	
R_{thjc}	Thermal resistance, Junction to case	0.95	0.85	ºC/W
R_{thcs}	Thermal resistance, Case to Sink	0.5	-	ºC/W
R_{thja}	Thermal resistance, Junction to ambient	57.5	-	ºC/W

Electrical characteristic (T_C = 25ºC unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Off characteristics						
BV_{DSS}	Drain to source breakdown voltage	V_{GS}=0V, I_D=250uA	200			V
ΔBV_{DSS} / ΔT_J	Breakdown voltage temperature coefficient	I_D=250uA, referenced to 25ºC		0.21		V/ºC
I_{DSS}	Drain to source leakage current	V_{DS}=200V, V_{GS}=0V			1	uA
		V_{DS}=160V, T_C=125ºC			50	uA
I_{GSS}	Gate to source leakage current, forward	V_{GS}=30V, V_{DS}=0V			100	nA
	Gate to source leakage current, reverse	V_{GS}=-30V, V_{DS}=0V			-100	nA
On characteristics						
$V_{GS(TH)}$	Gate threshold voltage	V_{DS}=V_{GS}, I_D=250uA	2.0		4.0	V
$R_{DS(ON)}$	Drain to source on state resistance	V_{GS}=10V, I_D = 5A		0.29	0.4	Ω
Gfs	Forward Transconductance	V_{DS} = 20 V, I_D = 5 A	5			S
Dynamic characteristics						
C_{iss}	Input capacitance	V_{GS}=0V, V_{DS}=25V, f=1MHz		420		pF
C_{oss}	Output capacitance			100		
C_{rss}	Reverse transfer capacitance			40		
$t_{d(on)}$	Turn on delay time	V_{DS}=100V, I_D=10A, R_G=25Ω (note 4,5)		7	15	ns
tr	Rising time			38	50	
$t_{d(off)}$	Turn off delay time			48	70	
t_f	Fall time			32	60	
Q_g	Total gate charge	V_{DS}=160V, V_{GS}=10V, I_D=10A (note 4,5)		22	40	nC
Q_{gs}	Gate-source charge			3		
Q_{gd}	Gate-drain charge			13		

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_S	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			10	A
I_{SM}	Pulsed source current				40	A
V_{SD}	Diode forward voltage drop.	I_S=10A, V_{GS}=0V			1.5	V
T_{rr}	Reverse recovery time	I_S=10A, V_{GS}=0V, dI_F/dt=100A/us		145		ns
Q_{rr}	Reverse recovery Charge			0.75		uC

※. Notes
1. Repeatitive rating : pulse width limited by junction temperature.
2. L = 12mH, I_{AS} = 10A, V_{DD} = 50V, R_G=25Ω, Starting T_J = 25ºC
3. I_{SD} ≤ 10A, di/dt = 100A/us, V_{DD} ≤ BV_{DSS}, Staring T_J =25ºC
4. Pulse Test : Pulse Width≤ 300us, duty cycle ≤ 2%
5. Essentially independent of operating temperature.

Fig. 1. On-state characteristics

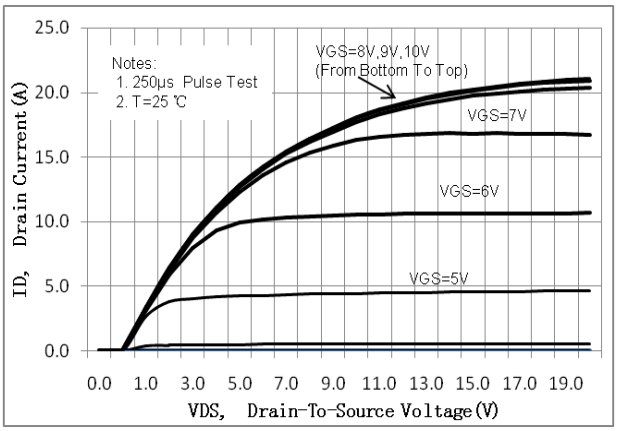


Fig. 2. On-resistance variation vs. drain current and gate voltage

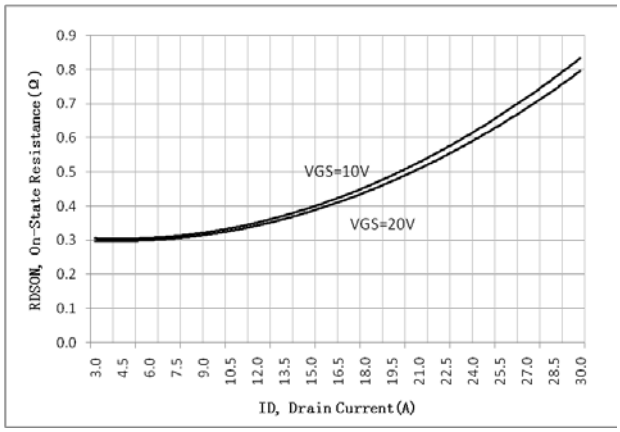


Fig. 3. Gate charge characteristics

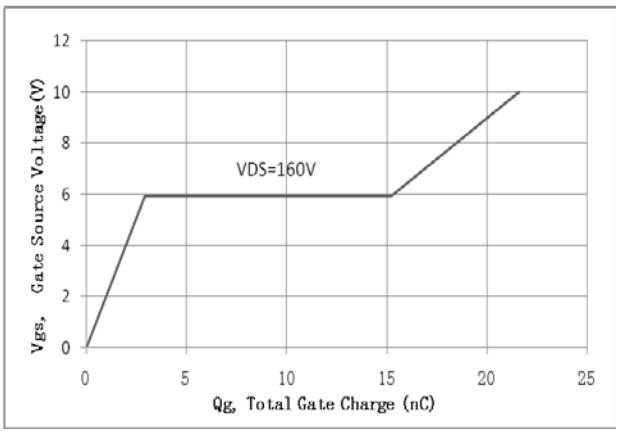


Fig. 4. On state current vs. diode forward voltage

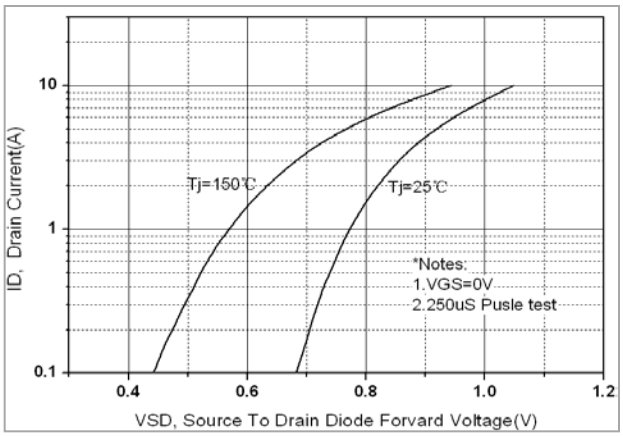


Fig 5. Breakdown Voltage Variation vs. Junction Temperature

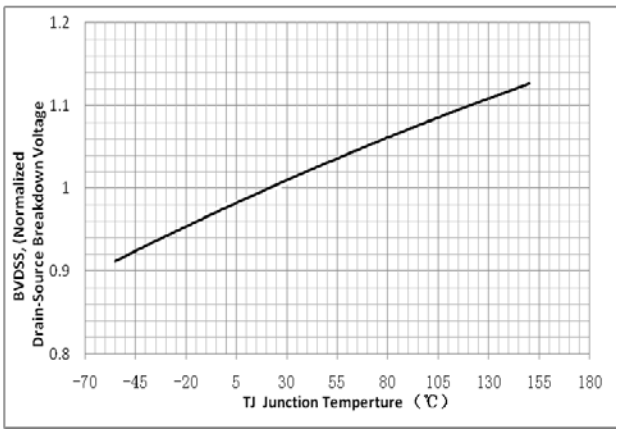


Fig. 6. On resistance variation vs. junction temperature

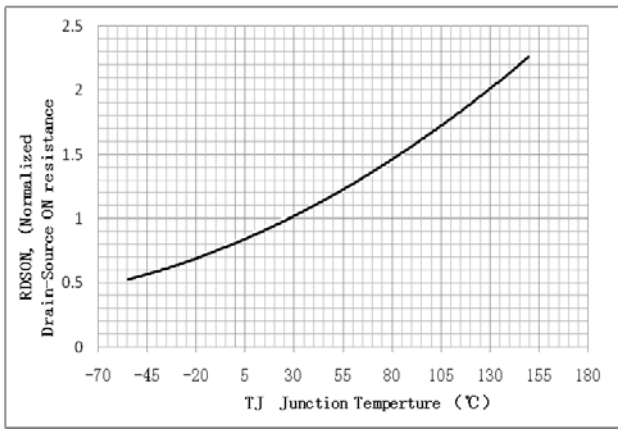


Fig. 7. Maximum safe operating area (TO-220)

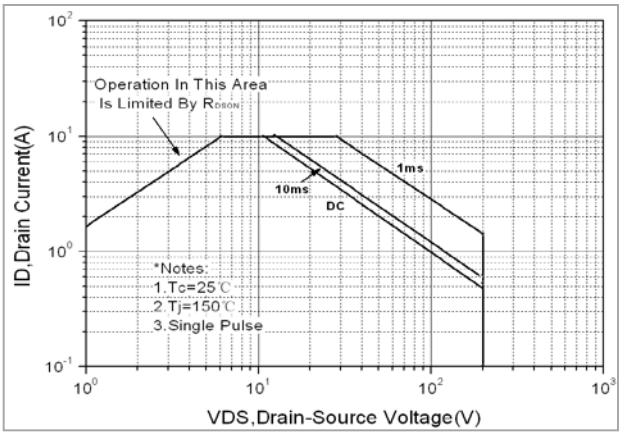


Fig. 8. Maximum safe operating area (TO-252)

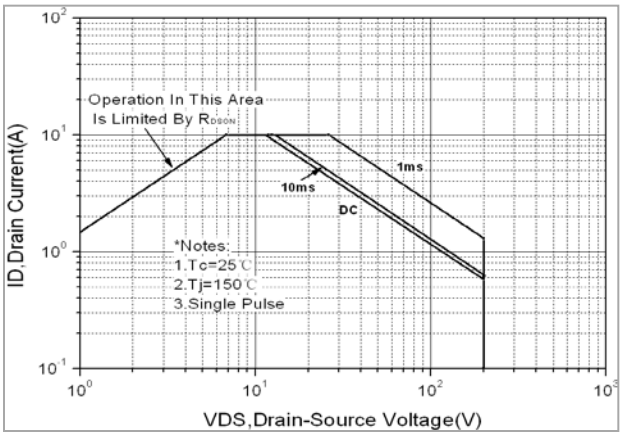


Fig. 9. Transient thermal response curve(TO-220)

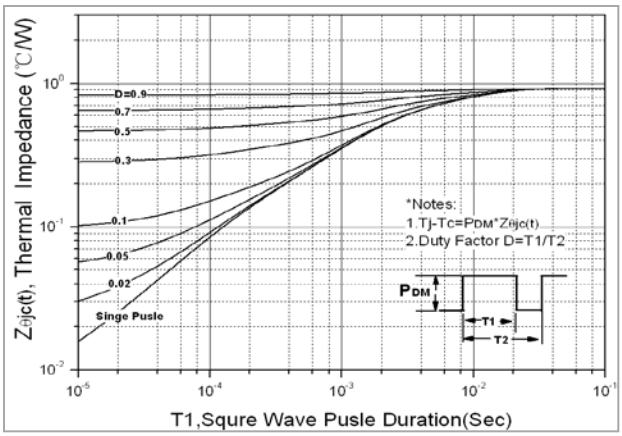


Fig. 10. Transient thermal response curve(TO-252)

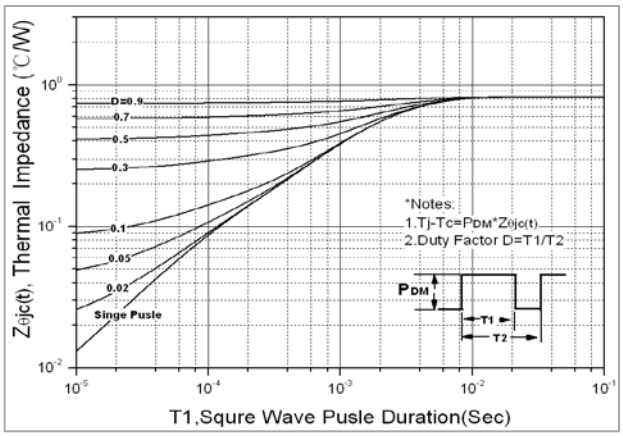


Fig. 11. Gate charge test circuit & waveform

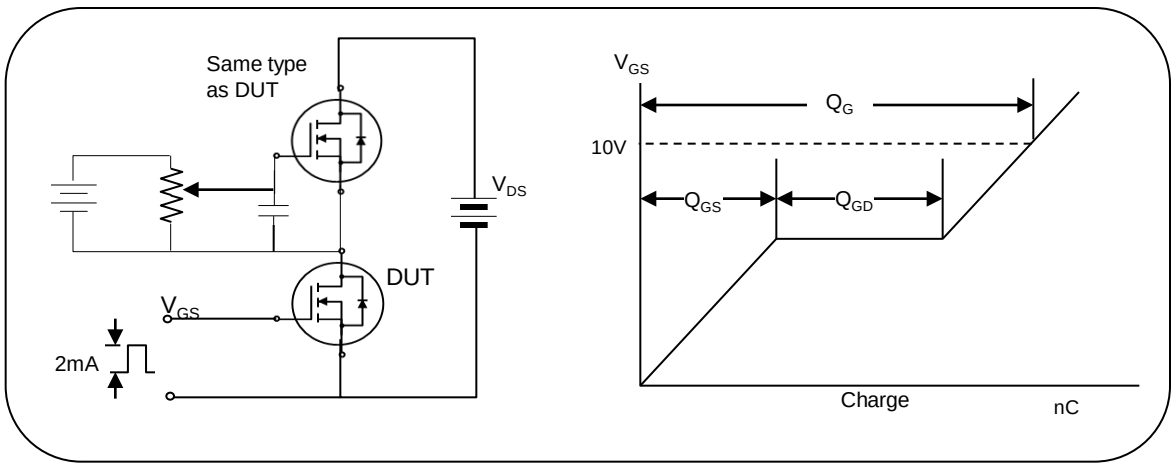


Fig. 12. Switching time test circuit & waveform

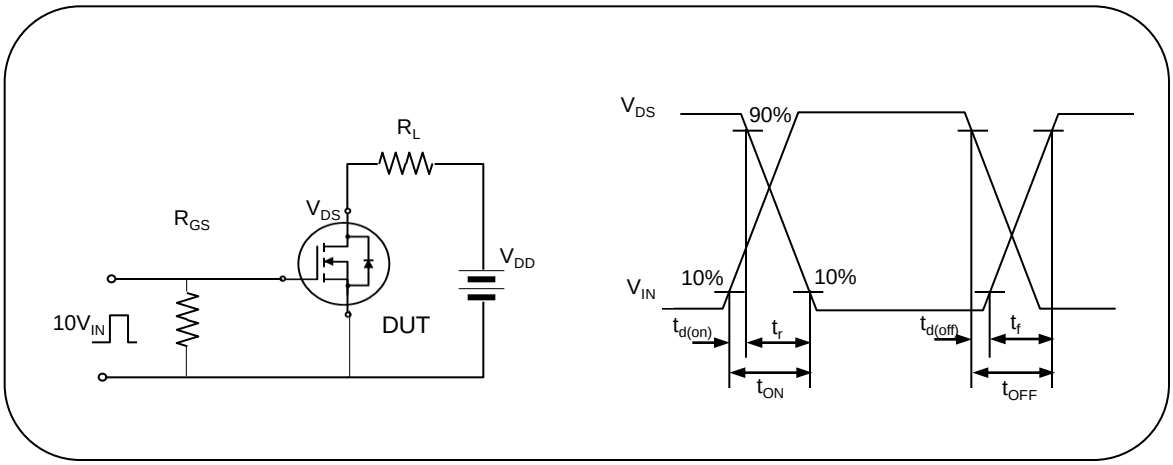


Fig. 13. Unclamped Inductive switching test circuit & waveform

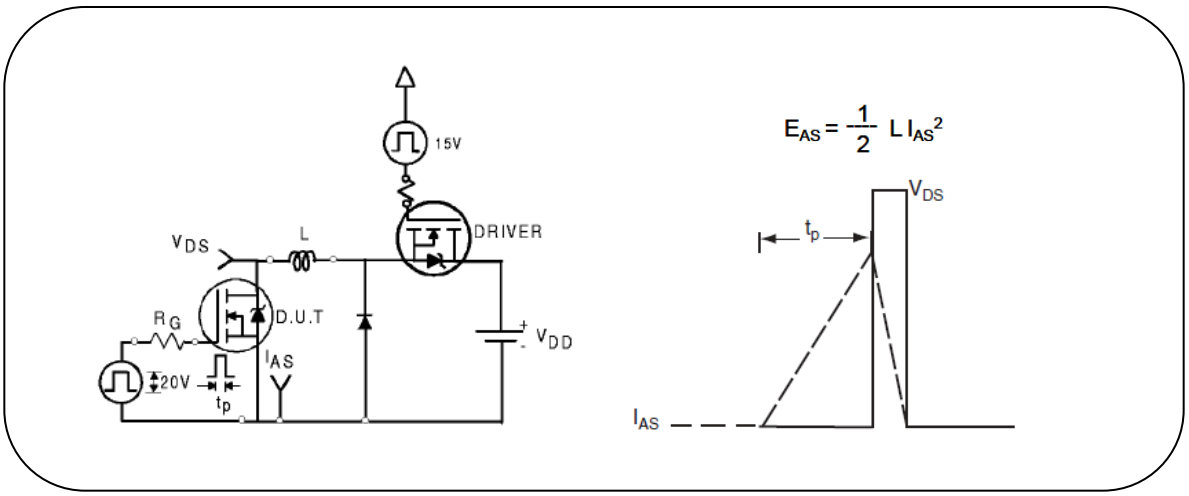


Fig. 14. Peak diode recovery dv/dt test circuit & waveform