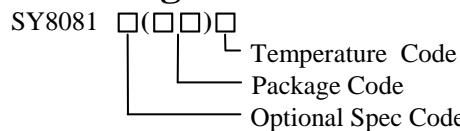


High Efficiency 2.5MHz, 1A Synchronous Step Down Regulator

General Description

The SY8081 is a high efficiency 2.5MHz synchronous step down DC/DC regulator capable of delivering up to 1A output currents. It can operate over a wide input voltage range from 2.5V to 5.5V and integrates main switch and synchronous switch with very low $R_{DS(ON)}$ to minimize the conduction loss.

Ordering Information



Ordering Number	Package Type	Note
SY8081DQC	DFN1.5×1.5-6	1A

Features

- Low $R_{DS(ON)}$ for Internal Switches (Top/Bottom) 230mΩ/150mΩ
- 2.5~5.5V Input Voltage Range
- High Switching Frequency 2.5MHz Minimizes the External Components
- Internal Soft-start Limits the Inrush Current
- 100% Dropout Operation
- RoHS Compliant and Halogen Free
- Output Auto Discharge Function
- Compact Package: DFN1.5×1.5-6

Applications

- Portable Navigation Device
- Set Top Box
- USB Dongle
- Media Player
- Smart phone

Typical Applications

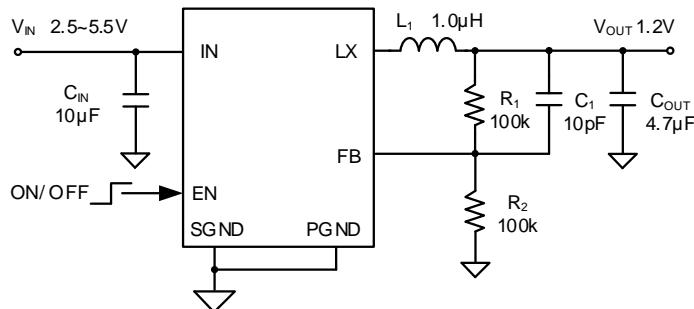


Figure1. Schematic Diagram

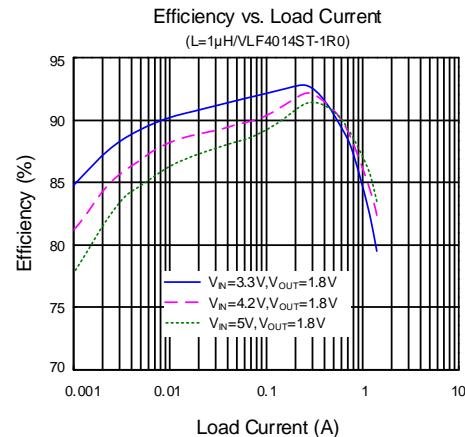
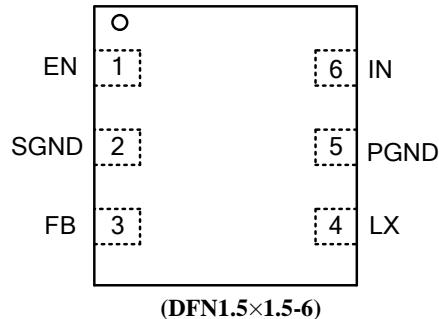


Figure2. Efficiency

Pinout (Top View)



Top Mark: MNxyz (device code: **MN**, *x*=year code, *y*=week code, *z*= lot number code)

Pin Name	Pin Number	Pin Description
EN	1	Enable control. Pull high to turn on. Do not leave it floating.
SGND	2	Signal ground pin.
FB	3	Output feedback pin. Connect this pin to the center point of the output resistor divider (as shown in Figure 1) to program the output voltage: $V_{OUT}=0.6\times(1+R_1/R_2)$.
LX	4	Inductor pin. Connect this pin to the switching node of inductor.
PGND	5	Power ground pin.
IN	6	Input pin. Decouple this pin to GND pin with at least a 10 μ F ceramic capacitor.

Absolute Maximum Ratings (Note 1)

Supply Input Voltage	6.0V
Enable, FB Voltage	$V_{IN} + 0.6V$
Power Dissipation, PD @ TA = 25°C,	
DFN1.5x1.5-6	1.6 W
Package Thermal Resistance (Note 2)	
θ_{JA}	145°C/W
θ_{JC}	81°C/W
Junction Temperature Range	-125°C
Lead Temperature (Soldering, 10 sec.)	260°C
Storage Temperature Range	-65°C to 150°C

Recommended Operating Conditions (Note 3)

Supply Input Voltage	2.5V to 5.5V
Junction Temperature Range	-40°C to 125°C
Ambient Temperature Range	-40°C to 85°C

Electrical Characteristics

(VIN = 5V, VOUT = 2.5V, L = 1.0µH, COUT = 4.7µF, TA = 25°C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage Range	V _{IN}		2.5		5.5	V
Quiescent Current	I _Q	I _{OUT} =0, V _{FB} =V _{REF} ×105%		40		µA
Shutdown Current	I _{SHDN}	EN=0		0.1	1	µA
Feedback Reference Voltage	V _{REF}		0.588	0.6	0.612	V
FB Input Current	I _{FB}	V _{FB} =V _{IN}	-50		50	nA
PFET RON	R _{DS(ON),P}			230		mΩ
NFET RON	R _{DS(ON),N}			150		mΩ
PFET Current Limit	I _{LIM}		1.3			A
EN Rising Threshold	V _{ENH}		1.5			V
EN Falling Threshold	V _{ENL}				0.4	V
Input UVLO Threshold	V _{UVLO}				2.5	V
UVLO Hysteresis	V _{HYS}			0.1		V
Oscillator Frequency	f _{OSC}			2.5		MHz
Min ON Time				65		ns
Max Duty Cycle			100			%
Soft Start Time	T _{SS}			1		ms
Thermal Shutdown Temperature	T _{SD}			160		°C
Thermal Shutdown Hysteresis	T _{HYS}			15		°C
Output Discharge Resistor	R _{DSC}			120		Ω
PFM Model Operation	I _{PFM}	V _{IN} =3.6V, V _{OUT} =3.3V, L=1.0µH.		100		mA

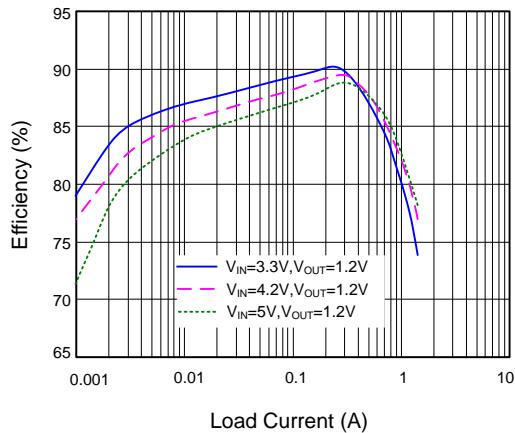
Note 1: Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note 2: θ_{JA} is measured in the natural convection at $T_A = 25^\circ\text{C}$ on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Exposed Paddle of DFN package is the case position for θ_{JC} measurement.

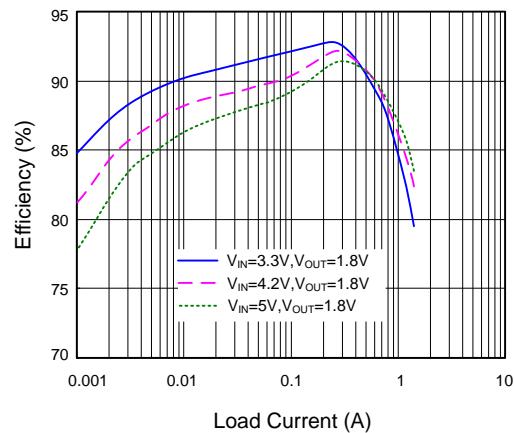
Note 3: The device is not guaranteed to function outside its operating conditions.

Typical Performance Characteristics

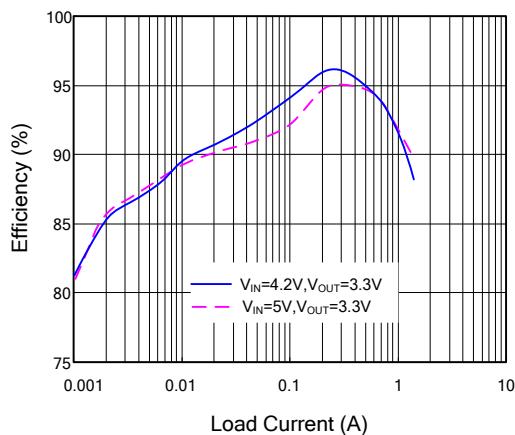
Efficiency vs. Load Current



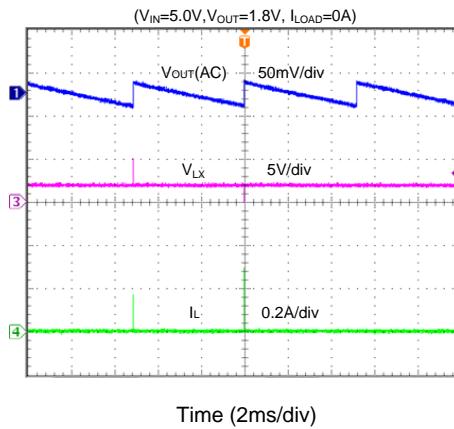
Efficiency vs. Load Current



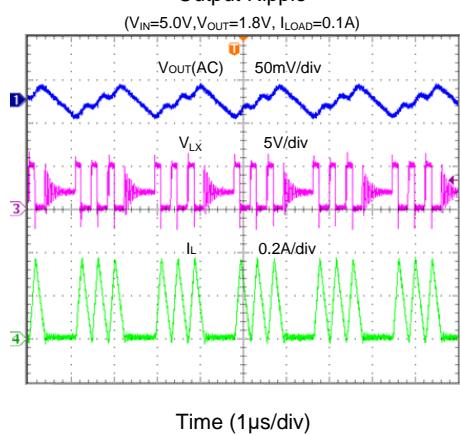
Efficiency vs. Load Current



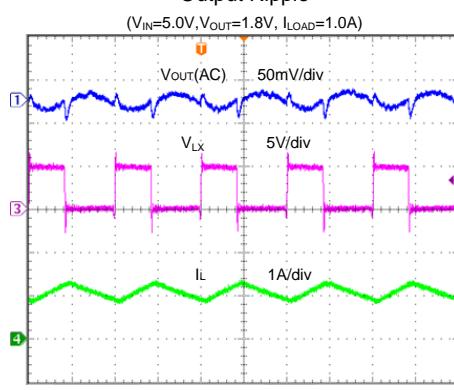
Output Ripple

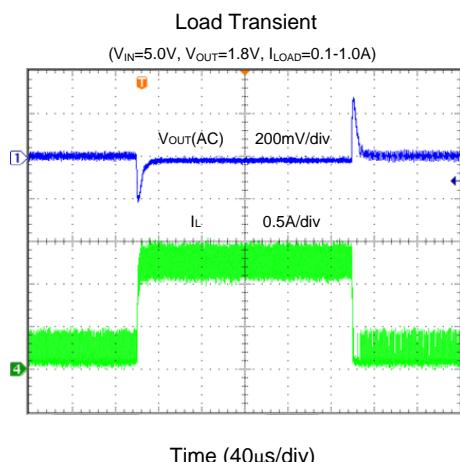
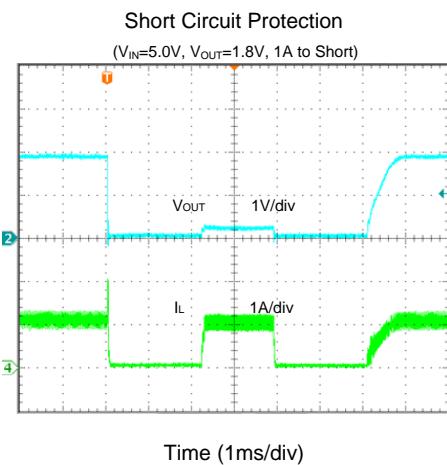
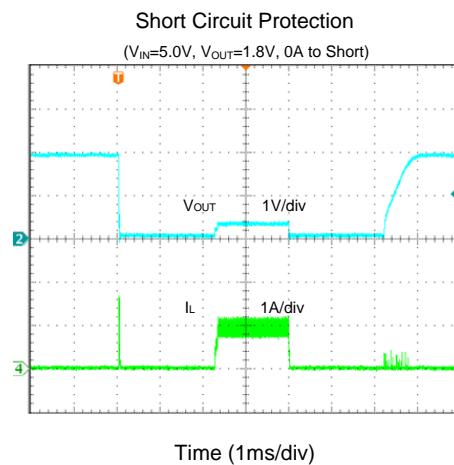
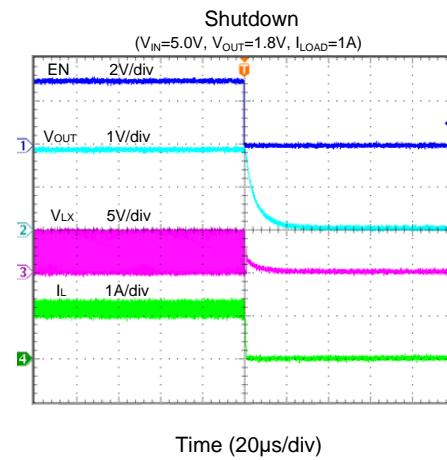
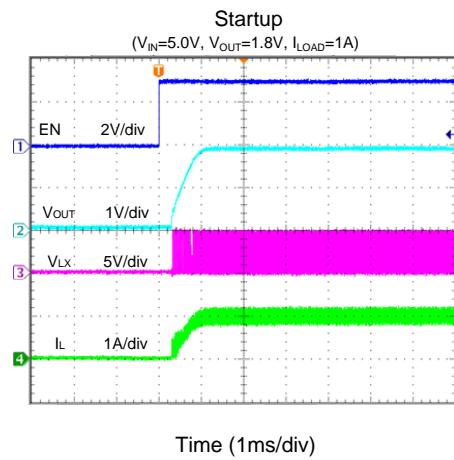


Output Ripple



Output Ripple





Operation

The SY8081 is a high efficiency 2.5MHz synchronous step down DC/DC regulator capable of delivering up to 1A output currents. It can operate over a wide input voltage range from 2.5V to 5.5V and integrates main switch and synchronous switch with very low $R_{DS(ON)}$ to minimize the conduction loss.

Applications Information

Because of the high integration in the SY8081, the application circuit based on this IC is rather simple. Only the input capacitor C_{IN} , the output capacitor C_{OUT} , the output inductor L and the feedback resistors (R_1 and R_2) need to be selected for the targeted applications specifications.

Feedback Resistor Dividers R_1 and R_2 :

Choose R_1 and R_2 to program the proper output voltage. To minimize the power consumption under light loads, it is desirable to choose large resistance values for both R_1 and R_2 . A value of between 100k Ω and 1M Ω is highly recommended for both resistors. If $R_2=120k\Omega$ is chosen, then R_1 can be calculated to be:

$$R_1 = \frac{(V_{OUT} - 0.6V) \times R_2}{0.6V}$$

Input Capacitor C_{IN} :

A typical X7R or better grade ceramic capacitor and greater than 4.7 μ F capacitance is recommended. To minimize the potential noise problem, place this ceramic capacitor really close to the IN and GND pins. Care should be taken to minimize the loop area formed by C_{IN} , and IN/GND pins.

Output Capacitor C_{OUT} :

The output capacitor is selected to handle the output ripple noise requirements. Both steady state ripple and transient requirements must be taken into consideration when selecting this capacitor. For the best performance, it is recommended to use an X7R or better grade ceramic capacitor with 6V rating and greater than 4.7 μ F capacitance.

Output Inductor L:

There are several considerations in choosing this inductor.

- 1) Choose the inductance to provide the desired ripple current. It is suggested to choose the ripple current to be about 40% of the maximum output current. The inductance is calculated as:

$$L = \frac{V_{OUT}(1 - V_{OUT}/V_{IN,MAX})}{f_{sw} \times I_{OUT,MAX} \times 40\%}$$

where f_{sw} is the switching frequency and $I_{out,max}$ is the maximum load current.

The SY8081 is quite tolerant of different ripple current amplitude. Consequently, the final choice of inductance can be slightly off the calculation value without significantly impacting the performance.

- 2) The saturation current rating of the inductor must be selected to be greater than the peak inductor current under full load conditions.

$$I_{SAT, MIN} > I_{OUT, MAX} + \frac{V_{OUT}(1 - V_{OUT}/V_{IN,MAX})}{2 \times f_{sw} \times L}$$

- 3) The DCR of the inductor and the core loss at the switching frequency must be low enough to achieve the desired efficiency requirement. It is desirable to choose an inductor with $DCR < 50m\Omega$ to achieve a good overall efficiency.

Layout Design:

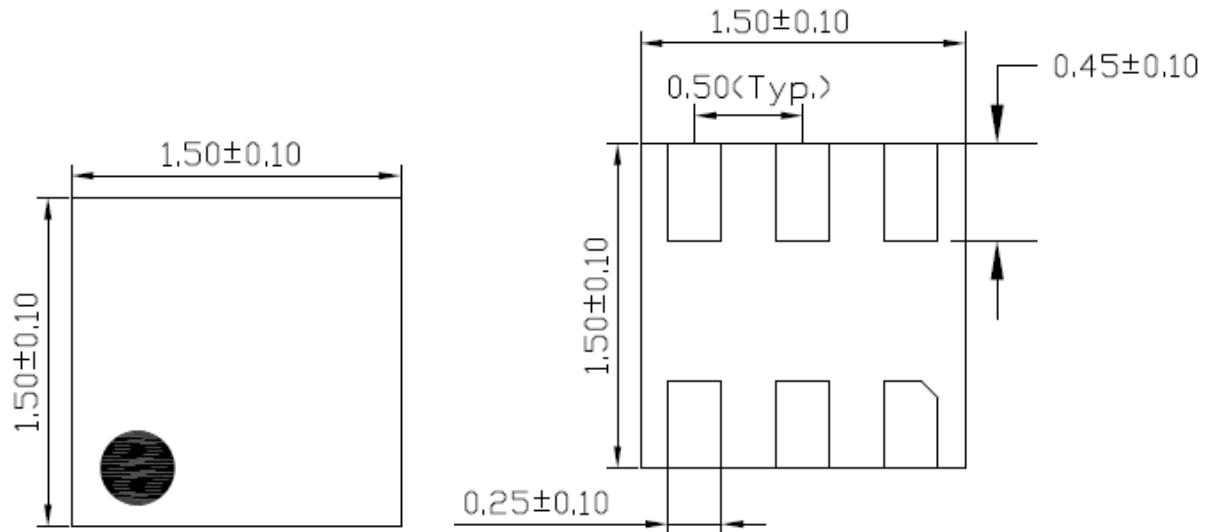
The layout design of SY8081 is relatively simple. For the best efficiency and minimum noise problems, the following components should be placed close to the IC: C_{IN} , L, R_1 and R_2 .

- 1) It is desirable to maximize the PCB copper area connecting to GND pin to achieve the best thermal and noise performance. If the board space allowed, a ground plane is highly desirable.
- 2) C_{IN} must be close to Pins IN and GND. The loop area formed by C_{IN} and GND must be minimized.
- 3) The PCB copper area associated with the LX pin must be minimized to avoid the potential noise problem.
- 4) The components R_1 and R_2 , and the trace connecting to the FB pin must NOT be adjacent to the LX net on the PCB layout to avoid the noise problem.
- 5) If the system chip interfacing with the EN pin has a high impedance state at shutdown mode and the IN pin is connected directly to a power source such as a LiIon battery, it is desirable to add a pull-down 1M Ω resistor between the EN and GND pins to prevent the noise from falsely turning on the regulator at shutdown mode.

Load Transient Considerations:

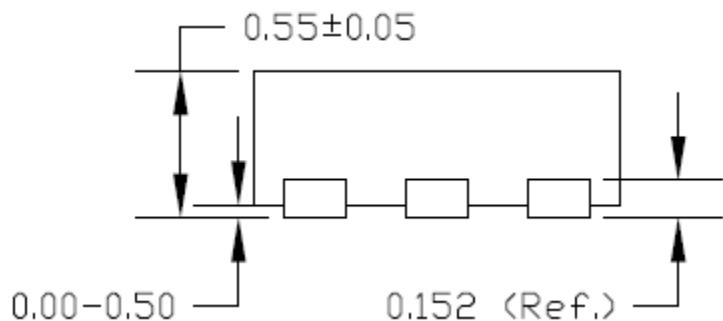
The SY8081 integrates the compensation components to achieve good stability and fast transient responses. In some applications, adding a 10pF ceramic cap in parallel with R_1 may further speed up the load transient responses and is thus recommended for applications with large load transient step requirements.

DFN1.5×1.5-6 Package Outline



Top View

Bottom View

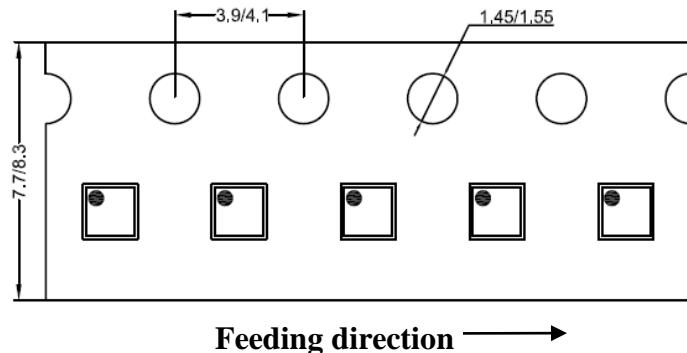


Side View

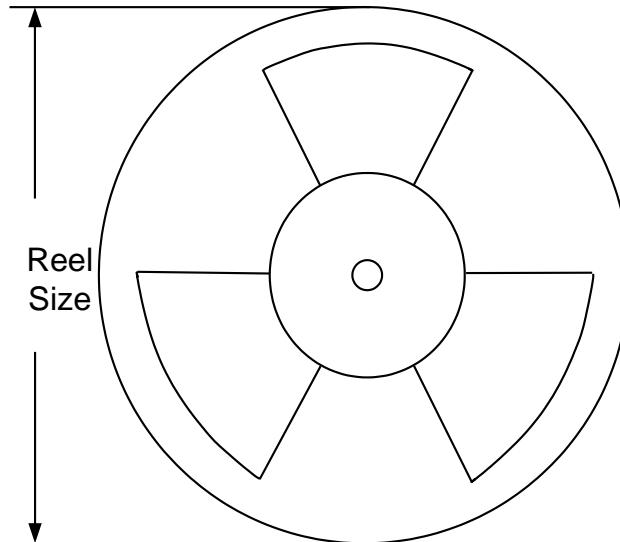
Notes: All dimension in millimeter and exclude mold flash & metal burr.

Taping & Reel Specification

1. DFN 1.5x1.5-6 taping orientation



2. Carrier Tape & Reel specification for packages



Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
DFN1.5x1.5	8	4	7"	400	160	3000

3. Others: NA

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