

SYM320240BZK

v3.4

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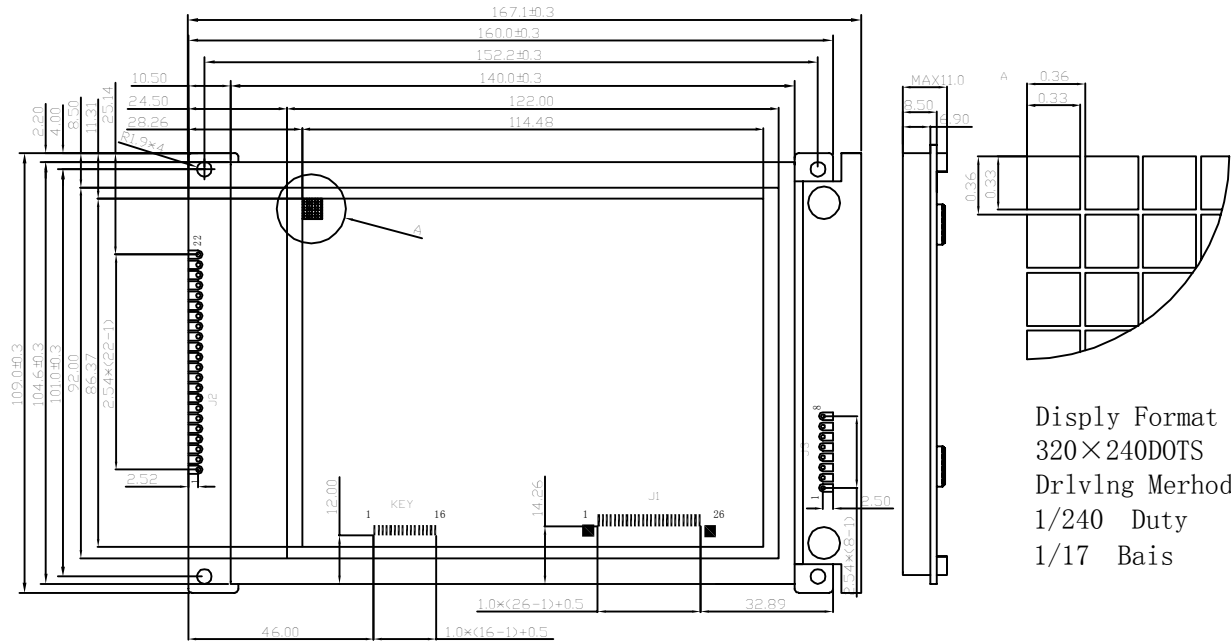
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① PHYSICAL DIMENSION/外型尺寸

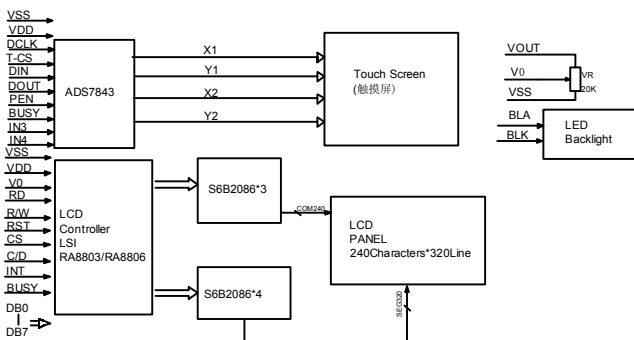


② FEATURES/特征

Feature(特征)	UNIT(单位)
LCD type & Colour(类型及颜色)	STN Blue ;FSTN
View Angle(视角)	6 0° clock
Operating temperature(工作温度)	-20°C ---- 70°C
Storage Temperature(贮存温度)	-30°C ---- 80°C
Control IC & Package(控制芯片及封装)	RA8803/RA8806 SMT
Weight(重量)	202 (With B/L) g
Backlight(背光类型)	LED

Ta=25°C	MIN(最小)	TYP(典型)	MAX(最大)	UNIT(单位)
Supply current don't contain backlight(无背光时电流)	0	69		mA
VDD(电源)	-0.3	5.0		V
VLCD-V0(LCD驱动电压)	0	-21.0		V
Backlight LED (背光电压, 电流)	侧背光	0	5.0	V
	CCFL	0	150	mA

④ BLOCK DIAGRAM/原理电路



③ PIN CONFIGURATION/管脚说明

PIN	SYMBOL	DESCRIPTION/定义描述(J2)
1	VSS	Power ground (地)
2	VDD	Power positive(逻辑电压)
3	V0	Operating voltage for LCD Driver(对比度调节)
4	R/W	Read or Write.(写数据)
5	RD	Register select(读信号)
6	CS	Chip select (片选)
7	C/D	H:Data L:Instruction Code(指令 数据信号)
8	RST	Reset Signal (复位)
9	DB0	Data bus [0~7]. There state I/O common terminal. (数据线0~7)
16	DB7	
17	VOUT	Negative voltage output(负压输出)
18	BLA	Power supply for backligh(+)(背光正极)
19	INT	Interrupt output Pin(中断)
20	BUSY	Busy Check Pin(忙信号)
21	BLA/NC	Power supply for backligh(+)(背光正极)
22	BLK/NC	Power supply for backligh(-)(背光负极)

PIN	SYMBOL	DESCRIPTION/定义描述(J3)
1	DCLK	Data Clock Signal(串行/转换时钟)
2	T-CS	Touch Pannal Select(片选)
3	DIN	Input Data(串行数据输入)
4	DOUT	Output Data(串行数据输出)
5	PEN	INT Signal(信号中断)
6	BUSY	Busy Signal(转换状态)
7	IN3	INT Signal(辅助输入)
8	IN4	INT Signal(辅助输入)

PIN (J1)	PIN (J1)	PIN (J1)	PIN (J1)	PIN (J1)	PIN (J1)
1 VSS	6 CS	11 DB2	16 DB7	21 BLK	26 BUSY
2 VDD	7 C/D	12 DB3	17 VOUT	22 DCLK	
3 V0	8 RST	13 DB4	18 INT	23 T-CS	
4 R/W	9 DB0	14 DB5	19 BUSY	24 DIN	
5 RD	10 DB1	15 DB6	20 BLA	25 PEN	