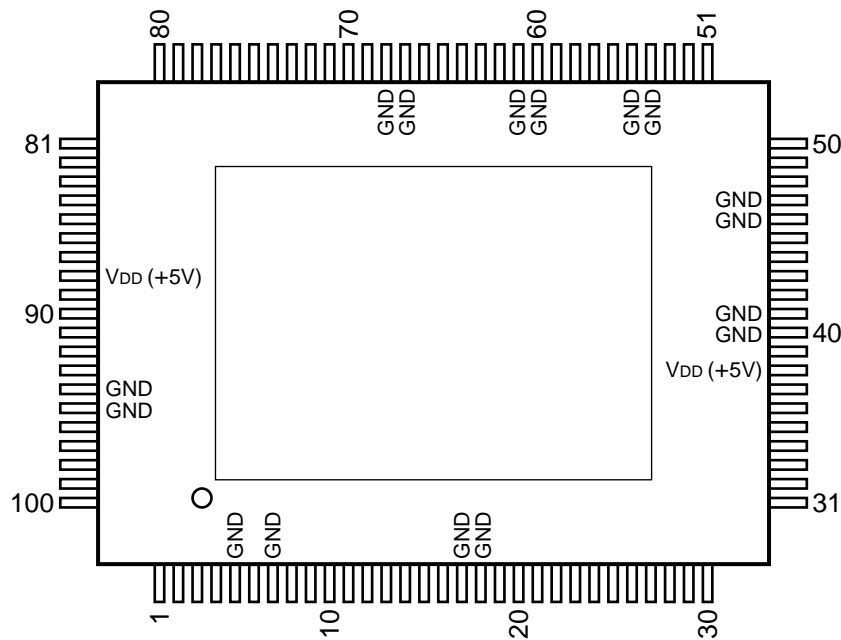

C-MOS FAST SCSI CONTROLLER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	$\overline{\text{DACK}}$	26	I/O	DB15	51	O	$\overline{\text{SDO7}}$	76	O	$\overline{\text{INT}}$
2	I	DBWR	27	I/O	DBP1	52	O	$\overline{\text{SDOP}}$	77	I	RESET
3	—	NC	28	—	NC	53	—	NC	78	—	NC
4	O	IGS	29	I/O	$\overline{\text{SDI0}}$	54	—	GND	79	I	WR
5	—	GND	30	I/O	$\overline{\text{SDI1}}$	55	—	GND	80	I	$\overline{\text{RD}}$
6	O	TGS	31	I/O	$\overline{\text{SDI2}}$	56	O	$\overline{\text{SELO}}$	81	I	CS
7	—	GND	32	I/O	$\overline{\text{SDI3}}$	57	O	$\overline{\text{BSYO}}$	82	I	A0-SA0
8	I/O	DB0	33	I/O	$\overline{\text{SDI4}}$	58	O	$\overline{\text{REQO}}$	83	I	A1-BHE
9	I/O	DB1	34	I/O	$\overline{\text{SDI5}}$	59	O	ACKO	84	I	A2-DBRD
10	I/O	DB2	35	I/O	$\overline{\text{SDI6}}$	60	—	GND	85	I	A3-ALE
11	I/O	DB3	36	I/O	$\overline{\text{SDI7}}$	61	—	GND	86	I	CLK
12	I/O	DB4	37	I/O	SDIP	62	I/O	$\overline{\text{MSGIO}}$	87	I	$\overline{\text{DIFFM}}$
13	I/O	DB5	38	—	V _{DD}	63	I/O	C/DIO	88	—	V _{DD}
14	I/O	DB6	39	—	NC	64	I/O	I/OIO	89	—	NC
15	I/O	DB7	40	—	GND	65	I/O	ATNIO	90	I/O	PAD0
16	I/O	DBP0	41	—	GND	66	O	$\overline{\text{RSTO}}$	91	I/O	PAD1
17	—	GND	42	O	$\overline{\text{SDO0}}$	67	—	GND	92	I/O	PAD2
18	—	GND	43	O	$\overline{\text{SDO1}}$	68	—	GND	93	I/O	PAD3
19	I/O	DB8	44	O	$\overline{\text{SDO2}}$	69	I	$\overline{\text{SELI}}$	94	—	GND
20	I/O	DB9	45	O	$\overline{\text{SDO3}}$	70	I	$\overline{\text{BSYI}}$	95	—	GND
21	I/O	DB10	46	—	GND	71	I	$\overline{\text{REQI}}$	96	I/O	PAD4
22	I/O	DB11	47	—	GND	72	I	ACKI	97	I/O	PAD5
23	I/O	DB12	48	O	$\overline{\text{SDO4}}$	73	I	$\overline{\text{RSTI}}$	98	I/O	PAD6
24	I/O	DB13	49	O	$\overline{\text{SDO5}}$	74	I	MODE1	99	I/O	PAD7
25	I/O	DB14	50	O	$\overline{\text{SDO6}}$	75	I	MODE0	100	O	DREQ

INPUT

A3 - A0	; ADDRESS (NON MULTIPLEX MODE)
$\overline{\text{ACKI}}$; SCSI ACKNOWLEDGE
ALE	; ADDRESS LATCH ENABLE (MULTIPLEX MODE)
BHE	; BUS HIGH ENABLE (MULTIPLEX MODE)
BSYI	; SCSI BUSY
CLK	; CLOCK
$\overline{\text{CS}}$; CHIP SELECT
$\overline{\text{DACK}}$; DMA ACKNOWLEDGE
$\overline{\text{DBRD}}$; DATA BUS READ (MULTIPLEX MODE)
$\overline{\text{DBWR}}$; DMA WRITE
$\overline{\text{DIFFM}}$; DIFFERENTIAL MODE ENABLE
MODE1, MODE0	; MODE SELECT
$\overline{\text{RD}}$; READ
$\overline{\text{REQI}}$; SCSI REQUEST
RESET	; CHIP RESET
$\overline{\text{RSTI}}$; SCSI RESET
SA0	; SYSTEM ADDRESS BUS (MULTIPLEX MODE)
$\overline{\text{SDI7}} - \overline{\text{SDI0}}$; SCSI DATA BUS
$\overline{\text{SDIP}}$; SCSI PARITY
$\overline{\text{SELI}}$; SCSI SELECT
$\overline{\text{WR}}$; WRITE

OUTPUT

ACKO	; ACKNOWLEDGE
BSYO	; SCSI BUS COMMAND BUSY
DREQ	; DMA REQUEST
IGS	; INITIATOR GROUP SELECT
$\overline{\text{INT}}$; INTERRUPT
REQO	; SCSI BUS COMMAND REQUEST
RSTO	; SCSI BUS COMMAND RESET
$\overline{\text{SDO7}} - \overline{\text{SDO0}}$; SCSI DATA BUS
$\overline{\text{SDOP}}$; SCSI PARITY
SELO	; SCSI BUS COMMAND SELECT
TGS	; TARGET GROUP SELECT

INPUT/OUTPUT

ATNIO	; 48 mA OPEN DRAIN OUT/SCHMITT TRIGGER IN
C/DIO	; SCSI PHASE
DB15 - DB0	; DATA BUS
DBP1	; ODD PARITY FROM DB15 - DB8
DBP0	; ODD PARITY FROM DB7 - DB0
$\overline{\text{I/OIO}}$; SCSI PHASE
$\overline{\text{MSGIO}}$; SCSI PHASE
PAD7 - PAD0	; PROCESSOR ADDRESS DATA BUS

SYM53CF96-2 (3/4)

82	A0 - SA0	SDI0	29
83	A1 - BHE	SDI1	30
84	A2 - DBRD	SDI2	31
85	A3 - ALE	SDI3	32
72	ACKI	SDI4	33
70	BSYI	SDI5	34
86	CLK	SDI6	35
81	CS	SDI7	36
1	DACK	SDIP	37
2	DBWR		
87	DIFFM	SDO0	42
75	MODE0	SDO1	43
74	MODE1	SDO2	44
80	RD	SDO3	45
71	REQI	SDO4	48
77	RESET	SDO5	49
73	RSTI	SDO6	50
69	SELI	SDO7	51
79	WR	SDOP	52
		SELO	56
8	DB0		
9	DB1	PAD0	90
10	DB2	PAD1	91
11	DB3	PAD2	92
12	DB4	PAD3	93
13	DB5	PAD4	96
14	DB6	PAD5	97
15	DB7	PAD6	98
19	DB8	PAD7	99
20	DB9		
21	DB10	TGS	6
22	DB11	ACKO	59
23	DB12	BSYO	57
24	DB13	DREQ	100
25	DB14	IGS	4
26	DB15	INT	76
		REOQ	58
		RSTO	66
		ATNIO	65
		C/DIO	63
		DBP0	16
		DBP1	27
		I/OIO	64
		MSGIO	62

