



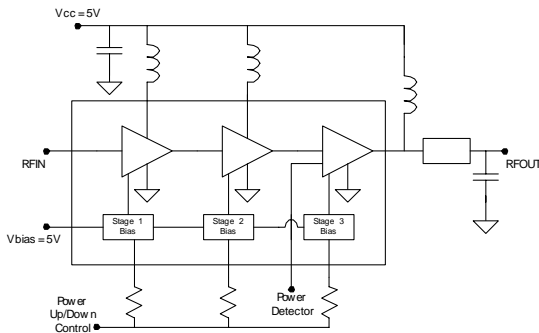
Preliminary

Product Description

Sirenza Microdevices' SZM-2166Z is a high linearity class AB Heterojunction Bipolar Transistor (HBT) amplifier housed in a low-cost surface-mountable plastic Q-FlexN multi-chip module package. This HBT amplifier is made with InGaP on GaAs device technology and fabricated with MOCVD for an ideal combination of low cost and high reliability.

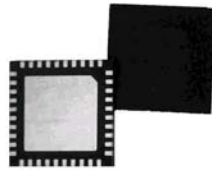
This product is specifically designed for 802.16 customer premise equipment (CPE) terminals in the 2.3-2.7 GHz bands. It can run from a 3V to 6V supply. The external output match and bias adjustability allows load line optimization for other applications or over narrower bands. It features an output power detector, on/off power control and high RF overdrive robustness. A 20dB step attenuator feature can be utilized by switching the second stage Power up/down control. This product features a RoHS compliant and Green package with matte tin finish, designated by the 'Z' suffix.

Functional Block Diagram



SZM-2166Z

2.3-2.7GHz 2W Power Amplifier



6mm x 6mm QFN Package



Product Features

- P1dB = 35dBm @ 6V
- Three Stages of Gain: 37dB
- 802.11g 54Mb/s Class AB Performance
- Pout = 27dBm @ 2.5% EVM, Vcc 6V, 878mA
- Active Bias with Adjustable Current
- On-chip Output Power Detector
- Low Thermal Resistance
- Power up/down control < 1μs
- Attenuator step 20dB @ Vpc2 = 0V

Applications

- 802.16 WiMAX Driver or Output Stage
- 802.11b/g WLAN, WiFi
- CPE Terminal Applications

Key Specifications

Symbol	Parameters: Test Conditions, 2.5-2.7GHz App circuit, Z ₀ = 50Ω, V _{CC} = 6.0V, I _q = 724mA, T _{BP} = 30°C	Unit	Min.	Typ.	Max.
f _O	Frequency of Operation	MHz	2300		2700
P _{1dB}	Output Power at 1dB Compression – 2.7GHz	dBm		35	
S ₂₁	Small Signal Gain – 2.7GHz	dB	34.5	36	
EVM%	EVM at 27dBm Output power EVM 802.11g 54Mb/s - 2.7GHz	%		2.5	
IM3	Third Order Suppression (Pout=23dBm per tone) - 2.7GHz	dBc		-40	-35
NF	Noise Figure at 2.7 GHz	dB		8.3	
IRL	Worst Case Input Return Loss 2.5-2.7GHz	dB	10	14	
ORL	Worst Case Output Return Loss 2.5-2.7GHz		13	17	
V _{det} Range	Output Voltage Range for Pout=10dBm to 33dBm	V		0.9 to 1.8	
I _{oq}	Quiescent Current (V _{cc} = 6V)	mA	615	724	832
I _{VPC}	Power Up Control Current (V _{pc} =6V, (I _{VPC1} + I _{VPC2} + I _{VPC3})	mA		4	
I _{leak}	V _{cc} Leakage Current (V _{cc} = 6V, V _{pc} = 0V)	μA			100
R _{th, j-l}	Thermal Resistance (junction - lead)	°C/W		12	

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EDS-105683 Rev B

Typical Performance with appropriate app circuit(V_{cc}=6V, *I_{cq}=655mA, * 802.11g 54Mb/s 64QAM)**

Parameter	Units	**2.3GHz	**2.4GHz	***2.5GHz	***2.6GHz	***2.7GHz
Gain @ Pout=26dBm	dB	37.5	37.5	37.5	37	35
P1dB	dBm	34	34	35	35	35
EVM% at 27dBm Output Power*	%	2.3	2.9	1.7	1.7	2.5
Current @ Pout 2.5% EVM*	mA	768	779	900	889	878
Input Return Loss	dB	23	21	14	14	14
Output Return Loss	dB	14	11	20	25	18

**Measured with 2.3-2.4GHz Application circuit. See page 11 for details.

***Measured with 2.5-2.7GHz Application circuit. See page 13 for details

Absolute Maximum Ratings

Parameters	Value	Unit
VC3 Collector Bias Current (I _{VC3})	1500	mA
VC2 Collector Bias Current (I _{VC2})	500	mA
VC1 Collector Bias Current (I _{VC1})	150	mA
*****Device Voltage (V _D)	9.0	V
Power Dissipation	6	W
Operating Lead Temperature (T _L)	-40 to +85	°C
****Max CW RF output Power for 50 ohm continuous long term operation	30	dBm
Max CW RF Input Power for 50 ohm output load	26	dBm
Max CW RF Input Power for 10:1 VSWR RF out load	5	dBm
Max Storage Temperature	+150	°C
Operating Junction Temperature (T _J)	+150	°C
ESD Human Body Model	Class 1B	

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias conditions should also satisfy the following expression:

$$I_{cq} V_{cc} < (T_J - T_L) / R_{TH} \quad j-1$$

Note: I_{cq} in this equation is for the stage with the highest current

**** With specified application circuit.

*****No RF Drive



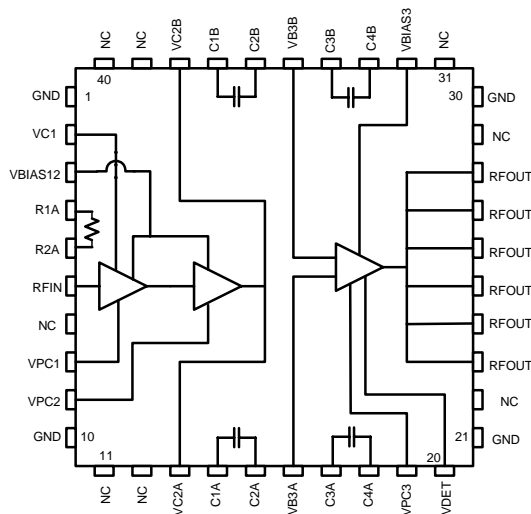
Caution: ESD Sensitive

Appropriate precaution in handling, packaging and testing devices must be observed.

Pin Out Description

Pin #	Function	Description
7, 11, 12, 22, 29, 31, 39, 40	NC	These are no connect (NC) pins and are not wired inside the package. It is recommended to connect them as shown in the application circuit to achieve the stated performance.
1, 10, 21, 30	GND	These pins are internally grounded inside the package to the backside ground paddle. It is recommended to also ground them external to the package to achieve the specified performance.
2	VC1	This is the collector of the first stage.
3	VBIAS12	This is the supply voltage for the active bias circuit of the 1st and 2nd stages.
4-5	R1A-R2A	A resistor is tied across these pins internal to the package.
6	RFIN	This is the RF input pin. It is DC grounded inside the package. Do not apply DC voltage to this pin.
8	VPC1	Power up/down control pin for the 1st stage. An external series resistor is required for proper setting of bias levels depending on control voltage. The voltage on this pin should never exceed the voltage on pin 3 by more than 0.5V unless the supply current from pin 3 is limited < 10mA.
9	VPC2	Power up/down control pin for the 2nd stage. Power down VPC<1V for step attenuator function enable. An external series resistor is required for proper setting of bias levels depending on control voltage. The voltage on this pin should never exceed the voltage on pin 3 by more than 0.5V unless the supply current from pin 3 is limited < 10mA.
13, 38	VC2A, VC2B	These two pins are connected internal to the package and connect to the 2nd stage collector. To achieve specified performance, the layout of these pins should match the Recommended Land Pattern, pg. 13.
14-15 17-18 33-34 36-37	C1A-C2A C3A-C4A C4B-C3B C2B-C1B	These pins have capacitors across them internal to the package as shown in the below schematic. They are used as tuning and RF coupling elements between the 2nd and 3rd stage.
16, 35	VB3A, VB3B	These are the connections to the base of the 3rd stage output device. To achieve specified performance, the layout of these pins should match the Recommended Land Pattern, pg. 13.
19	VPC3	Power up/down control pin for the 2nd stage. An external series resistor is required for proper setting of bias levels depending on control voltage. The voltage on this pin should never exceed the voltage on pin 32 by more than 0.5V unless the supply current from pin 33 is limited < 10mA.
20	VDET	This is the output port for the power detector. It samples the power at the input of the 3rd stage.
23-28	RFOUT	These are the RF output pins and DC connections to the 3rd stage collector.
32	VBIAS3	This is the supply voltage for the active bias circuit of the 3rd stage.

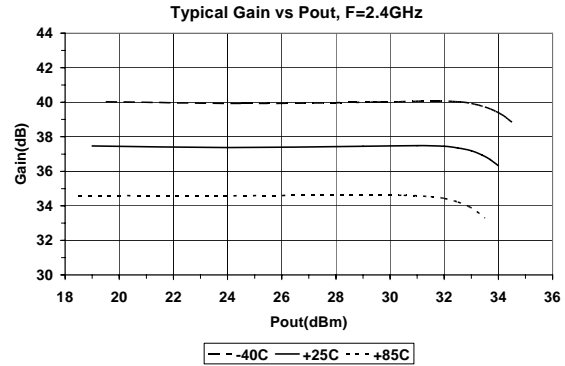
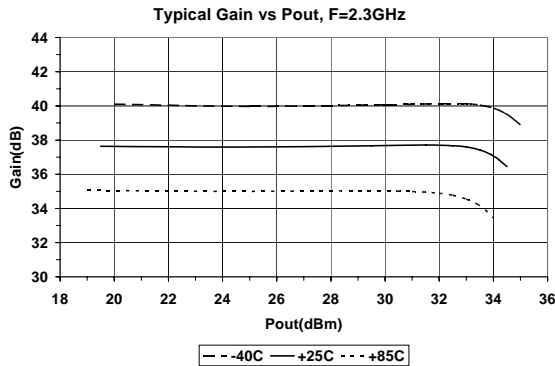
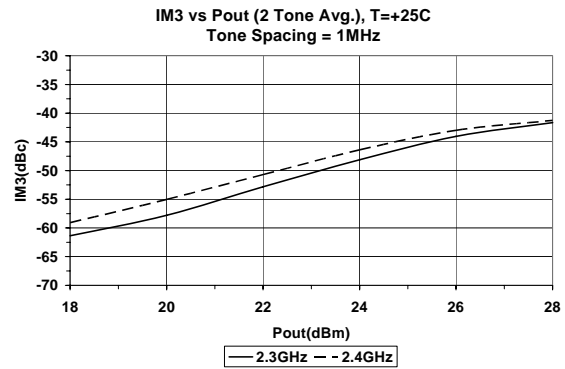
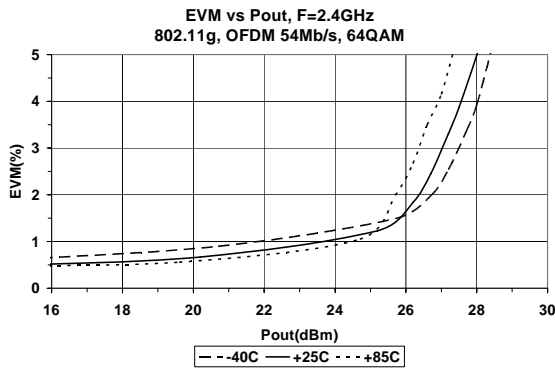
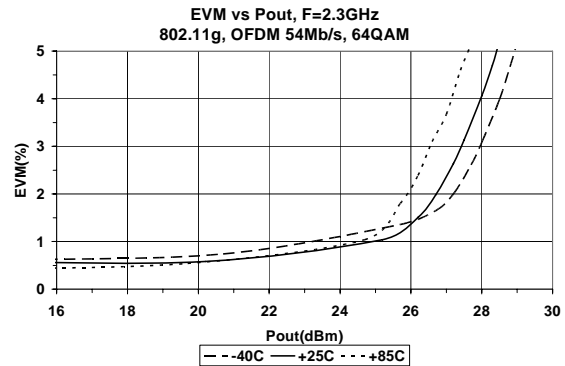
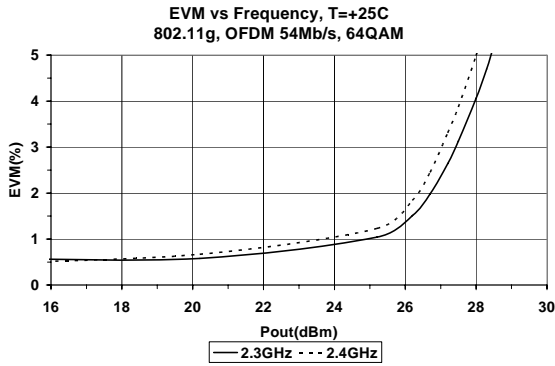
Simplified Device Schematic





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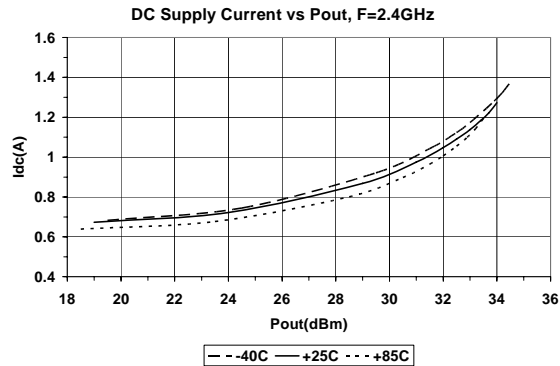
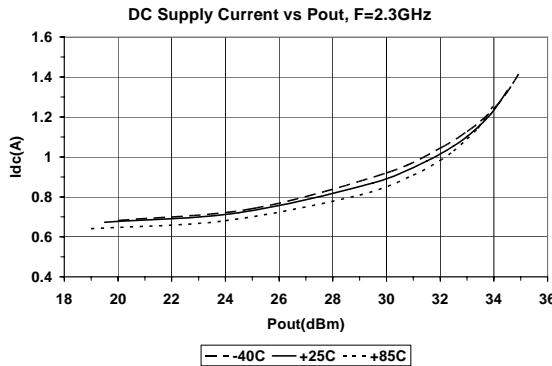
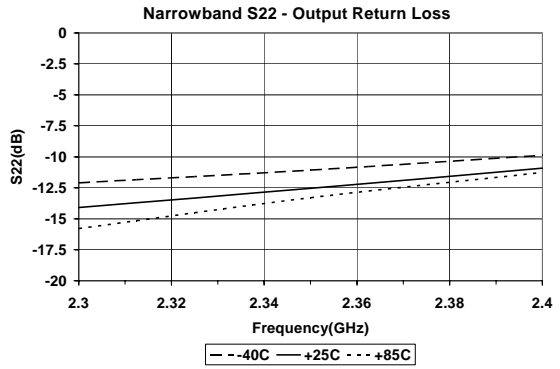
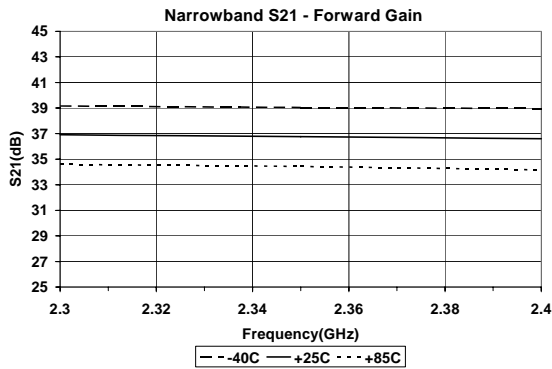
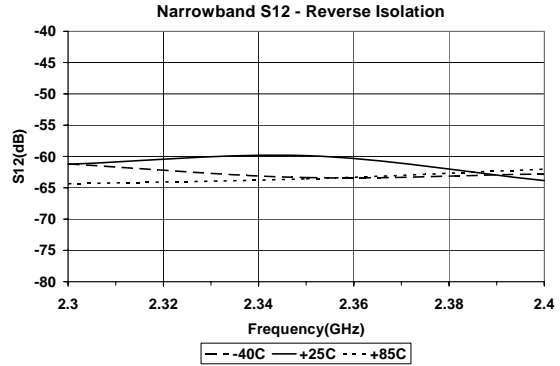
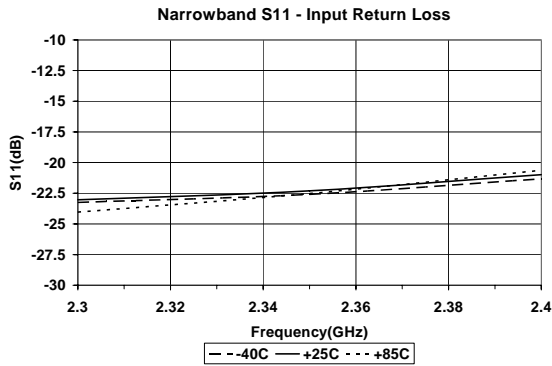
Measured 2.3-2.4 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 653mA$, $T=25C$)



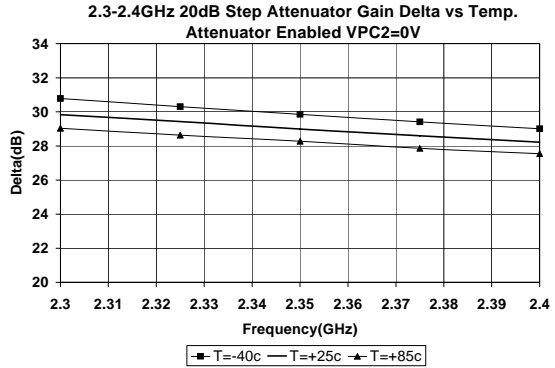
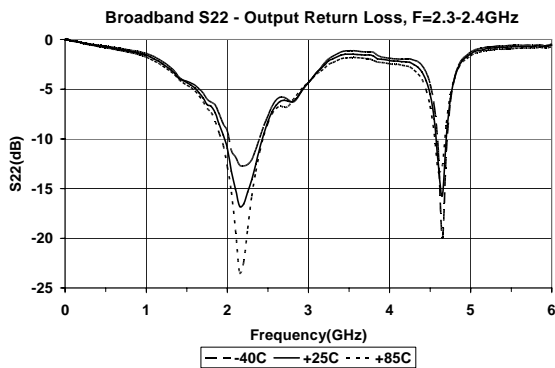
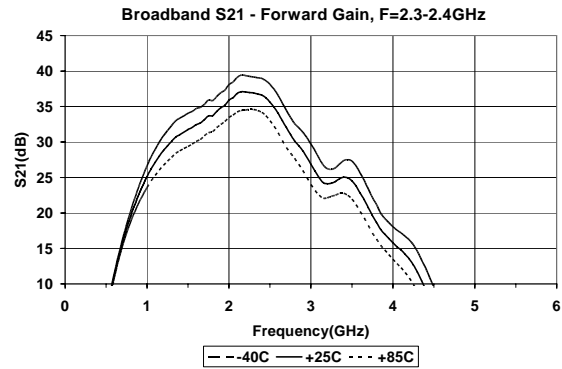
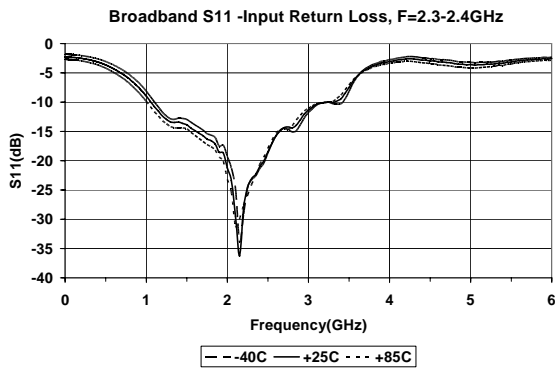
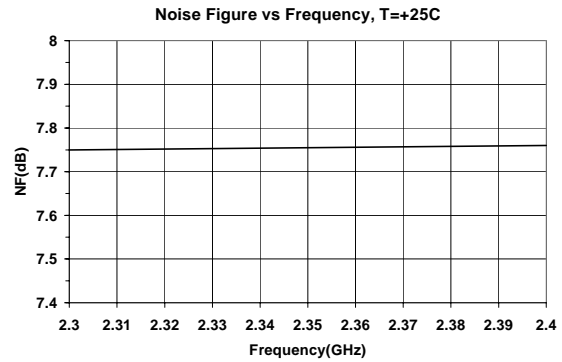
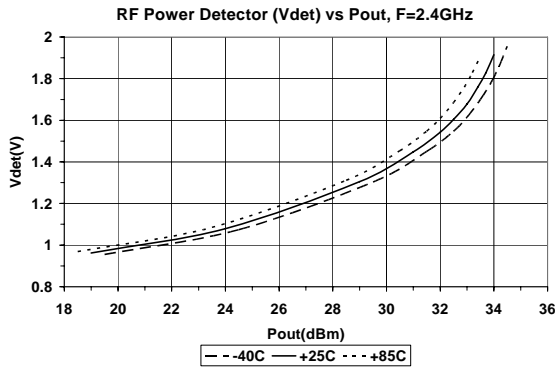


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Measured 2.3-2.4 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 653mA$, $T=25C$)



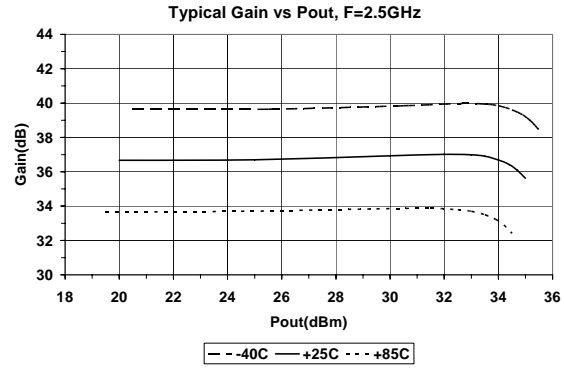
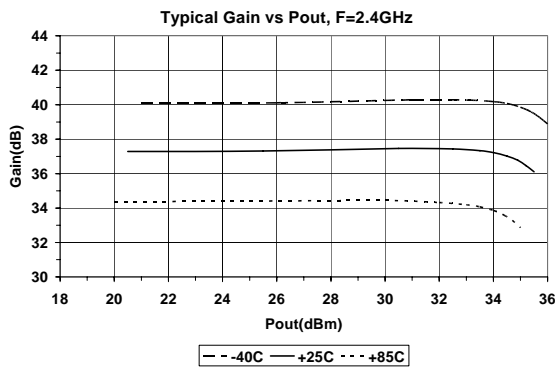
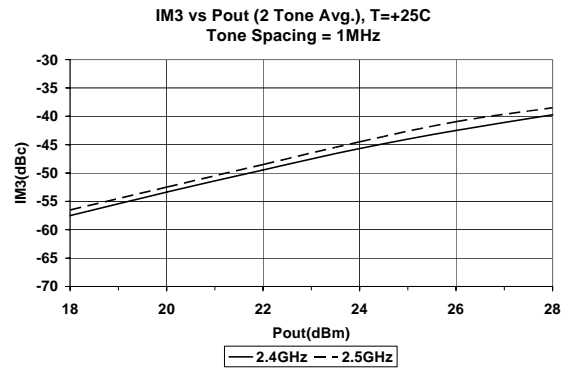
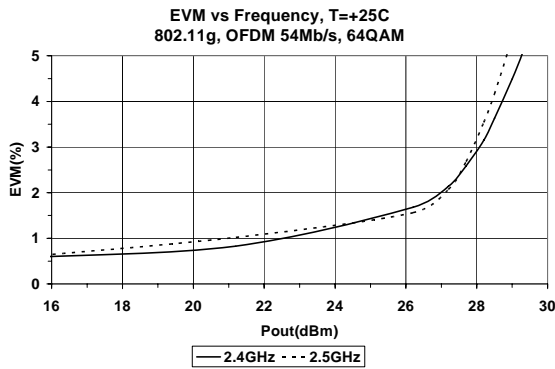
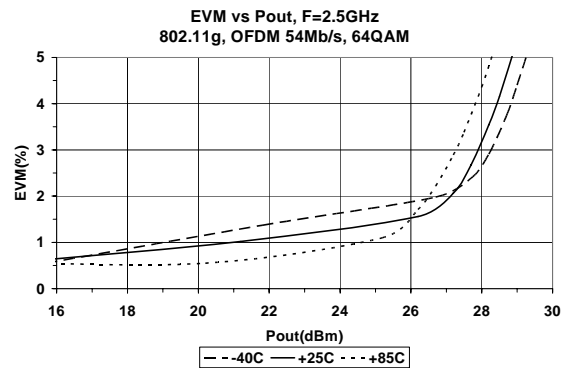
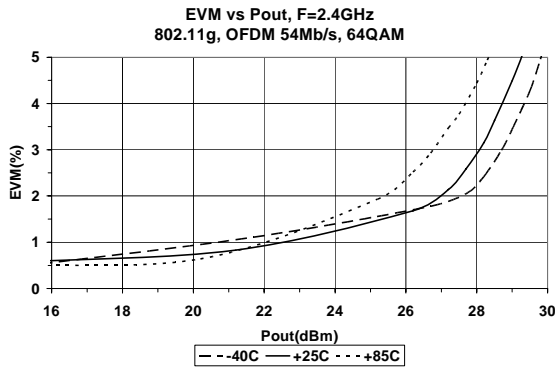
Measured 2.3-2.4 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 653mA$, $T=25C$)





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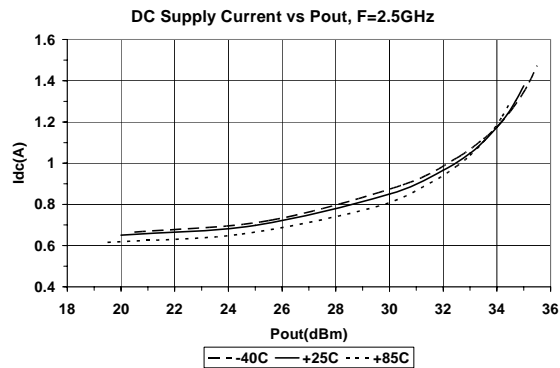
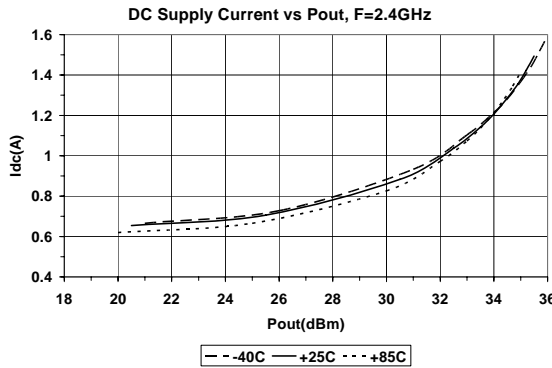
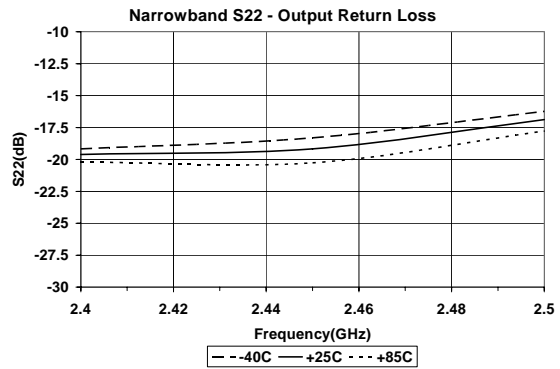
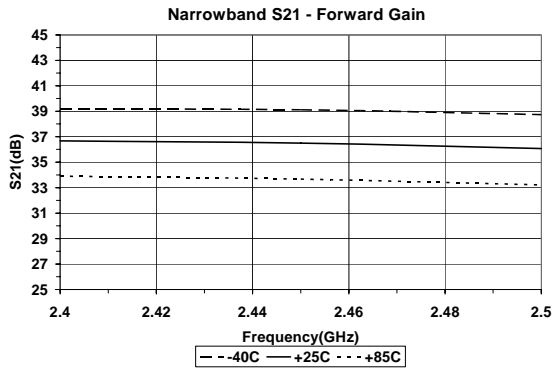
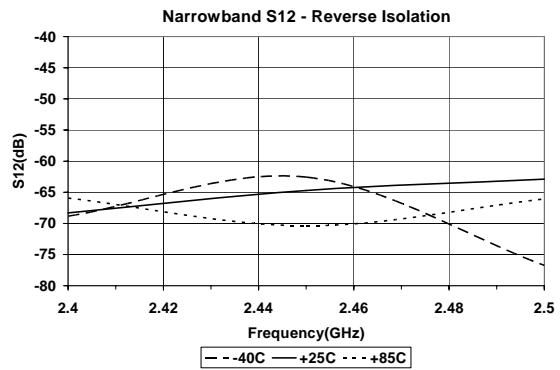
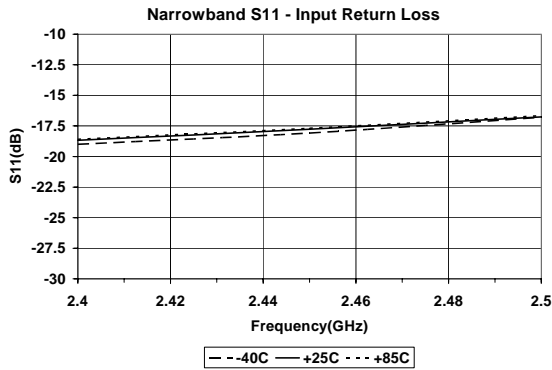
Measured 2.4-2.5 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 632mA$, $T=25C$)



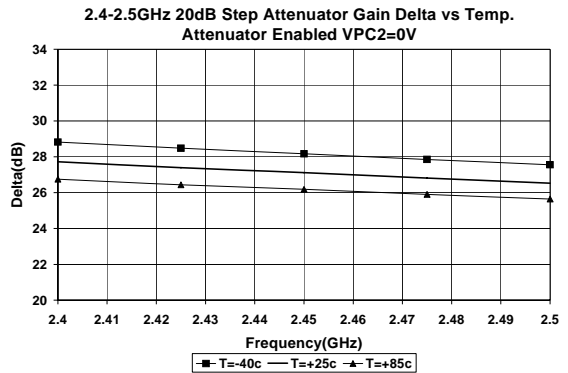
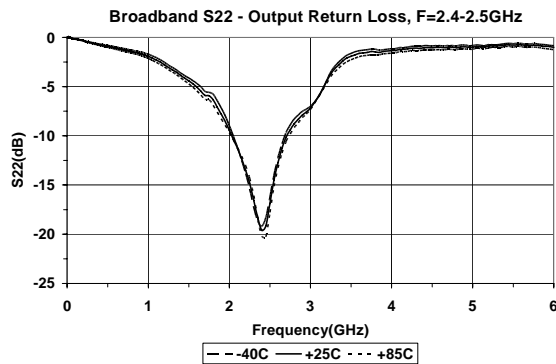
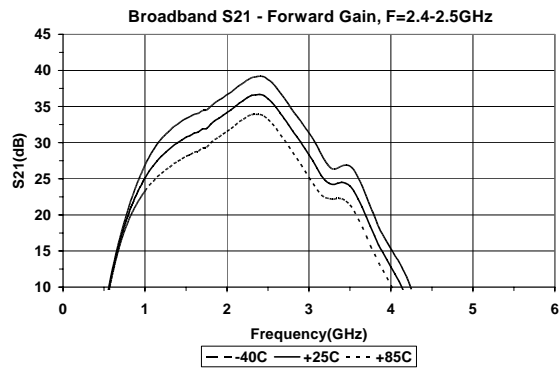
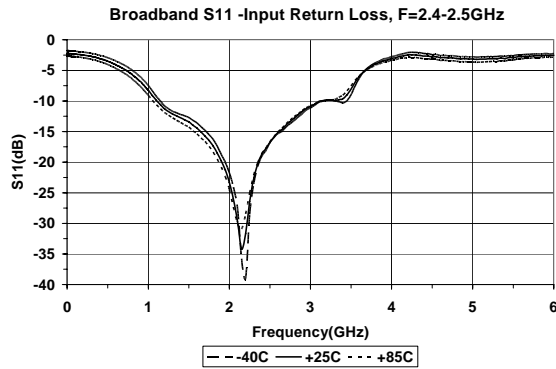
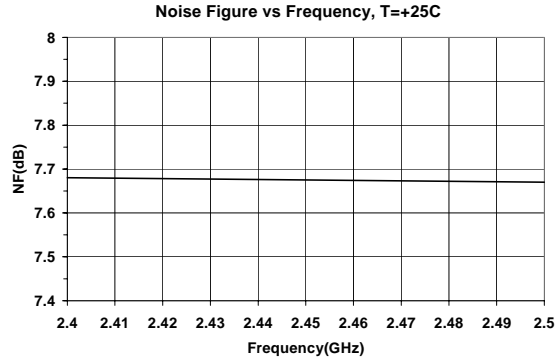
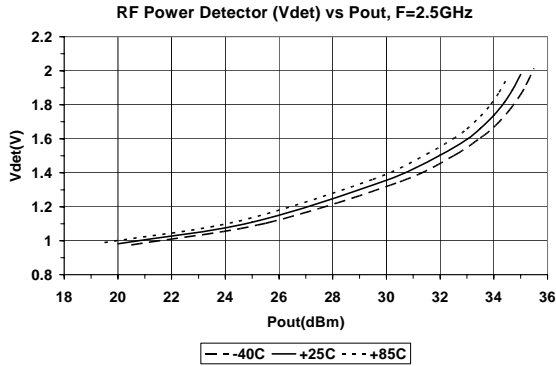


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Measured 2.4-2.5 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 632mA$, $T=25C$)



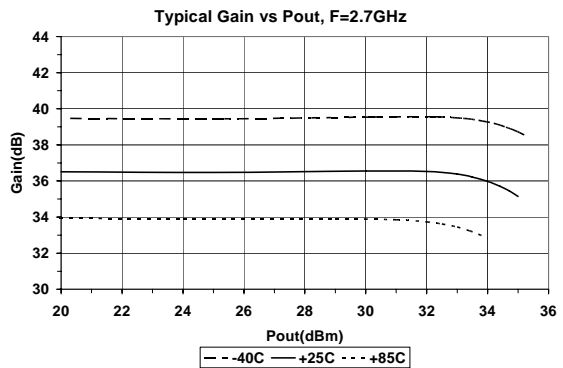
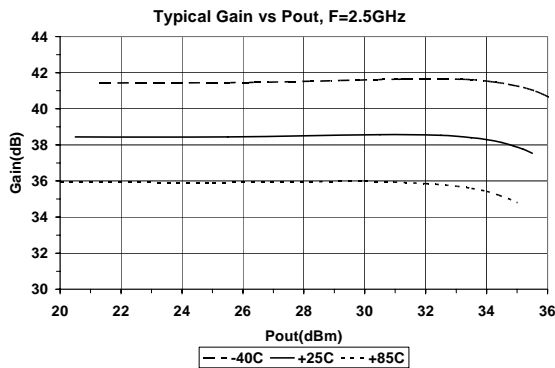
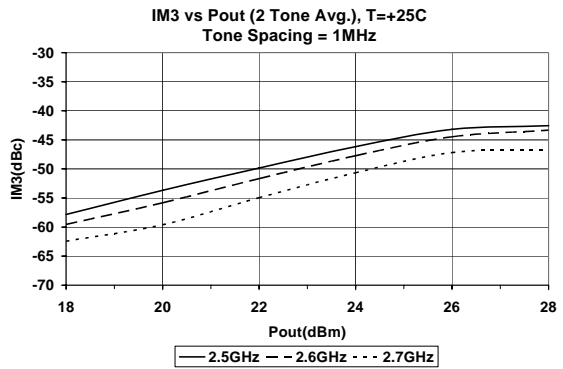
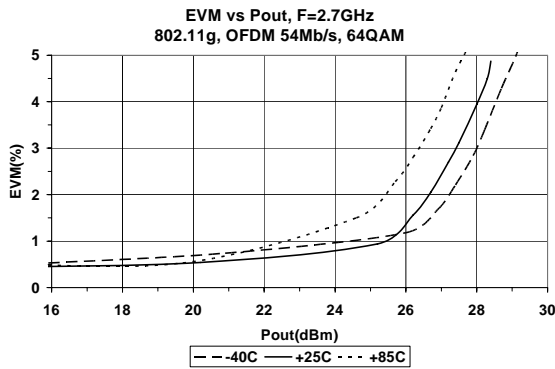
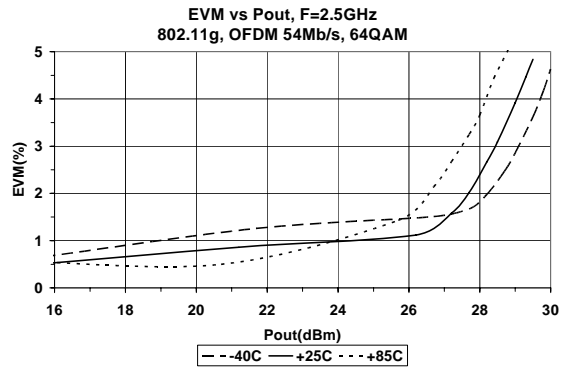
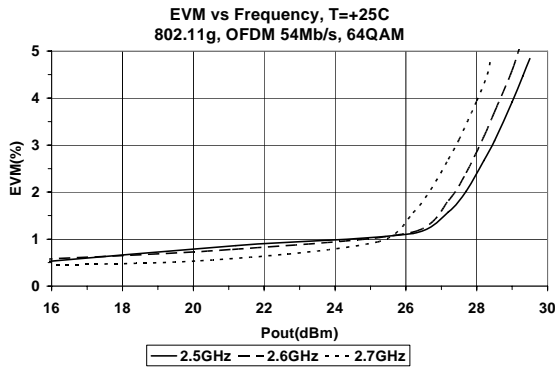
Measured 2.4-2.5 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 632mA$, $T=25C$)





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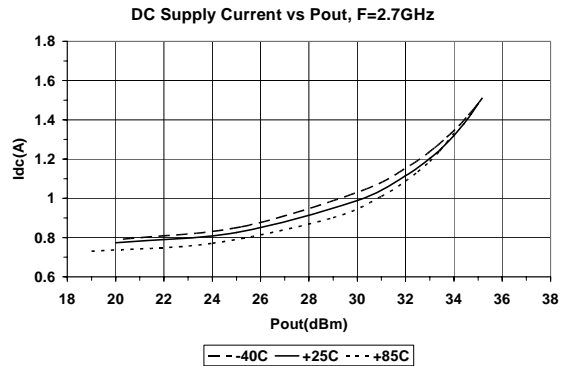
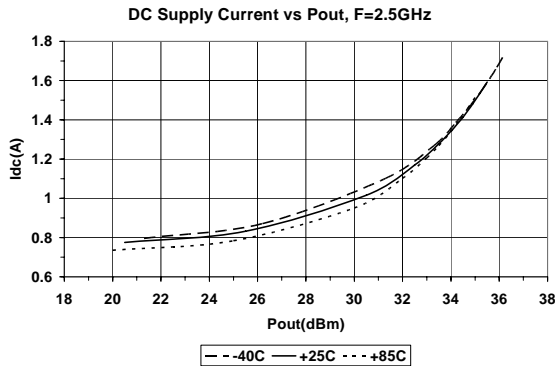
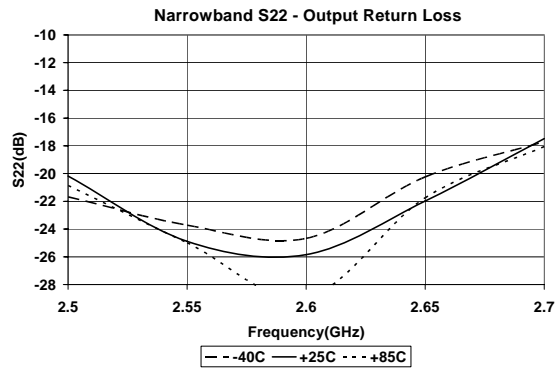
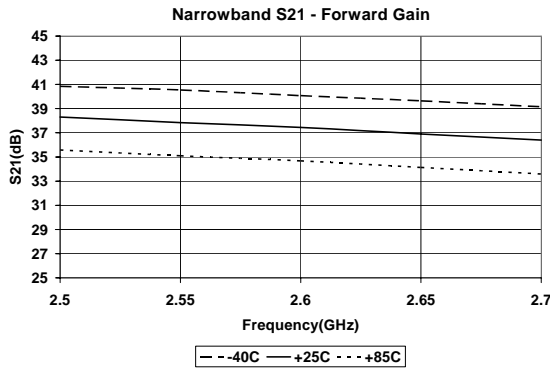
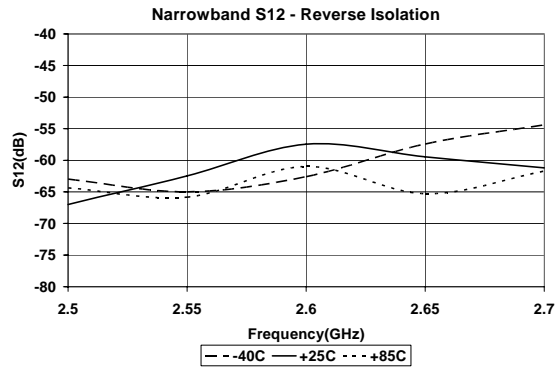
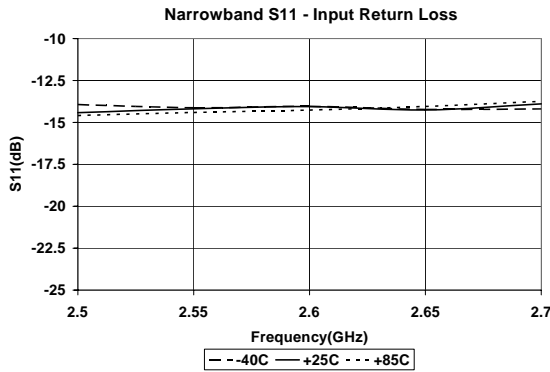
Measured 2.5-2.7 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 724mA$, $T=25C$)





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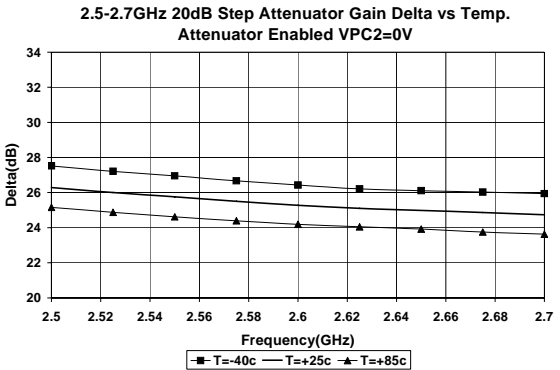
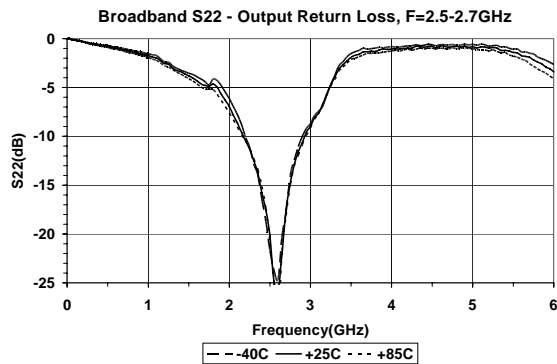
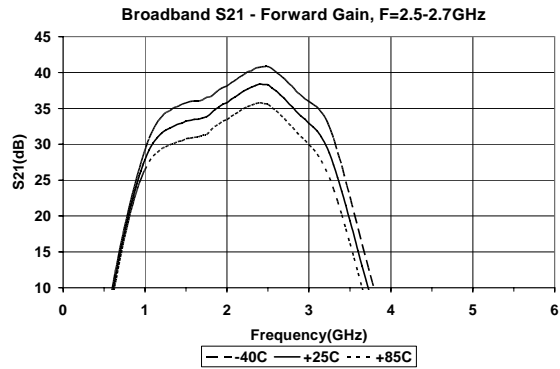
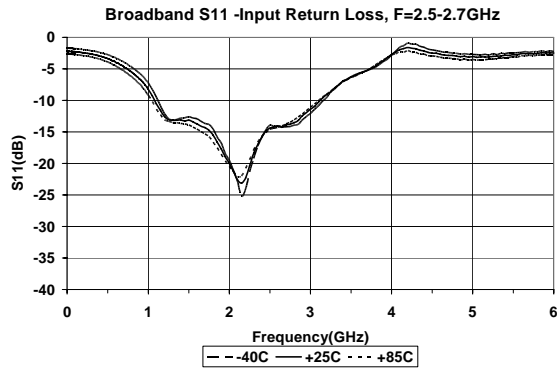
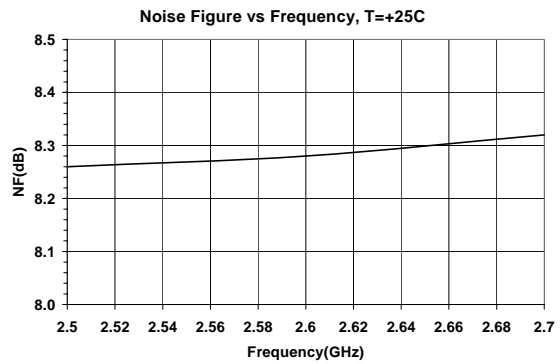
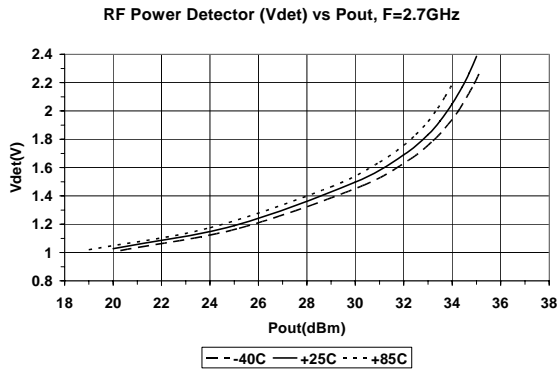
Measured 2.5-2.7 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 724mA$, $T=25C$)



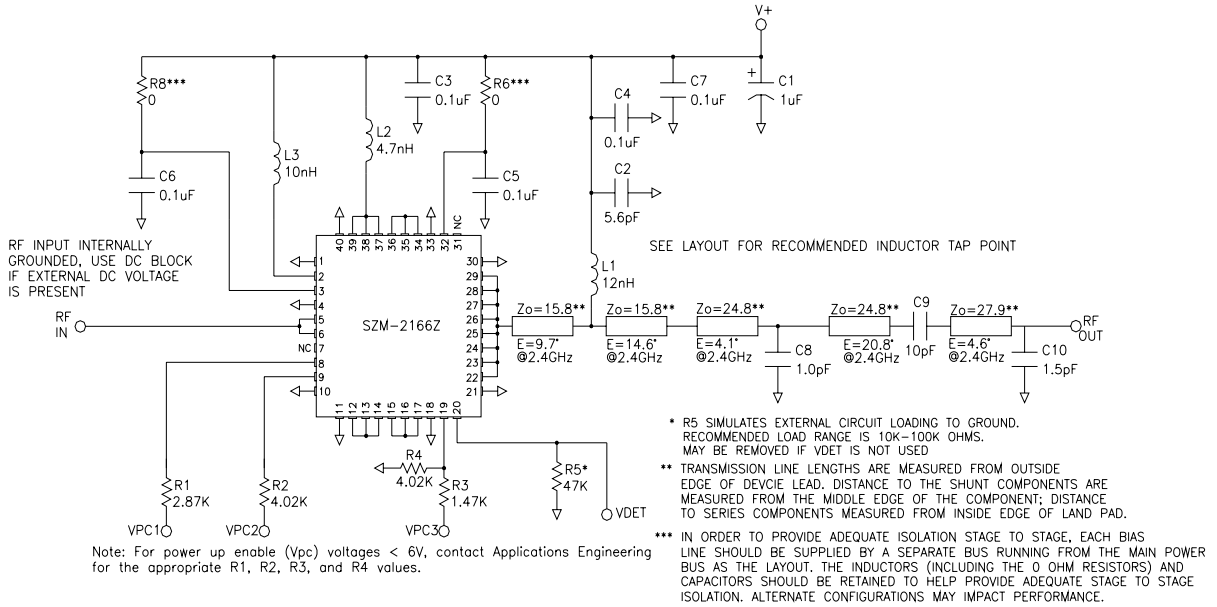


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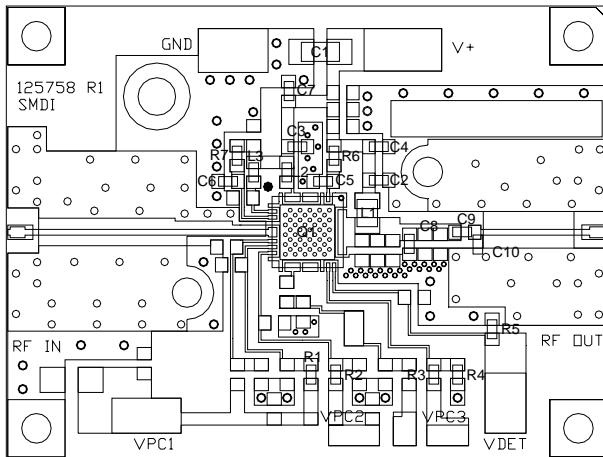
Measured 2.5-2.7 GHz Application Circuit Data ($V_{CC} = V_{PC} = 6.0V$, $I_q = 724mA$, $T=25C$)



2.3-2.4 GHz Evaluation Board Schematic For Vcc = V+ = Vpc = 6.0V

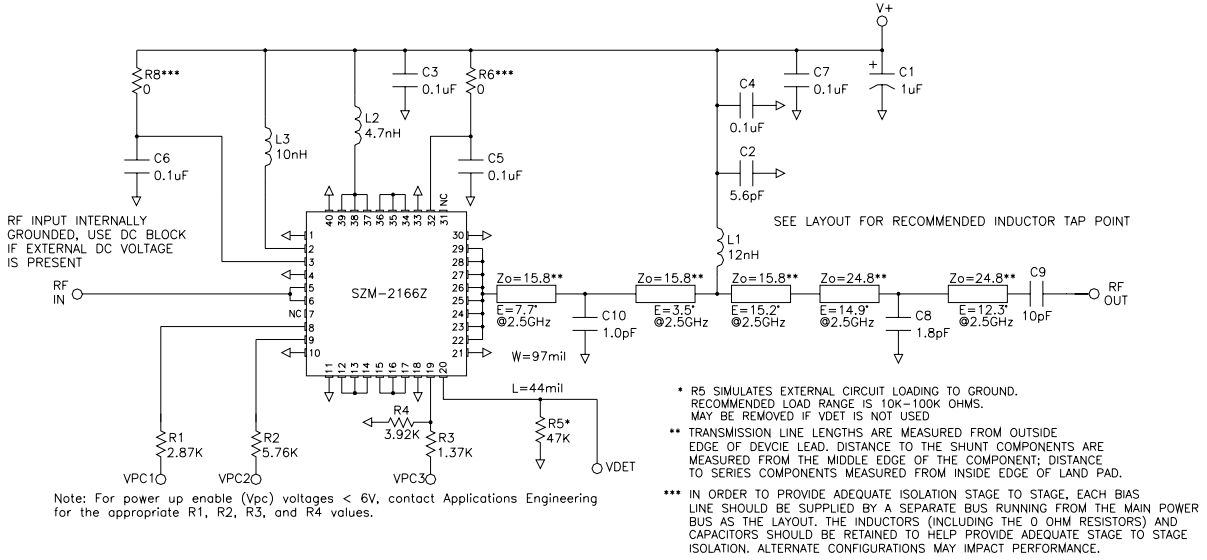


2.3-2.4 GHz Evaluation Board Layout For Vcc = V+ = Vpc = 6.0V
 Board material GETEK, 10mil thick, Dk=3.9, 2 oz. copper

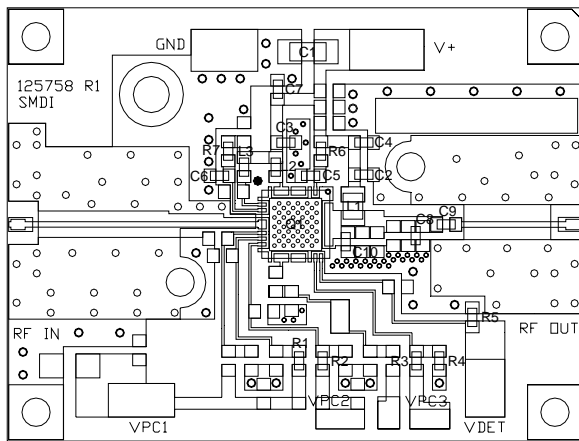


DESG	DESCRIPTION	NOTES
Q1	SZM-2166Z	6x6mm QFN
R1	2.87K OHM, 0603 1%	0402 may be used
R2	4.02K OHM, 0603 1%	"
R3	1.47K OHM, 0603 1%	"
R4	4.02K OHM, 0603 1%	"
R5	47K OHM, 0603	"
R6,7	0 OHM, 0603	"
C1	1uF 16V MLCC CAP	Tantalum ok for EVM performance Use MLCC type for best IM3 levels
C2	5.6pF CAP, 0603	NPO ROHM MCH185A5R6DK or equiv.
C3,4,5,6,7	0.1uF CAP, 0603	NPO 0402 ok ROHM MCH184CN105K or equiv.
C8	1.0pF CAP, 0603	NPO, low ESR ATC 60051ROCW250 or equiv.
C9	10pF CAP, 0603	NPO, low ESR ATC 6005100JW250 or equiv.
C10	1.5pF CAP, 0603	NPO, low ESR ATC 60051R5JW250 or equiv.
L1	12nH IND 0805	Coilcraft 0805HQ - 12NXJBB
L2	4.7nH IND, 0603	TOKO 0603 - LL1608FH4N7J
L3	10nH IND, 0603	TOKO 0603 - LL1608FH10N1

2.4-2.5 GHz Evaluation Board Schematic For Vcc = V+ = Vpc = 6.0V

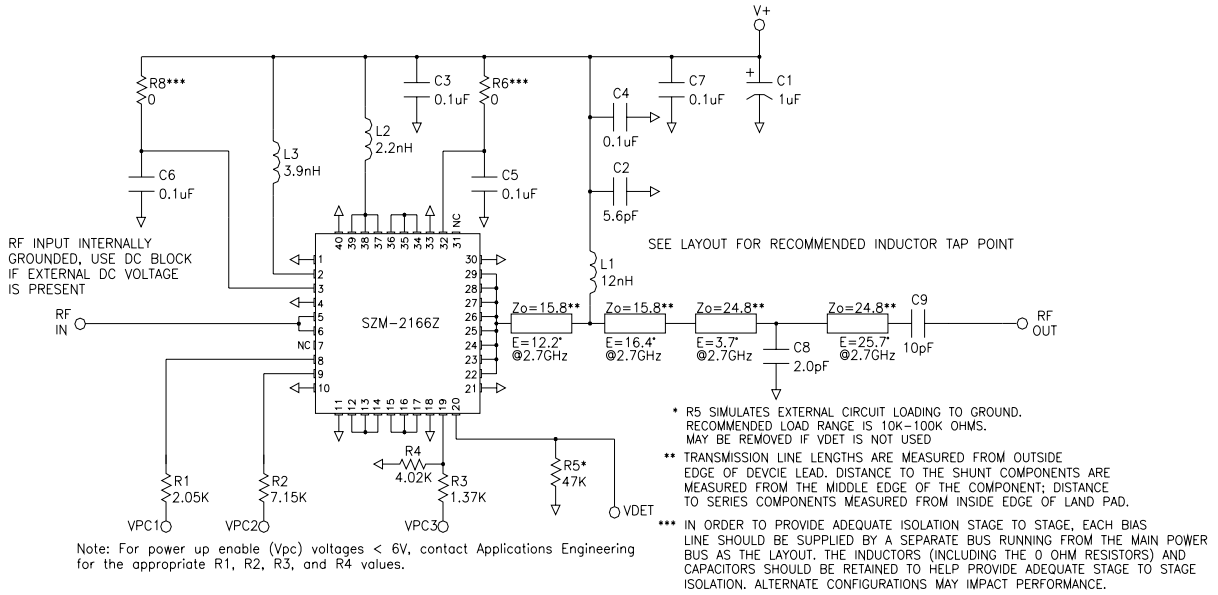


2.4-2.5 GHz Evaluation Board Layout For Vcc = V+ = Vpc = 6.0V
Board material GETEK, 10mil thick, Dk=3.9, 2 oz. copper

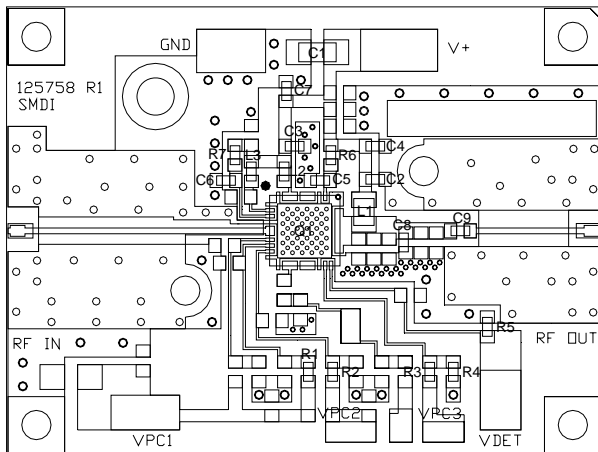


DESG	DESCRIPTION	NOTES
Q1	SZM-2166Z	6x6mm QFN
R1	2.87K OHM, 0603 1%	0402 may be used
R2	5.76K OHM, 0603 1%	"
R3	1.37K OHM, 0603 1%	"
R4	3.92K OHM, 0603 1%	"
R5	47K OHM, 0603	"
R6,7	0 OHM, 0603	"
C1	1uF 16V MLCC CAP	Tantalum ok for EVM performance Use MLCC type for best IM3 levels
C2	5.6pF CAP, 0603	NPO ROHM MCH185A5R6DK or equiv.
C3,4,5,6,7	0.1uF CAP, 0603	NPO 0402 ok ROHM MCH184CN105K or equiv.
C8	1.8pF CAP, 0603	NPO, low ESR ATC 60051R8CW250 or equiv.
C9	10pF CAP, 0603	NPO, low ESR ATC 6005100JW250 or equiv.
C10	1.0pF CAP, 0603	NPO, low ESR ATC 60051R0JW250 or equiv.
L1	12nH IND 0805	Coilcraft 0805HQ - 12NXJBB
L2	4.7nH IND, 0603	TOKO 0603 - LL1608FH4N7J
L3	10nH IND, 0603	TOKO 0603 - LL1608FH10NJ

2.5-2.7 GHz Evaluation Board Schematic For Vcc = V+ = Vpc = 6.0V



2.5-2.7 GHz Evaluation Board Layout For Vcc = V+ = Vpc = 6.0V
Board material GETEK, 10mil thick, Dk=3.9, 2 oz. copper



DESG	DESCRIPTION	NOTES
Q1	SZM-2166Z	6x6mm QFN
R1	2.05K OHM, 0603 1%	0402 may be used
R2	4.99K OHM, 0603 1%	"
R3	1.37K OHM, 0603 1%	"
R4	4.02K OHM, 0603 1%	"
R5	47K OHM, 0603	"
R6,7	0 OHM, 0603	"
C1	1uF 16V MLCC CAP	Tantalum ok for EVM performance Use MLCC type for best IM3 levels
C2	5.6pF CAP, 0603	NPO ROHM MCH185A5R6DK or equiv.
C3,4,5,6,7	0.1uF CAP, 0603	NPO 0402 ok ROHM MCH184CN105K or equiv.
C8	2.0pF CAP, 0603	NPO, low ESR ATC 600S2R0CW250 or equiv.
C9	10pF CAP, 0603	NPO, low ESR ATC 600S100JW250 or equiv.
L1	12nH IND 0805	Coilcraft 0805HQ - 12NXJBB
L2	2.2nH IND, 0603	TOKO 0603 - LL1608FH2N2J
L3	3.9nH IND, 0603	TOKO 0603 - LL1608FH3N9J



Preliminary

SZM-2166Z 2.3-2.7GHz 2W Power Amp

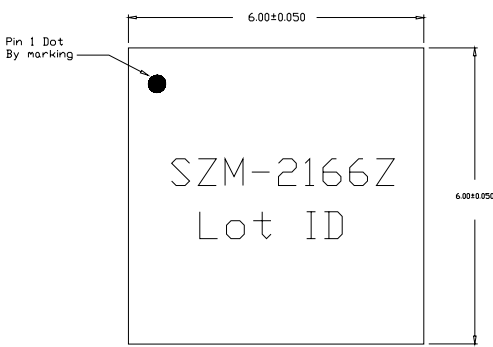
Part Symbolization

The part will be symbolized with "SZM2166Z" to designate it as a RoHs green compliant product. Marking designator will be on the top surface of the package.

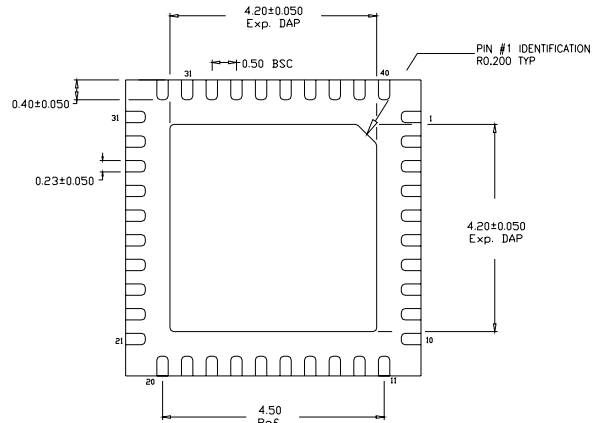
Part Number Ordering Information

Part Number	Description	Reel Size	Devices/Reel
SZM-2166Z	Lead Free, RoHs compliant	7"	1000
SZM-2166Z-EVB1	2.3-2.4GHz Evaluation Board	N/A	N/A
SZM-2166Z-EVB2	2.4-2.5GHz Evaluation Board	N/A	N/A
SZM-2166Z-EVB3	2.5-2.7GHz Evaluation Board	N/A	N/A

Package Outline Drawing (dimensions in mm):



TOP VIEW

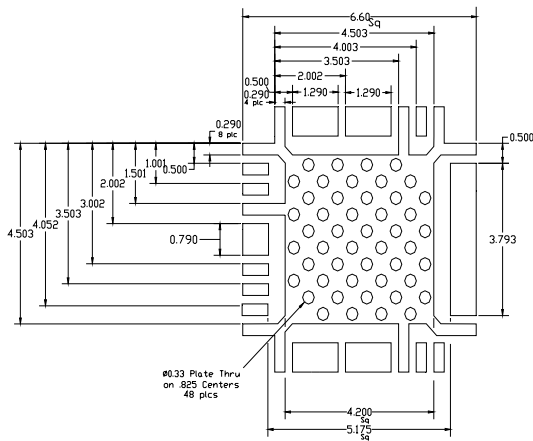


BOTTOM VIEW



SIDE VIEW

Recommended Metal Land Pattern (dimensions in mm[in]):



Recommended PCB Soldermask for Land Pattern (dimensions in mm[in]):

