

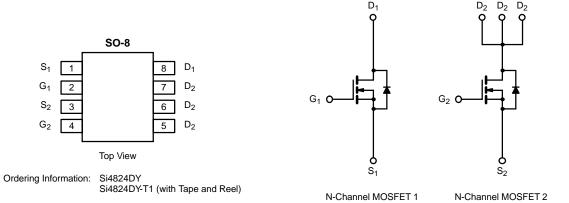


# Asymmetric N-Channel, Reduced $\mathbf{Q}_{\mathbf{g}}$ , Fast Switching MOSFET

PRODUCT SUMMARY						
	V <sub>DS</sub> (V)	$r_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
N-Channel 1	30	0.040 @ V <sub>GS</sub> = 10 V	4.7			
		0.065 @ V <sub>GS</sub> = 4.5 V	3.7			
N-Channel 2		0.0175 @ V <sub>GS</sub> = 10 V	9			
		0.027 @ V <sub>GS</sub> = 4.5 V	7.3			

#### **FEATURES**

- High-Efficiency
- PWM Optimized
- 100% R<sub>g</sub> Tested



ABSOLUTE MAXIMUM RATINGS (TA = 25°C UNLESS OTHERWISE NOTED)							
Parameter		Symbol	N-Channel 1	N-Channel 2	Unit		
Drain-Source Voltage		V <sub>DS</sub>	30	30	V		
Gate-Source Voltage		V <sub>GS</sub>	±20	±20			
0 / D : 0 //T /F00000 h	T <sub>A</sub> = 25°C	- I <sub>D</sub>	4.7	9			
Continuous Drain Current (T <sub>J</sub> = 150°C) <sup>a, b</sup>	T <sub>A</sub> = 70°C		3.7	7.2			
Pulsed Drain Current		I <sub>DM</sub>	40	60	A		
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		I <sub>S</sub>	1.2	2.0			
Maniana Bana Biada atau ah	T <sub>A</sub> = 25°C	Б	1.4	2.25	w		
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70°C	P <sub>D</sub>	0.9	1.5			
Operating Junction and Storage Temperature Range	•	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	-55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter			Symbol	Typical	Maximum	Unit	
	N-Ch 1	t ≤10 sec	R <sub>thJA</sub>		90		
		Steady State		125			
Maximum Junction-to-Ambient <sup>a</sup>	N-Ch 2	t ≤10 sec			55	°C/W	
		Steady State		80			

#### Notes

a. Surface Mounted on FR4 Board.

b.  $t \le 10$  sec.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm

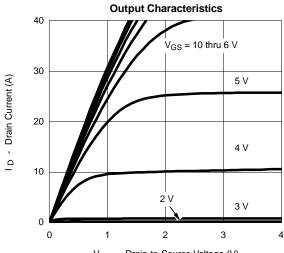


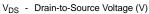
SPECIFICATIONS (T <sub>J</sub> = 25°C UNLESS OTHERWISE NOTED)  Parameter Symbol Test Condition Min Typ Max Unit									
	Symbol	Test Condition	IVIII	Тур	Max	Unit			
Static			1		ı	1	ı		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	N-Ch 1	1.0			V		
		103 103, 10 200 p. 1	N-Ch 2	1.0			·		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	N-Ch 1			±100	nA		
			N-Ch 2			±100	11/1		
	Ipss	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch 1			1	- μΑ		
Zero Gate Voltage Drain Current			N-Ch 2			1			
	.033	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	N-Ch 1			5			
			N-Ch 2			5			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch 1	20			Α		
	D(OII)		N-Ch 2	30					
		$V_{GS} = 10 \text{ V}, I_D = 4.7 \text{ A}$	N-Ch 1		0.033	0.040			
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10 \ V, I_D = 9 \ A$	N-Ch 2		0.014	0.0175	Ω		
Stain Goding on State Registance		$V_{GS} = 4.5 \text{ V}, I_D = 3.7 \text{ A}$	N-Ch 1		0.048	0.065	35		
		$V_{GS} = 4.5 \text{ V}, I_D = 7.3 \text{ A}$	N-Ch 2		0.020	0.027			
Forward Transconductancea	_	$V_{DS} = 15 \text{ V}, I_{D} = 4.7 \text{ A}$	N-Ch 1		12		s		
Forward Transconductance	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 9 \text{ A}$	N-Ch 2		25				
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_S = 1.2 A, V_{GS} = 0 V$	N-Ch 1	N-Ch 1 0.7		1.2	V		
blode i diward voltage-		$I_S = 2.0 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch 2		0.7	1.2	V		
Dynamic <sup>b</sup>									
			N-Ch 1		6.5	10	nC		
Total Gate Charge	Q <sub>g</sub>	N-Channel 1	N-Ch 2		17.5	27			
		$V_{DS} = 15 \text{ V}, \ V_{GS} = 5 \text{ V}, \ I_D = 4.7 \text{ A}$	N-Ch 1		3.0				
Gate-Source Charge		N-Channel 2 $V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 9 \text{ A}$	N-Ch 2		7.5				
		30 4 60 4 2	N-Ch 1		2.5				
Gate-Drain Charge	$\mathbf{Q}_{gd}$		N-Ch 2		6.5				
			N-Ch 1	0.1		2.7	Ω		
Gate Resistance	R <sub>g</sub>		N-Ch 2	0.1		2.3			
T 0 D T	t <sub>d(on)</sub>		N-Ch 1		10	20			
Turn-On Delay Time		N-Channel 1	N-Ch 2		15	30	ns		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ =15 $\Omega$	N-Ch 1		12	20			
		$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$ N-Channel 2	N-Ch 2		15	30			
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$	N-Ch 1		20	35			
		$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$	N-Ch 2		45	70			
Fall Time	_		N-Ch 1		10	20			
	t <sub>f</sub>		N-Ch 2		20	35			
Source Drain Powerse Becovery Time		I <sub>F</sub> = 1.2 A, di/dt = 100 A/μs N-Ch 1			40	80			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.0 A, di/dt = 100 A/μs	N-Ch 2		40	80			

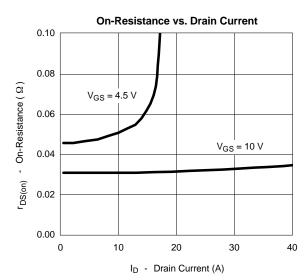




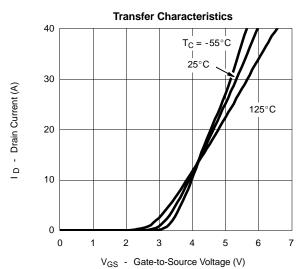
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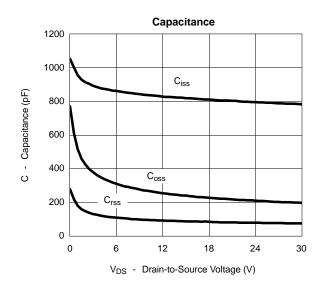


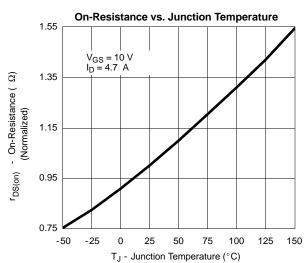




**Gate Charge** 10  $V_{DS} = 15 V$  $I_{D} = 4.7 A$ VGS - Gate-to-Source Voltage (V) 8 6 4 2 0 0 3 6 9 12 Q<sub>g</sub> - Total Gate Charge (nC)

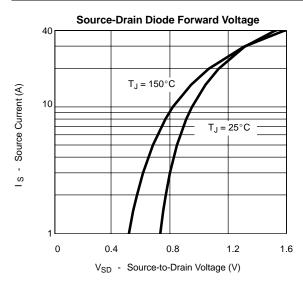


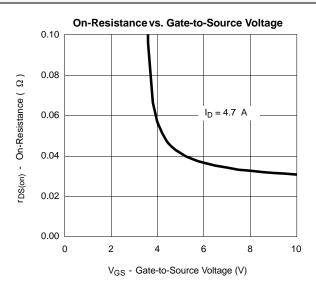


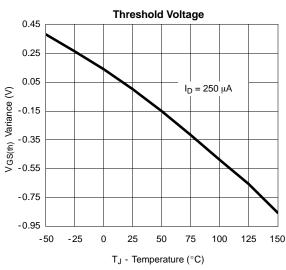


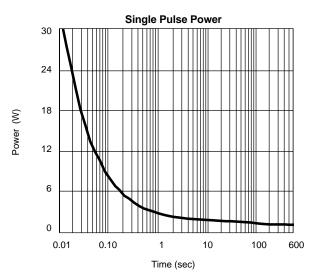


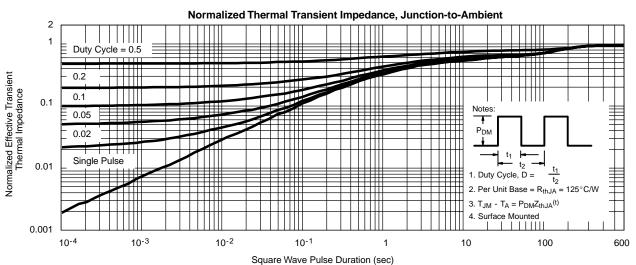
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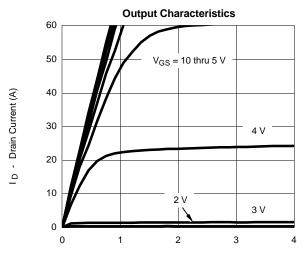




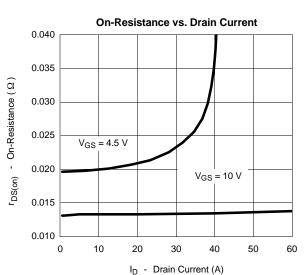




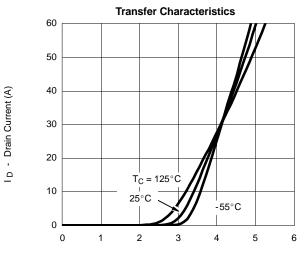
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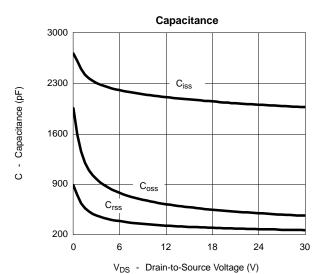
V<sub>DS</sub> - Drain-to-Source Voltage (V)

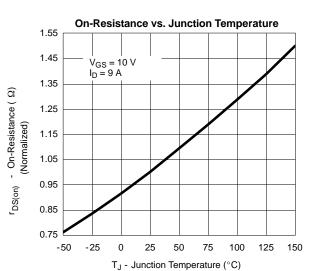


**Gate Charge** 10  $V_{DS} = 15 V$  $I_D = 9 A$ V GS - Gate-to-Source Voltage (V) 8 6 4 2 0 0 8 12 20 24 32 16 28 Q<sub>g</sub> - Total Gate Charge (nC)



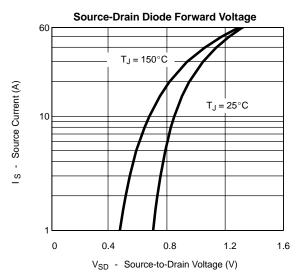
V<sub>GS</sub> - Gate-to-Source Voltage (V)

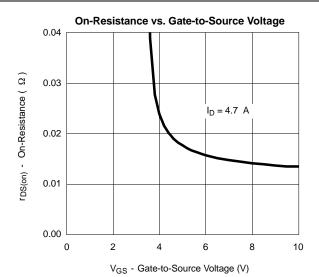


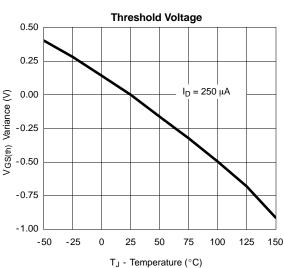


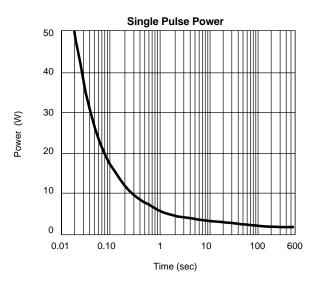


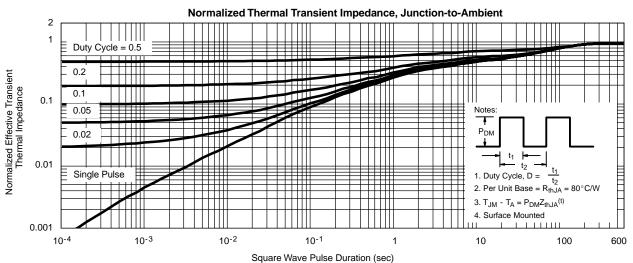
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