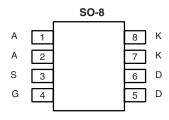


P-Channel 20-V (D-S) MOSFET with Schottky Diode

| PRODUCT SUMMARY | | | | | | |
|---------------------|------------------------------------|---------------------------------|-----------------------|--|--|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) ^a | Q _g (Typ.) | | | |
| - 20 | 0.210 at V _{GS} = - 4.5 V | - 2.7 | 2.9 | | | |
| - 20 | 0.345 at V _{GS} = - 2.5 V | - 2.1 | 2.9 | | | |

| SCHOTTKY PRODUCT SUMMARY | | | | | |
|--------------------------|---|--------------------|--|--|--|
| V _{KA} (V) | V _F (V) Diode Forward Voltage | I _F (A) | | | |
| 20 | 0.50 V at 1.0 A | 2.4 | | | |



Top View

Ordering Information: Si4845DY-T1-E3 (Lead (Pb)-free)

Si4845DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

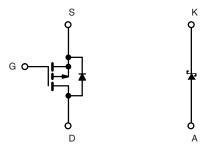
FEATURES

- Halogen-free According to IEC 61249-2-21 **Definition**
- LITTLE FOOT[®] Plus Integrated Schottky
 Compliant to RoHS Directive 2002/95/EC

COMPLIANT **HALOGEN** FREE

APPLICATIONS

· Asynchronous dc-to-dc Buck



P-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS TA | , , | | | | |
|--|-----------------------------------|------------------|-----------------------|------|--|
| Parameter | | Symbol | Limit | Unit | |
| Drain-Source Voltage (MOSFET) | V_{DS} | - 20 | | | |
| Reverse Voltage (Schottky) | V_{KA} | - 20 | V | | |
| Gate-Source Voltage (MOSFET) | | V_{GS} | ± 12 | | |
| | T _C = 25 °C | | - 2.7 | | |
| Continuous Drain Current (T _{.I} = 150 °C) (MOSFET) | T _C = 70 °C | I _D | - 2.1 | | |
| Continuous Diam Curient (1) = 130 C) (MOSI E1) | T _A = 25 °C | 'D | - 2.1 ^{b, c} | | |
| | T _A = 70 °C | | - 1.7 ^{b, c} | | |
| Pulsed Drain Current (MOSFET) | | I _{DM} | - 7 | A | |
| Continuous Source-Drain Diode Current | T _C = 25 °C | - I _S | - 2.4 | | |
| (MOSFET Diode Conduction) | T _A = 25 °C | | - 1.9 ^{b, c} | | |
| Average Forward Current (Schottky) | | I _F | 1 ^b | | |
| Pulsed Foward Current (Schottky) | I _{FM} | - 7 | | | |
| | T _C = 25 °C | | 2.75 | | |
| Maximum Power Dissipation (Schottley) | T _C = 70 °C | D_ | 1.75 | w | |
| Maximum Power Dissipation (Schottky) | T _A = 25 °C | P _D | 1.75 ^{b, c} | vv | |
| | T _A = 70 °C | 7 | 1.1 ^{b, c} | | |
| Operating Junction and Storage Temperature Range | T _J , T _{sta} | - 55 to 150 | °C | | |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|-------------------|------|------|--------|--|
| Parameter | Symbol | Тур. | Max. | Unit | |
| Maximum Junction-to-Ambient (MOSFET and Schottky) | R _{thJA} | 60 | 71.5 | °C/W | |
| Maximum Junction-to-Foot (Drain) (MOSFET and Schottky) | R_{thJF} | 35 | 45 |] 'C/W | |

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 120 °C/W.



| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|-------------------------|--|-------|--------|-------|-------|--|
| Static | | | I | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$ | - 20 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | | | - 25 | | | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | I _D = - 250 μA | | 2.6 | | mV/°C | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = -250 \mu A$ | - 0.5 | | - 1.5 | V | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$ | | | ± 100 | nA | |
| Zarra Cata Valtarra Duais Commant | | V _{DS} = - 20 V, V _{GS} = 0 V | | | - 1 | μА | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 75 ^{\circ}\text{C}$ | | | - 10 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$ | - 5 | | | Α | |
| Drain-Source On-State Resistance ^a | | V _{GS} = - 4.5 V, I _D = - 2 A | | 0.175 | 0.210 | Ω | |
| Drain-Source On-State Resistance | $R_{DS(on)}$ | V _{GS} = - 2.5 V, I _D = - 1.0 A | | 0.285 | 0.345 | 5.2 | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 15 V, I _D = - 2 A | | 3.5 | | S | |
| Dynamic ^b | | | | | | | |
| Input Capacitance | C _{iss} | | | 312 | | pF | |
| Output Capacitance | C _{oss} | $V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | 63 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 33 | | | |
| Total Gate Charge | Q _g | | | 2.9 | 4.5 | nC | |
| Gate-Source Charge | Q _{gs} | $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4 \text{ A}$ | | 0.72 | | | |
| Gate-Drain Charge | Q _{gd} | | | 0.65 | | | |
| Gate Resistance | R_{g} | f = 1 MHz | | 5.5 | | Ω | |
| Turn-On Delay Time | t _{d(on)} | $V_{DD} = -10 \text{ V}, R_L = 2.5 \Omega$ $I_D \cong -4 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$ | | 8 | 13 | ns | |
| Rise Time | t _r | | | 40 | 60 | | |
| Turn-Off DelayTime | t _{d(off)} | | | 17 | 26 | | |
| Fall Time | t _f | | | 11 | 18 | | |
| Turn-On Delay Time | t _{d(on)} | | | 3 | 6 | | |
| Rise Time | t _r | $V_{DD} = -10 \text{ V}, R_1 = 2.5 \Omega$ | | 10 | 16 | | |
| Turn-Off DelayTime | t _{d(off)} | $I_D \cong -4 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$ | | 12 | 20 | | |
| Fall Time | t _f | 1 | | 8 | 15 | | |
| Drain-Source Body Diode Characteris | tics | | | | | | |
| Continuous Source-Drain Diode | I _S | T _C = 25 °C | | | - 2.7 | | |
| Current | | .0 20 0 | | | | Α | |
| Pulse Diode Forward Current | I _{SM} | 1 1011 | | 0.05 | - 7 | ., | |
| Body Diode Voltage | V _{SD} | $I_S = -1.9 \text{ A}, V_{GS} = 0 \text{ V}$ | | - 0.85 | - 1.2 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | _ | | 24 | 40 | ns | |
| Body Diode Reverse Recovery Charge | Q _{rr} | I _F = - 2 A, dl/dt = 100 A/μs, T _J = 25 °C | | 14 | 20 | nC | |
| Reverse Recovery Fall Time | t _a | | | 14 | | ns | |
| Reverse Recovery Rise Time | t _b | | | 10 | | | |

Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

| SCHOTTKY SPECIFICATIONS $T_J = 25$ °C, unless otherwise noted | | | | | | | |
|--|-----------------|--|------|------|------|------|--|
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
| Forward Voltage Drop | V _F | I _F = 1 A | | 0.45 | 0.50 | V | |
| Torward Voltage Drop | | I _F = 1 A, T _J = 125 °C | | 0.36 | 0.42 | ' | |
| | I _{rm} | V _R = 30 V | | 0.04 | 0.1 | | |
| Maximum Reverse Leakage Current | | V _R = 30 V, T _J = 75 °C | | 0.1 | 2 | mA | |
| | | V _R = 30 V, T _J = 125 °C | | 2 | 10 | | |
| Junction Capacitance C _T | | V _R = 10 V | | 62 | | pF | |

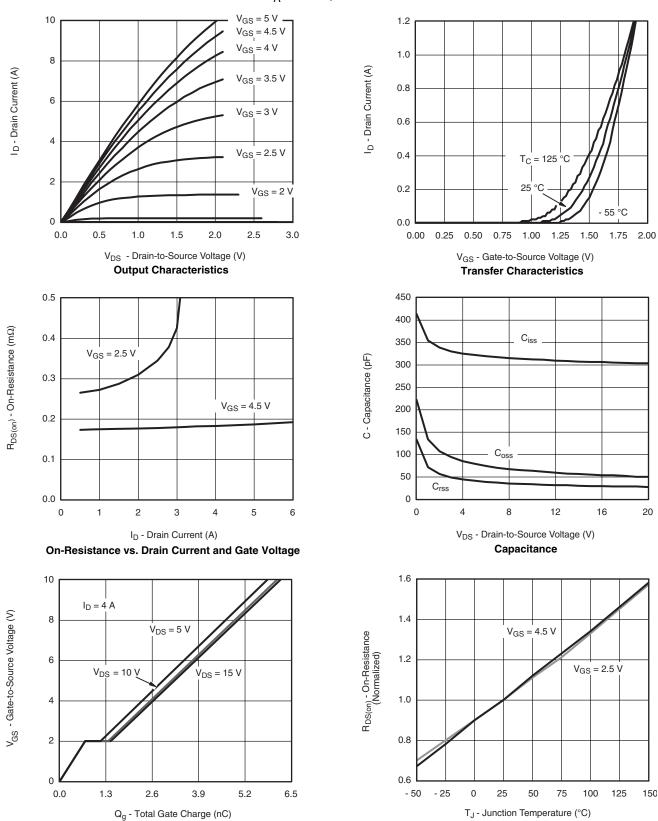
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







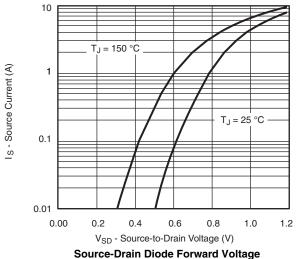
MOSFET TYPICAL CHARACTERISTICS $T_A = 25~^{\circ}C$, unless otherwise noted

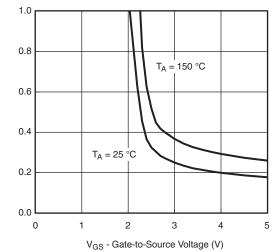


Gate Charge

On-Resistance vs. Junction Temperature

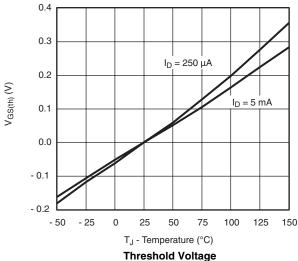
MOSFET TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



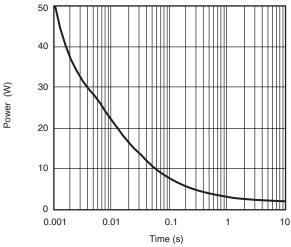


Source-Drain Diode Forward Voltage

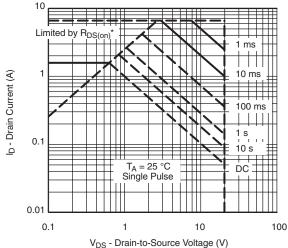




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



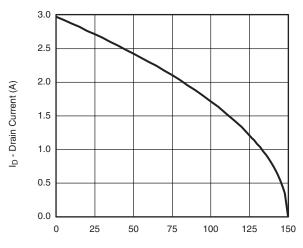
 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain-to-Source On-Resistance ($\Omega)$

* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

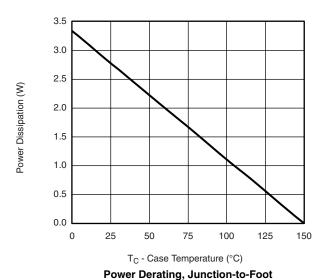


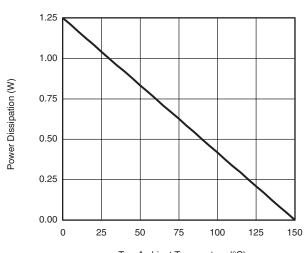
MOSFET TYPICAL CHARACTERISTICS $T_A = 25~^{\circ}\text{C}$, unless otherwise noted



T_C - Case Temperature (°C)

Current Derating*



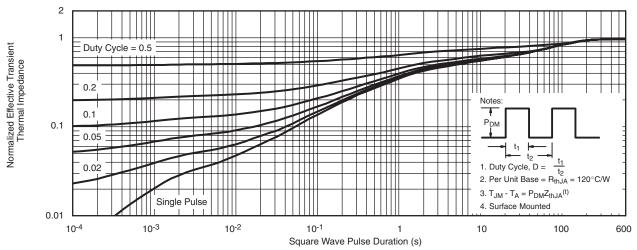


T_A - Ambient Temperature (°C) **Power Derating, Junction-to-Ambient**

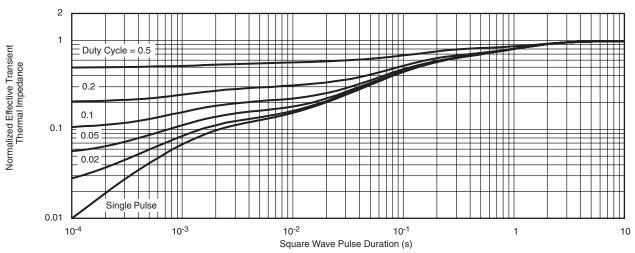
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



MOSFET TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



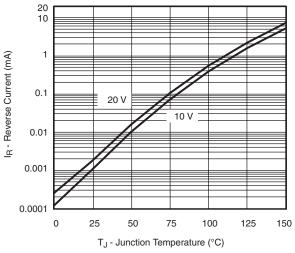
Normalized Thermal Transient Impedance, Junction-to-Foot

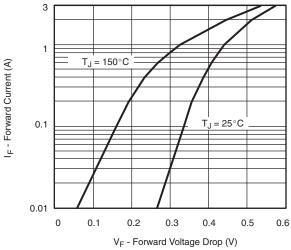






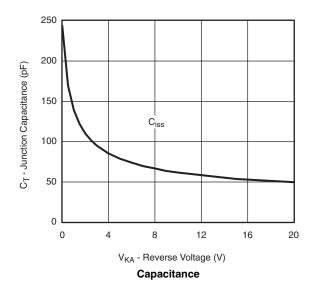
SCHOTTKY TYPICAL CHARACTERISTICS $T_A = 25~^{\circ}C$, unless otherwise noted





Reverse Current vs. Junction Temperature





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