

Date: - 8th Feb, 2018

Data Sheet Issue:- P3

Insulated Gate Bi-Polar Transistor Type T0115QC45G

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	4500	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	2800	V
V_{GES}	Peak gate – emitter voltage	±20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I _{C(DC)}	DC collector current, IGBT	115	Α
I _{CRM}	Repetitive peak collector current, tp=1ms, IGBT	230	Α
I _{F(DC)}	Continuous DC forward current, Diode	115	Α
I _{FRM}	Repetitive peak forward current, t _p =1ms, Diode	230	Α
I _{FSM}	Peak non-repetitive surge t _p =10ms, V _{RM} =60%V _{RRM} , Diode (Note 4)	690	Α
I _{FSM2}	Peak non-repetitive surge t _p =10ms, V _{RM} ≤10V, Diode (Note 4)	760	Α
P _{MAX}	Maximum power dissipation, IGBT (Note 2)	925	W
(di/dt) _{cr}	Critical diode di/dt (note 3)	300	A/µs
Tj	Operating temperature range.	-40 to +125	°C
T_{stg}	Storage temperature range.	-40 to +125	°C

Notes: -

- 1) Unless otherwise indicated T_i = 125°C.
- 2) $T_{sink} = 25$ °C, double side cooled.
- 3) Maximum commutation loop inductance 200nH.
- 4) Half-sinewave, 125°C T_j initial.



Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
M	Collector emitter acturation valtage	-	2.9	3.2	I _C = 115A, V _{GE} = 15V, T _j = 25°C	V
V _{CE(sat)}	Collector – emitter saturation voltage	-	3.5	3.8	I _C = 115A, V _{GE} = 15V	V
V_{T0}	Threshold voltage	-	-	1.68	Current range: 38 – 115A	V
r_{T}	Slope resistance	-	-	18.4	Current range: 36 – 115A	mΩ
$V_{\text{GE(TH)}}$	Gate threshold voltage	-	5.5	-	$V_{CE} = V_{GE}$, $I_C = 12mA$	V
I _{CES}	Collector – emitter cut-off current	-	3	10	V _{CE} = V _{CES} , V _{GE} = 0V	mA
I_{GES}	Gate leakage current	-	-	±3	$V_{GE} = \pm 20V$	μΑ
Cies	Input capacitance	-	TBC	-	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	nF
$t_{d(on)}$	Turn-on delay time	-	2.8	-		μs
$t_r(V)$	Rise time	-	3.8	-	I _C =115A, V _{CE} =2800V, di/dt=300A/μs	μs
$Q_{g(on)}$	Turn-on gate charge	-	1.5	-	V_{GE} = ±15V, L _s =3 μ H	μC
E _{on}	Turn-on energy	-	830	-	$R_{g(ON)}$ = 56 Ω , $R_{g(OFF)}$ =82 Ω , C_{GE} =22nF	mJ
$t_{\text{d(off)}}$	Turn-off delay time	-	3.4	-	Integral diode used as freewheel diode	μs
$t_f(I)$	Fall time	-	2.5	-	(Note 3 & 4)	μs
$Q_{g(off)}$	Turn-off gate charge	-	1.3	-		μC
E _{off}	Turn-off energy	-	480	-		mJ
I _{SC}	Short circuit current	-	350	-	V_{GE} =+15V, V_{CC} =2800V, V_{CEmax} \leq V_{CES} , t_p \leq 10 μ s	А

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
\ /	Converse veltage	-	3.30	3.60	I _F = 115A, T _j =25°C	V
V_{F}	Forward voltage	-	3.45	3.75	I _F = 115A	V
V _{To}	Threshold voltage	-	-	2.07	Current range 29 115A	V
\mathbf{r}_{T}	Slope resistance	-	-	14.6	Current range 38 - 115A	mΩ
I _{rm}	Peak reverse recovery current	-	95	-		Α
Q_{rr}	Recovered charge	-	180	-	1 - 4450 \/ - 45\/ 45/44-2000/	μC
t_{rr}	Reverse recovery time, 50% chord	-	2.5	-	I _F = 115A, V _{GE} = -15V, di/dt=300A/μs	μs
Er	Reverse recovery energy	-	180	-		mJ

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
		-	-	108	Double side cooled	K/kW
R_{thJK}	Thermal resistance junction to sink, IGBT	-	-	177	Collector side cooled	K/kW
		-	-	279	Emitter side cooled	K/kW
		-	-	172	Double side cooled	K/kW
R_{thJK}	Thermal resistance junction to sink, Diode	-	-	268	Cathode side cooled	K/kW
		-	-	478	Anode side cooled	K/kW
F	Mounting force	4.5	-	6	Note 2	kN
W_{t}	Weight	-	240	-		g

Notes:-

- 1) Unless otherwise indicated T_j=125°C.
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3) C_{GE} is additional gate emitter capacitance added to output of gate drive
- 4) Figures 6 to 9 are obtained using integral diode as freewheeling diode



Curves

Figure 1 – Typical collector-emitter saturation voltage

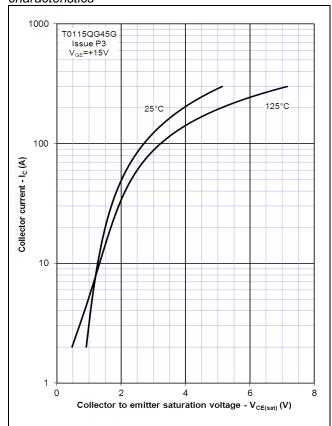


Figure 3 – Typical output characteristic

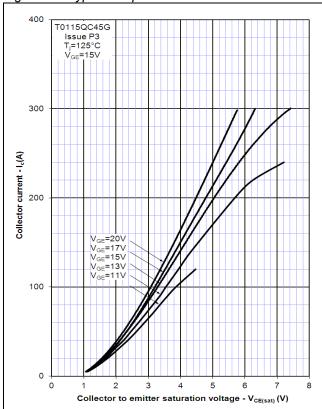


Figure 2 – Typical output characteristic

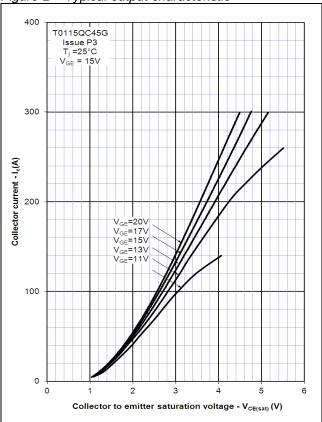


Figure 4 – Typical turn-on delay time vs gate resistance

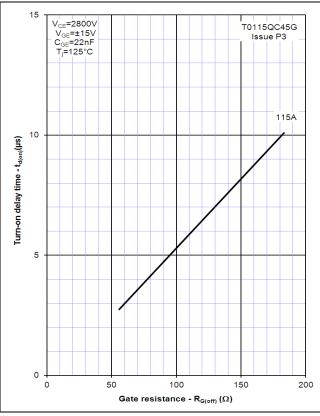




Figure 5 - Typical turn-off delay time vs. gate resistance

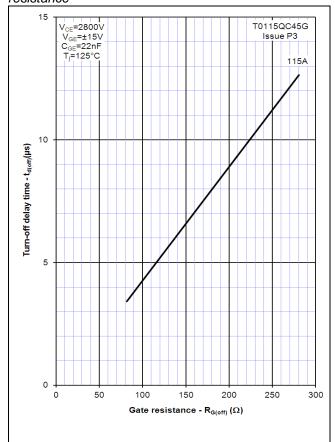


Figure 6 – Typical turn-on energy vs. collector current

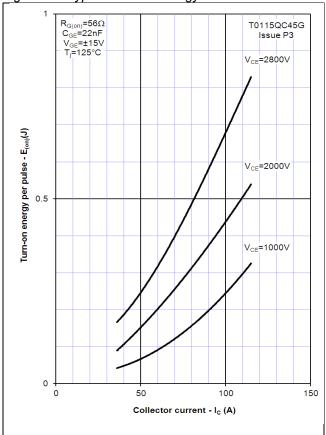


Figure 7 - Typical turn-on energy vs. di/dt

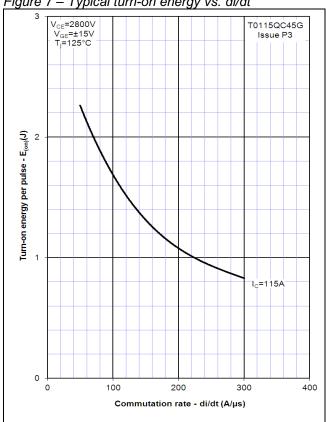
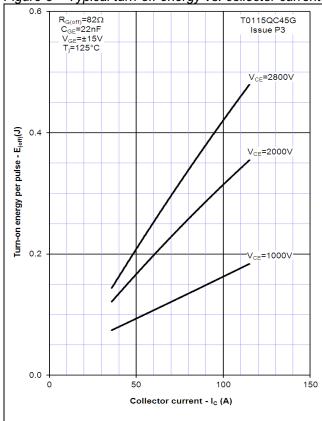
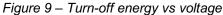


Figure 8 – Typical turn-off energy vs. collector current







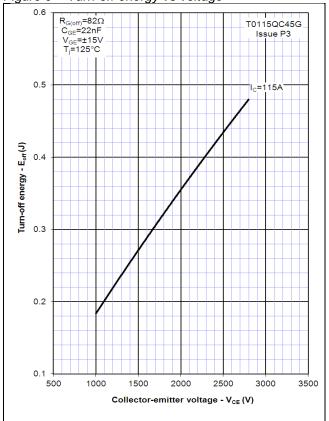


Figure 10 – Safe operating area

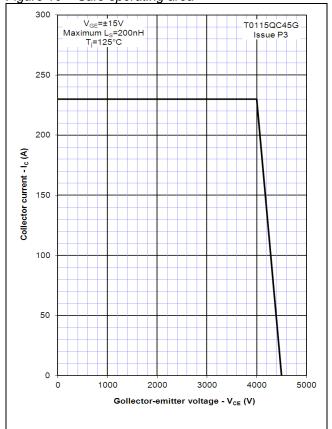


Figure 11 – Typical diode forward characteristics

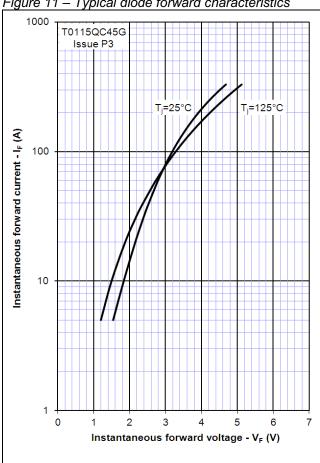


Figure 12 – Typical recovered charge

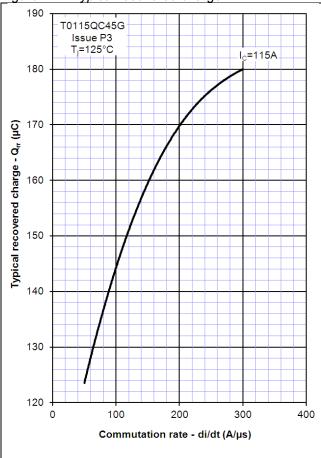




Figure 13 - Typical reverse recovery current

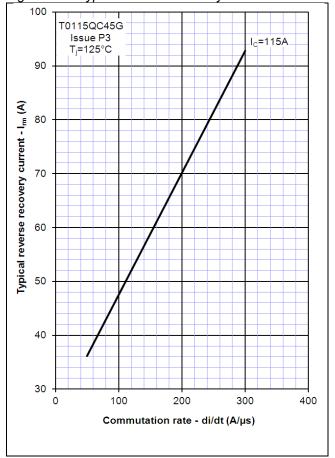


Figure 14 – Typical reverse recovery time

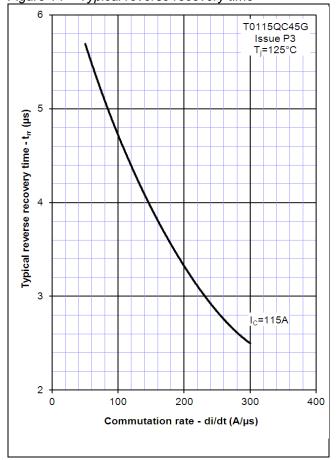


Figure 15 – Safe operating area (Diode)

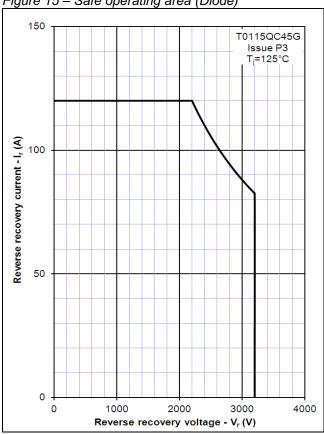
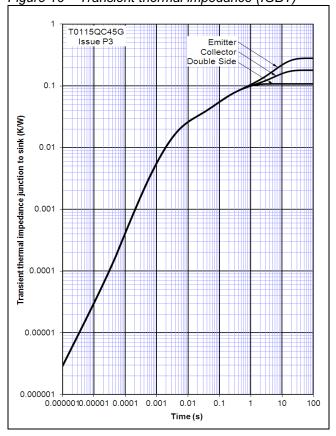
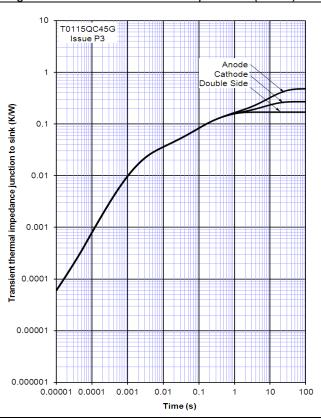


Figure 16 – Transient thermal impedance (IGBT)



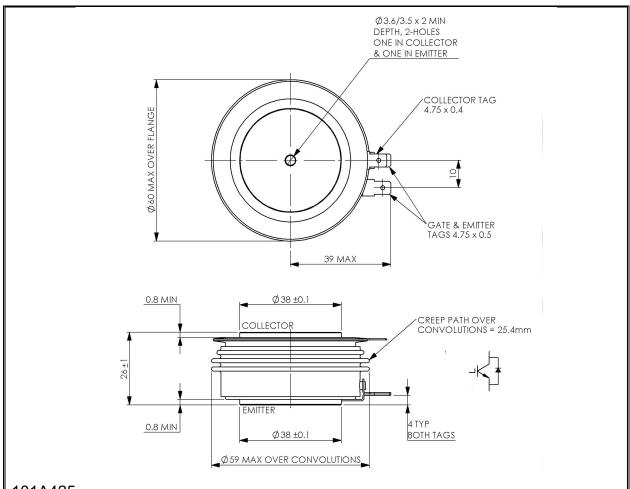








Outline Drawing & Ordering Information



101A425

ORDERING INFORMATION

(Please quote 10 digit code as below)

T0115	QC	45	G
Fixed type Code	Fixed Outline Code	Voltage Grade V _{CES} /100 45	Fixed format code

Typical order code: T0115QC45G (VcEs = 4500V)

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