



T0410H/T0420H/T0435/T0450H Series 4A TRIACs

Rev.3.0

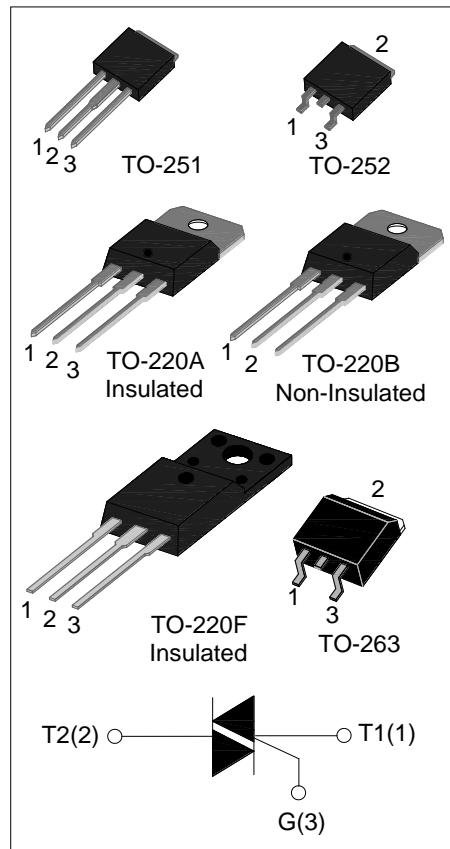
DESCRIPTION:

T04xxH series triacs of high junction temperature with high dv/dt rate with strong resistance to electromagnetic interference provide high ability to withstand the shock loading of large current. They are especially recommended for use on inductive load and high environment temperature condition.

T04xxH-xxA provides insulation voltage rated at 2500V RMS and T04xxH-xxF provides insulation voltage rated at 2000V RMS from all three terminals to external heatsink complying with UL standards (File ref: E252906).

MAIN FEATURES

Symbol	Value	Unit
$I_T(\text{RMS})$	4	A
$V_{\text{DRM}} / V_{\text{RRM}}$	600 and 800	V
$T_{j\max}$	150	°C

**ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40 - 150	°C
Operating junction temperature range	T_j	-40 - 150	°C
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	600/800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	600/800	V
Non repetitive surge peak Off-state voltage	V_{DSM}	$V_{\text{DRM}} + 100$	V
Non repetitive peak reverse voltage	V_{RSM}	$V_{\text{RRM}} + 100$	V
RMS on-state current	$I_{\text{T(RMS)}}$	4	A
TO-251/ TO-252($T_c=100^\circ\text{C}$)			
TO-220A(Ins) ($T_c=87^\circ\text{C}$)			
TO-220B(Non-Ins) ($T_c=110^\circ\text{C}$)			
TO-220F(Ins) ($T_c=95^\circ\text{C}$)			
TO-263 ($T_c=113^\circ\text{C}$)			

T04xxH Series

 JieJie Microelectronics CO., Ltd

Non repetitive surge peak on-state current (full cycle, F=50Hz)	I _{TSM}	40	A
I ² t value for fusing (tp=10ms)	I ² t	8	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT})	dI/dt	50	A/μs
Peak gate current	I _{GM}	2	A
Average gate power dissipation	P _{G(AV)}	1	W
Peak gate power	P _{GM}	5	W

ELECTRICAL CHARACTERISTICS (T_j=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant		Value				Unit
				T0410H	T0420H	T0435H	T0450H	
I _{GT}	V _D =12V R _L =33Ω	I - II -III	MAX	10	20	35	50	mA
V _{GT}		I - II -III	MAX	1.5				V
V _{GD}	V _D =V _{DRM} T _j =150°C R _L =3.3KΩ	I - II -III	MIN	0.2				V
I _L	I _G =1.2I _{GT}	I -III	MAX	20	40	50	70	mA
		II		35	55	70	80	
I _H	I _T =100mA		MAX	20	30	45	60	mA
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =150°C	MIN		200	500	1000	1500	V/μs
(dV/dt)c	(dI/dt)c =-1.7A/ms T _j =150°C	MIN		1	5	15	25	V/μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _{TM} =5.5A tp=380μs	T _j =25°C	1.55	V
I _{DRM}		T _j =25°C	5	μA
I _{RRM}	V _D =V _{DRM} V _R =V _{RRM}	T _j =150°C	1	mA

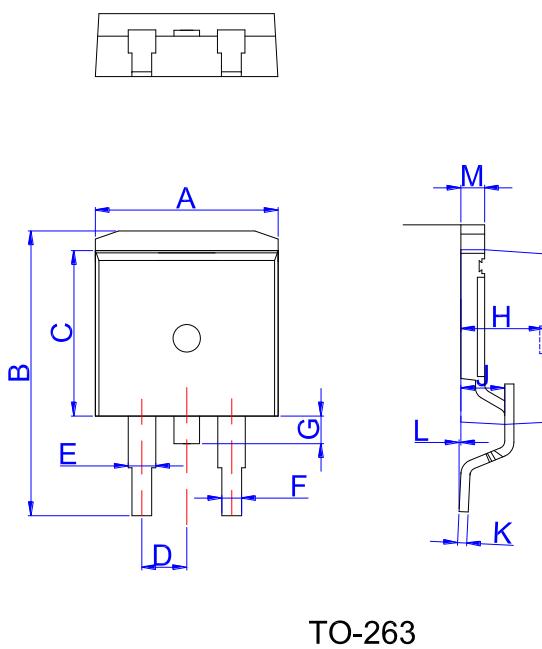
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case(AC)	TO-251/ TO-252	3.5
		TO-220A(Ins)	5.4
		TO-220B(Non-Ins)	2.1
		TO-220F(Ins)	4.8
		TO-263	1.9

ORDERING INFORMATION

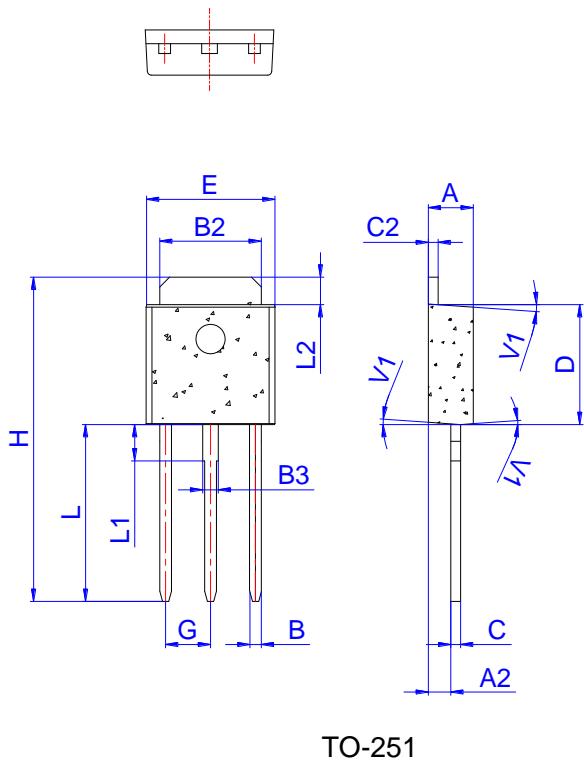
T	04	20	H	-6	F	E:TO-263 A:TO-220A(Ins) F:TO-220F(Ins) B:TO-220B(Non-Ins) H:TO-251 K:TO-252
Triacs						6:V _{DRM} / V _{RRM} ≥ 600V 8:V _{DRM} / V _{RRM} ≥ 800V High junction temperature

PACKAGE MECHANICAL DATA

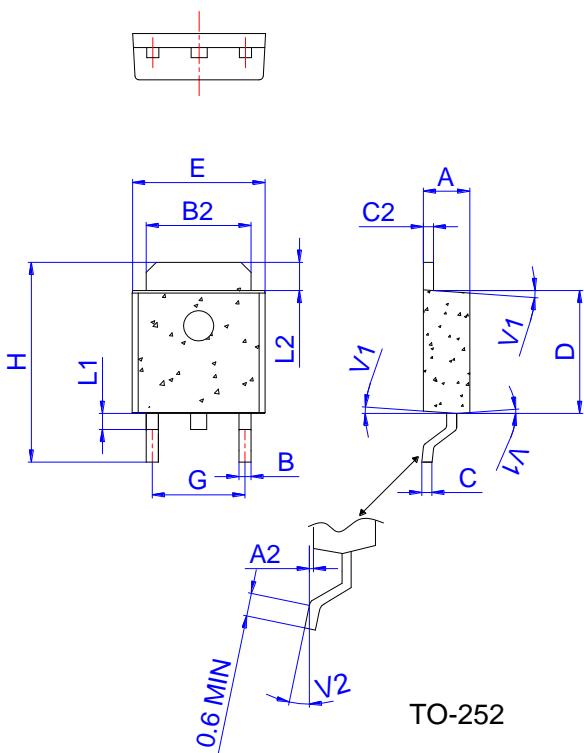


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.4		9.6	0.37		0.378
D		2.54			0.100	
E	1.20		1.40	0.047		0.055
F	0.75		0.85	0.029		0.033
G			1.75			0.069
H	4.40		4.70	0.173		0.185
J	2.30		2.70	0.091		0.106
K	0.38		0.55	0.015		0.022
L	0	0.10	0.25	0	0.004	0.010
M	1.25		1.35	0.049		0.053

PACKAGE MECHANICAL DATA

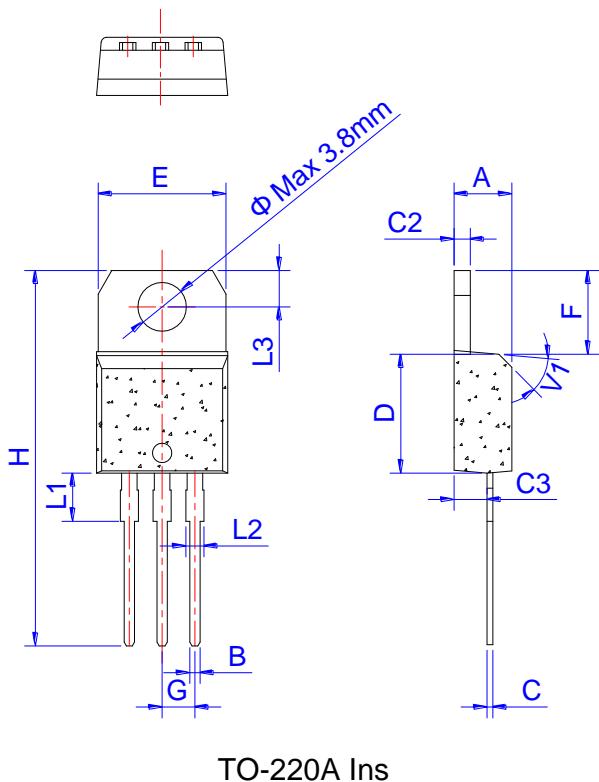


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.90		1.20	0.035		0.047
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
B3	0.76		0.85	0.030		0.033
C	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G		2.30			0.091	
H	16.0		17.0	0.630		0.669
L	8.90		9.40	0.350		0.370
L1	1.80		1.90	0.071		0.075
L2	1.37		1.50	0.054		0.059
V1		4°			4°	

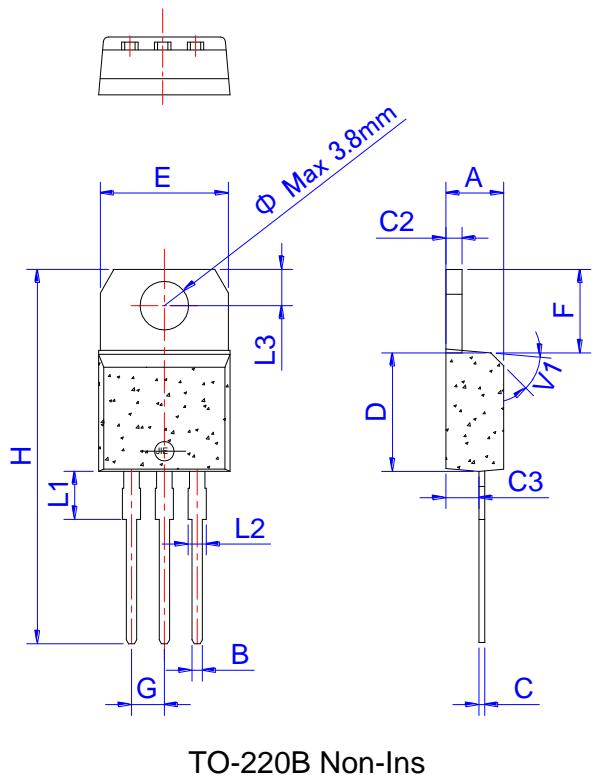


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.03		0.23	0.001		0.009
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
C	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G	4.40		4.70	0.173		0.185
H	9.35		10.6	0.368		0.417
L1	1.30		1.70	0.051		0.067
L2	1.37		1.50	0.054		0.059
V1		4°			4°	
V2	0°		8°	0°		8°

PACKAGE MECHANICAL DATA

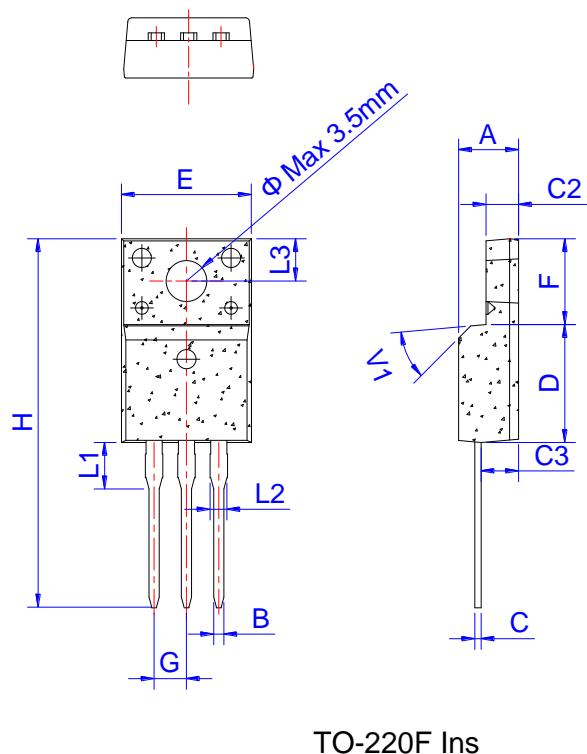


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.80		10.4	0.386		0.409
F	6.55		6.95	0.258		0.274
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.20		6.60	0.244		0.260
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.80	0.173		0.189
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.48		0.75	0.019		0.030
C2	2.40		2.70	0.094		0.106
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.70		10.3	0.382		0.406
F	6.40		7.00	0.252		0.276
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.63			0.143	
L2	1.14		1.70	0.045		0.067
L3		3.30			0.130	
V1		45°			45°	

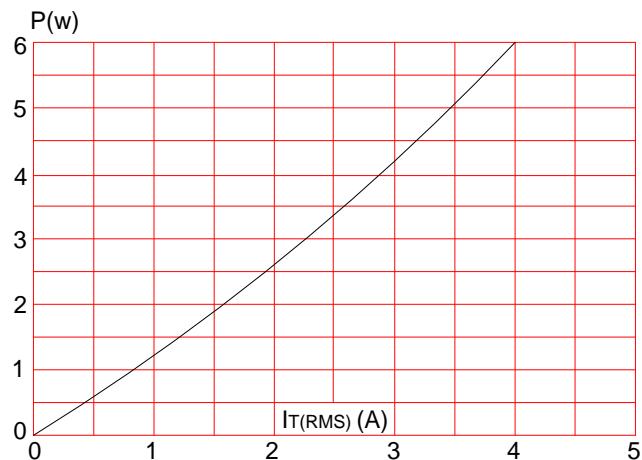
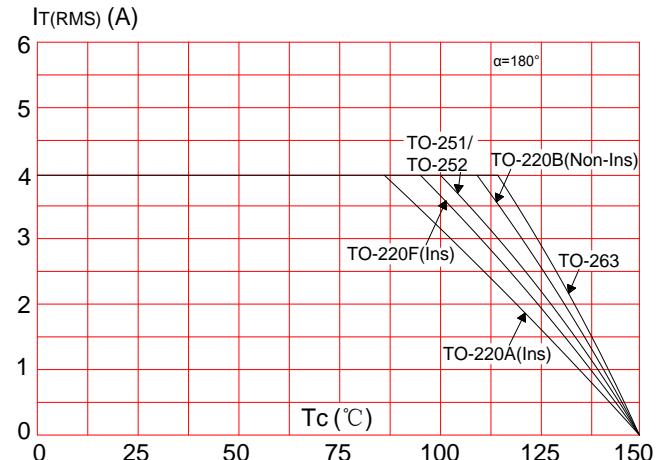
FIG.1 Maximum power dissipation versus RMS on-state current**FIG.2:** RMS on-state current versus case temperature

FIG.3: Surge peak on-state current versus number of cycles

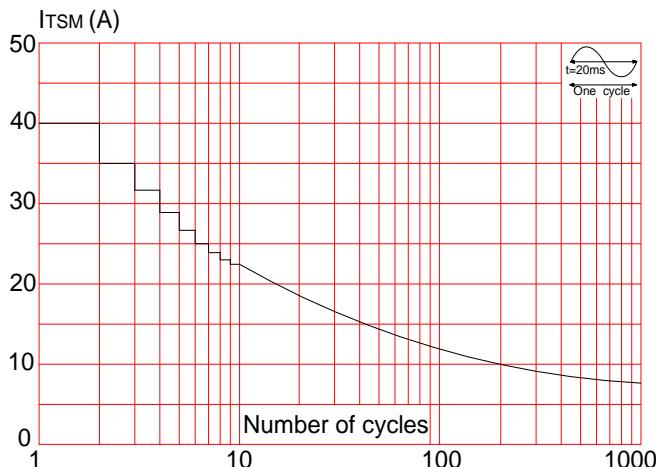


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($dI/dt < 50\text{A}/\mu\text{s}$)

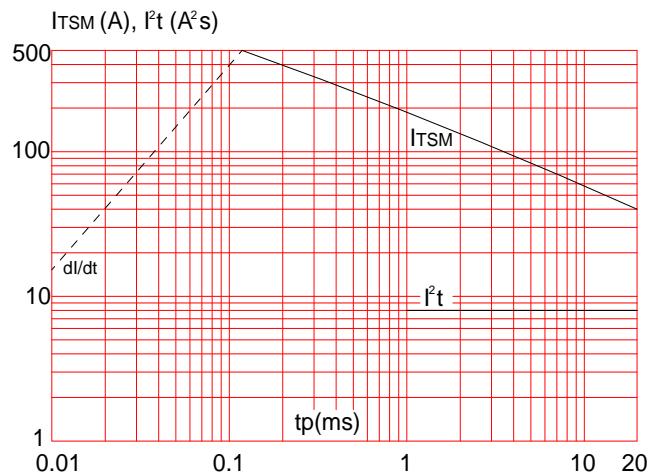


FIG.4: On-state characteristics (maximum values)

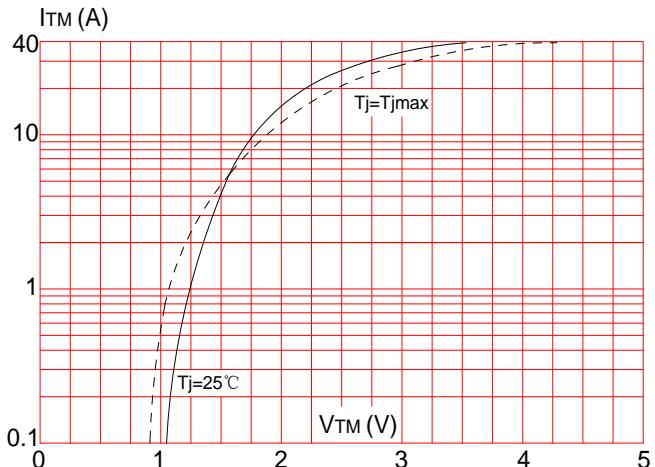
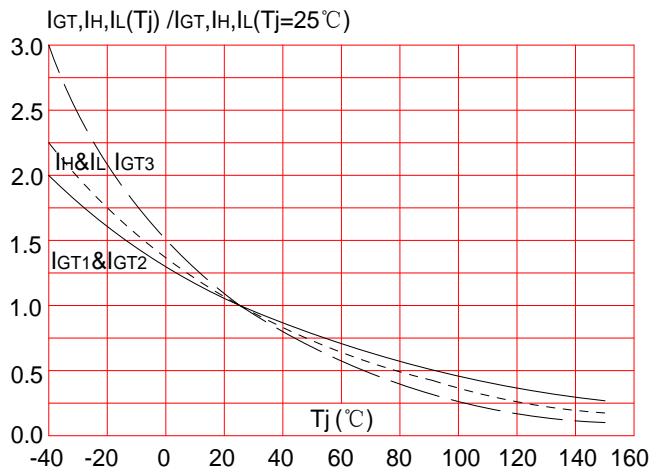


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the third version which is made in 10-June-2015. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2015 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.