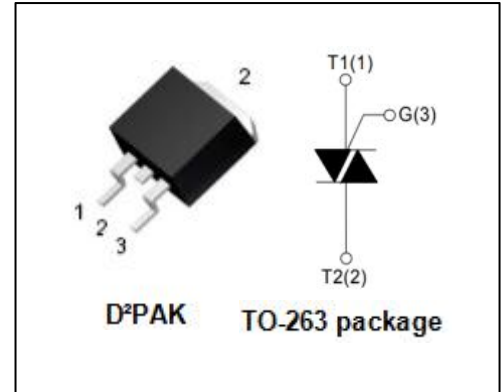


isc Thyristors
T1050-8G
APPLICATIONS

- With TO-D²PAK package
- Low thermal resistance with clip bonding
- Be suitable for general purpose AC switching, they can be used as an ON/OFF function in applications.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.


ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	800	V
V _{RRM}	Repetitive peak reverse voltage	800	V
I _{T(RMS)}	RMS on-state current full sine wave; T _{mb} ≤ 101°C	10	A
I _{TSM}	Surge non-repetitive on-state current t=20ms t=16.7ms	100 105	A
P _{G(AV)}	Average gate power dissipation (over any 20 ms period)	1	W
T _j	Operating junction temperature	-40~125	°C
T _{stg}	Storage temperature	-40~150	°C

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R=V_{RRM}$	$T_j=25^\circ\text{C}$		0.005	mA
			$T_j=125^\circ\text{C}$		1	
I_{DRM}	Repetitive peak off-state current	$V_D=V_{DRM}$	$T_j=25^\circ\text{C}$		0.005	mA
			$T_j=125^\circ\text{C}$		1	
V_{TM}	On-state voltage	$I_{TM}=14\text{A}$			1.55	V
I_{GT}	Gate-trigger current Quadrant (I - II - III)	$V_D=12\text{V}; I_T=0.1\text{A}$		2	50	mA
V_{GT}	Gate-trigger voltage Quadrant (I - II - III)	$V_D=12\text{V}; I_T=0.1\text{A}$			1.3	V

NOTICE:

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