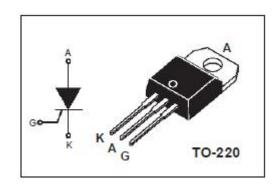


isc Thyristors T1210T-8T

APPLICATIONS

- With TO-220AB package.
- · General purpose AC line load switching
- · Motor control circuits
- Small home appliances
- Lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.





ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER		UNIT
V_{DRM}	Repetitive peak off-state voltage @T _j =125℃ @T _j =150℃	800 600	V
V_{RRM}	Repetitive peak reverse voltage $@T_j=125^{\circ}C$ $@T_j=150^{\circ}C$	800 600	V
I _{T(RMS)}	RMS on-state current @T _c =131℃		А
I _{TSM}	Surge non-repetitive on-state current F=50HZ; t=20ms F=60HZ;16.7ms	90 95	A
P _{G(AV)}	Average gate power dissipation @T _j =150 ℃	1	W
Tj	Operating junction temperature		$^{\circ}$
T _{stg}	Storage temperature	-40~150	$^{\circ}$



isc Thyristors

T1210T-8T

ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
			T _j =25℃		7.5	μ A
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RRM}	T _j =125℃		1.0	mA
			T _j =150℃		2.7	mA
I _{DRM}	Repetitive peak off-state current	V _{DM} =V _{DRM}	T _j =25℃		7.5	μ Α
			T _j =125℃		1.0	mA
			Tj=150℃		2.7	mA
V_{TM}	On-state voltage	I _{TM} = 17A; t _p =380 μ s			1.55	V
Ідт	Gate-trigger current (minimum IGT is guaranted at 5% of IGT max) Quadrant(I - II - III)	V _D = 12 V;R _L =30 Ω			35	mA
V _{GT}	Gate-trigger voltage Quadrant (I - II - III)	V _D = 12 V;R _L =30 Ω			1.0	V

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
R _{th(j-c)}	Thermal resistance	Junction to case		1.3	°C/W
R _{th(j-a)}	Thermal resistance	Junction to case		60	°C/W

NOTICE:

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