



Product Description: 15 inch TFT-LCD PANEL					
AUO Model Name: T150XG	601 V3				
Customer Part No/Project Name:					
Customer Signature	Date	AUO		Date	
		Approved By	/: PL Chen		
		Prepared B	y: CJ Tan 12/1	2006	

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





Document Version: 1.0 Date: 2006/12/1

# **Product Functional Specification**

15" Color TFT-LCD Module Model Name: T150XG01 V3 (QDI Model: QD15XL1601)

() Preliminary Specification (\*) Final Specification

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





This specification sheet is for model name change, since AUO merged QDI from 2006/10/1

This Specification Sheet keep the original QDI Model name and Spec.

New Model name and old model name comparison table as following:

	AUO	QDI		
Model Name	T150XG01 V3	QD15XL1601		
Change Item	1. Carton Printing format			
	2. Product Serial label format			

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





Revision History					
REV.	Date	ECN NO.	Change Content		
1	12/1		Change AUO product name		
	_				
4					
	7				

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





## **Content List**

		raye	
1.	Application	5	
2.	Overview	5	
3.	General Specifications	5	
4.	Input Terminals	6	
5.	Absolute Maximum Ratings		8
6.	Electrical Characteristics		9
7.	Timing Characteristics	12	
3.	Input Signals, Basic Display Colors and Gray	/	
	Scale of Each Color	14	
9.	Optical Characterics		15
10.	Display Quality	18	
11.	Handling Precautions	18	
12.	Reliability Test Items	19	
13.	Others	19	
14.	Drawing	20	

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





This specification applies to a color TFT-LCD module, QD15XL16

#### 2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel; driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a 1024×3×768 dots panel with 16.2 million colors by using the LVDS (Low Voltage Differential Signaling) interface, 6-bit+FRC driving method and supplying +5V DC supply voltage for TFT-LCD panel driving.

The TFT-LCD panel used for this module has very high aperture ratio. A low-reflection and higher-color-saturation type color filter is also used for this panel. Therefore, high-brightness and high-contrast image, which is suitable for the LCD TV,HDTV and multimedia use, can be obtained by using this module.

#### [Features]

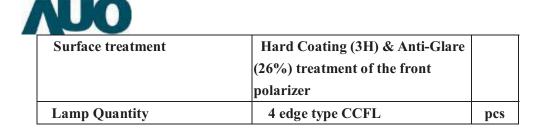
- A Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Filament Lamp (CCFL) backlight system.
- TFT as the active element.
- Associated electronics (drivers, control circuits, etc)
- A metal frame

#### 3. General Specifications

Parameter	Specifications	Unit
Display size	380.16 (15.0") Diagonal	mm
Active area	304.128 (H)×228.096 (V)	mm
Pixel format	1024 (H)×768 (V)	Pixel
	(1 pixel = R+G+B dots)	
Pixel pitch	0.297 X 0.297	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally White	
<b>Unit outline dimensions</b>	326.5(W)×253.5 (H)	mm
Thickness	Тур. 15. 4	mm
Weight	1500 max.	g

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



Global LCD Panel Exchange Center

4-1. TFT-LCD panel driving

CN1 (LVDS signals and +5V DC power supply)

Using connector: DF14H-20P-1.25H (Hirose) or Equivalent

LCD Connector: DF14-20S-1.25C(Manufactured by Hirose) or Equivalent

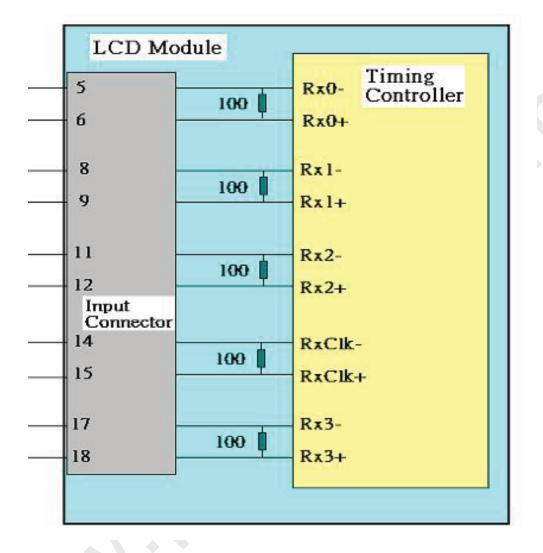
Pin No.	Symbol	Function	Remark
1	VDD	+5V Input	
2	VDD	+5V Input	
3	GND	Power Ground	
4	GND	Power Ground	
5	RxIN0-	Receiver signal (-)	LVDS
6	RxIN0+	Receiver signal (+)	LVDS
7	GND	Ground	
8	RxIN1-	Receiver signal (-)	LVDS
9	RxIN1+	Receiver signal (+)	LVDS
10	GND	Ground	
11	RxIN2-	Receiver signal (-)	LVDS
12	RxIN2+	Receiver signal (+)	LVDS
13	GND	Ground	
14	CLKIN-	Clock signal (-)	LVDS
15	CLKIN+	Clock signal (+)	LVDS
16	GND	Ground	
17	RxIN3-	Receiver signal (-)	LVDS
18	RxIN3+	Receiver signal (+)	LVDS
19	GND	Ground	
20	GND	Ground	

[Note 1] All GND(ground) pins should be connected together.

[Note 2] All  $V_{DD}$  (power supply) pins should be connected together.

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed







Pin	Symbol Description		Notes
1	HV	Power supply for lamp (high)	1) LCD : BHSR-02VS-1 (JST)
2	LV	Power supply for lamp (Low)	2) System: SM02B-BHSS-1 (JST)

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





### LCD module

Parameter	Symbol	Condition	Ratings	Unit	Remark
+5V supply voltage	$V_{DD}$	Ta=25℃	-0.3 ~ 6.0	V	
Storage temperature	Tstg	_	<b>-20</b> ~ 60	್ರೆ	[Note1]
Operating temperature (Ambient)	Тора	_	0 ~ +50	${\mathbb C}$	

Note1 Humidity: 90%RH Max. at Ta≤40°C.

Maximum wet-bulb temperature at 39℃ or less at Ta>40

°C.

No condensation.

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





## 6. Electrical Characteristics

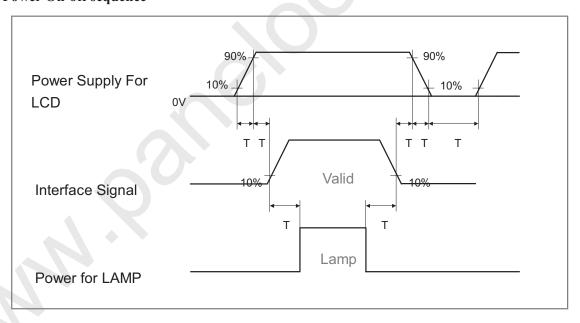
## 6-1.TFT-LCD panel driving

Ta=25°C

	Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
$V_{DD}$	Supply voltage	ge	$V_{DD}$	4.75	5.0	5.25	V	[Note2]
	Current dissipation		I <sub>DD</sub>	-	430	550	m A	[Note3]
Pe	Permissive input ripple		$V_{RP}$	-	-	100	mV p-p	V <sub>DD</sub> =+5V
volta	ge							
Diffe	rential input	High	$V_{TH}$	-	-	+100	mV	V <sub>CM</sub> =+1.2V
thi	reshold voltage	Low	V <sub>TL</sub>	100	-	-	mV	[Note1]
Rush current		I <sub>RUSH</sub>	-	-	2	A	Rise time	
								200 uS

[Note1]  $V_{CM}$ : Common mode voltage of LVDS driver.

## [Note2] Power On-off sequence



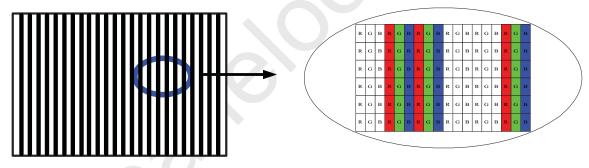
©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





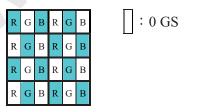
	Min	Max	Units
T1	1	10	ms
T2	0	50	ms
T3	200	-	ms
T4	200	-	ms
T5	0	50	ms
Т6	0	10	ms
Т7	400	-	ms

[Note3] Typical current condition; 2-line vertical stripe pattern (0,255GS).  $V_{DD}$ =+5V



Maximum current condition; Change to 1x1 dot checker board pattern.

 $V_{DD}=+5V$ 



: 255 GS

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



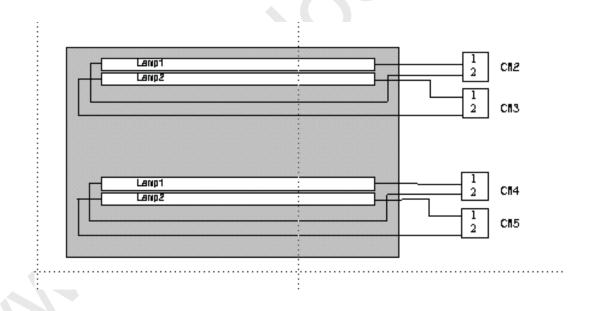


## 6-2. Backlight driving

The backlight system is a edge type with 4 CCFL lamp.

The characteristics of the lamp are shown in the following table.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current range	$I_{L}$	5.5	6.5	7.0	mArm	[Note1]
					S	
Lamp voltage	$V_{L}$	-	585	-	Vrms	
Lamp power	$\mathbf{P}_{\mathrm{L}}$	-	15.2	-	W	[Note2] IL=6.5
consumption						mA
Lamp frequency	$\mathbf{F}_{\mathrm{L}}$	40-	60	80-	kHz	[Note3]
<b>Established starting</b>	Vs	-	-	900	Vrms	Ta=25
voltage						$^{\circ}$
		-	-	1150	Vrms	Ta=0 [Note4]
						$ \mathbb{C} $
Lamp life time	$L_{\rm L}$	50,000	-		hour	[Note5]



©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



[Note1] Lamp current is measured with current meter for high frequency as shown below.

- [Note2] Calculated Value for reference ( $I_L \times V_L$ )
- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display.

  Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- [Note4] The voltage above this value should be applied to the lamp for more than 1 second to start-up. Otherwise the lamp may not be turned on.
- [Note5] Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of Ta = 25°C and  $I_L = 6.5$  mArms.
- ① Brightness becomes 50 % of the original value under standard condition.
  - ② Kick-off voltage at  $Ta = 0^{\circ}C$  exceeds maximum value.
- [Note6] The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.
- [Note7] The lamp wire length is +/- mm(from AL back cover surface to connector, not including connector length)

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





## 7. Timing characteristics of LCD module input signals

## 7-1. Timing characteristics

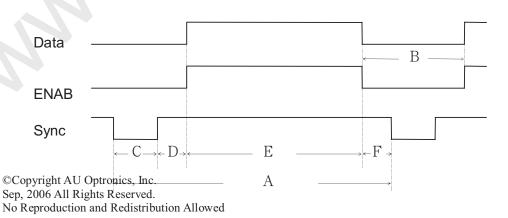
(This is specified at digital outputs of LVDS driver.)

ITEM	Symbol		Min	Typ	Max	Unit	Notes
DCLK	Frequency	Fclk	42	65	82	MHz	
	Period	tclk	23.81	15.38	12.20	ns	
Hsync	Frequency	$\mathbf{f}_{H}$	36.21	48.36	60.76	KHz	
	Period	tha	1160	1344	1720	tclk	
	Width-Active	<b>t</b> HC	20	136	-	tclk	
Vsync	Frequency	fv	46.66	60.00	75.01	Hz	
	Period	tva	776	806	-	tha	
	Width-Active	tvc	2	6	-	tha	
Data	Horizontal back porch	$t_{ m HD}$	60	160	-	tclk	
Enable	Horizontal front porch	$t_{ m HF}$	<b>56</b>	24	1	tclk	
	Horizontal active	the	1024	1024	1024	tclk	
	Horizontal blanking	tнв	136	320	-	tclk	
	Vertical back porch	$t_{ m VD}$	2	29	-	t <sub>HA</sub>	
	Vertical front porch	tvf	4	3	-	t <sub>HA</sub>	
	Vertical active	tve	768	768	768	t <sub>HA</sub>	
	Vertical blanking	tvB	8	38	-	tha	

Notes: 1. The performance of electro-optical characteristics may be influenced by variance of the vertical refresh rate.

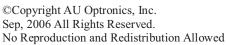
2. Hsync period will be a double number of character (8).

#### 7-2 Signal Timing Waveform













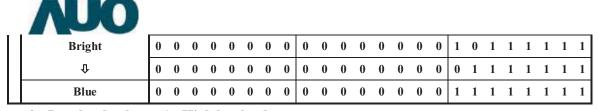
## 8. Input Signals, Basic Display Colors and Gray Scale of Each Color

П	Colors &				Dat	a S	i gn	al																	
	Gray scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	<b>G</b> 7	В0	B1	<b>B2</b>	В3	<b>B4</b>	В5	<b>B6</b>	В7
Ба	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ित्	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Basic Colo	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ĭ	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Ħ	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
İ	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
li	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
ij	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
6	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Ked	Û	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
g	Darker	0	1	0	0	0	0	0 <	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
l d	<b>·</b>		Ψ					Ψ						Ψ											
19	<b>1</b>		<b>V</b>						<b>V</b>							<b>V</b>									
led	Bright	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Φ.	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ଦ୍ର	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Gree	Û	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Sca	Darker	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le c	Û																								
G	Û																								
ree	Bright	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
ň	Û	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
۵	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ray	Û	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
SC	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
ale	· û																								
Gray Scale of Blue	Φ.																								
읦	!																								

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved.

No Reproduction and Redistribution Allowed





0 : Low level voltage, 1 : High level voltage

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16.2M-color display can be achieved on the screen.

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed

PDF created with FinePrint pdfFactory Pro trial version <a href="www.pdffactory.com">www.pdffactory.com</a>





 $Ta=25^{\circ}C, V_{DD}=+5V$ 

motor	Symbol					1 1	
Parameter		Condition	Min.	Typ.	Max.	Unit	Remark
Viewing L/R		CR>10	TBD	70		Deg.	[Note1,4]
U	θ 11		TBD	65		Deg.	
D	θ 12		TBD	60		Deg.	
ast ratio	CRn	θ =0°	450	500	_		[Note2,4]
nse time	τ		_	16		ms	[Note3,4]
e τr				TBD		ms	
Fall time τ d				TBD		ms	
ticity of	Wx		0.259	0.289	0.319		[Note4]
CIE 1931)	Wy		0.274	0.304	0.334		
ticity of	Rx		0.589	0.619	0.649		
E 1931)	Ry		0.313	0.343	0.373		
ticity of	Gx		0.268	0.298	0.328		
CIE 1931)	Gy		0.548	0.578	0.608		
ticity of	Bx		0.119	0.149	0.179		
E 1931)	By		0.052	0.082	0.112		
ce of white	YL		TBD	450		Cd/m	
ote4]						2	
niformity	δw		_	-	1.3		[Note5]
	L/R  U D  ast ratio  nse time e	L/R $\theta$ 21, $\theta$ 22  U $\theta$ 11 D $\theta$ 12  Ast ratio CR n  Insetime $\tau$ E $\tau$ C $\tau$ C T $\tau$ E $\tau$ C $\tau$ C T $\tau$	L/R $\theta 21, \theta 22$ CR>10  U $\theta 11$ D $\theta 12$ Ast ratio CR n $\theta = 0^{\circ}$ Inset time $\tau$ E $\tau$ d $\tau$ Cicity of Rx  E 1931) Ry  Cicity of Gx  CIE 1931) Gy  Cicity of Bx  E 1931) By  Cc of white $\tau$ CR n $\theta = 0^{\circ}$ The set $\tau$ d $\tau$ Consider $\tau$ CR n $\tau$	L/R $\theta$ 21, $\theta$ 22       CR>10       TBD         U $\theta$ 11       TBD         D $\theta$ 12       TBD         ast ratio       C R n $\theta$ =0°       450         nse time $\tau$ —         e $\tau$ r       —         e $\tau$ d       —         e       1931       G         e	L/R $\theta  21, \theta  22$ CR>10       TBD       70         U $\theta  11$ TBD       65         TBD       60         ast ratio       C R n $\theta = 0^{\circ}$ 450       500         nse time $\tau$ —       16         e $\tau$ r       TBD       TBD         e $\tau$ d       TBD       TBD         cicity of       Wx       0.259       0.289         cicity of       Rx       0.589       0.619         cicity of       Gx       0.268       0.298         cicity of       Gx       0.268       0.298         cicity of       Bx       0.119       0.149         cicity of       Bx       0.052       0.082         ce of white       Y <sub>L</sub> TBD       450	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

\* The measurement shall be executed 30 minutes after lighting at rating. (typical cond ition :  $I_L = 6.5 \text{mArms}$ )

The optical characteristics shall be measured in a dark room or equivalent state — TFT-LCD module with the method shown in Fig.1 below. Photo detector (BM-5A: TOPCON) Field=2°

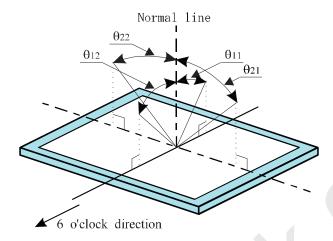
Center of the screen 500 mm LCD Panel ©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed

Fig 1. Optical characteristics measurement method





### [Note1] Definitions of viewing angle range:

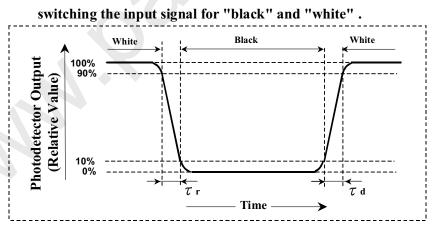


#### [Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

### [Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by



[Note4] This shall be measured at center of the screen.

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed

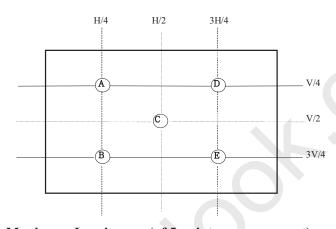




[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements

 $(A \sim E)$ .HxV: active area



Maximum Luminance (of 5 points measurement)

δ<sub>w</sub> = Minnum Luminance (of 5 points measurement)

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

## 11 · Handling Precautions

a) Be sure to turn off the power supply when inserting or disconnecting the cable.

b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.

- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
  - f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
  - g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.
  - h) Observe all other precautionary requirements in handling components.
  - i) This module has its circuitry PCBs on the rear side and should be handled carefully in order not to be stressed.
  - j) Laminated film is attached to the module surface to prevent it from being scratched. Peel the film off slowly just before the use with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off the 'dust' on the polarizer by using an ionized nitrogen gun, etc..
  - k) The LCD Module shall be supplied by power complied to International Standards (IEC60950 or UL60950).

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed





The display module must operate error free when operated under the following environmental test condition.

No	Test Item	Condition
1	High Temperature storage test	Ta=60 <sup>0</sup> C 240 hours
2	Low Temperature storage test	Ta=-20 <sup>0</sup> C 240 hours
3	High Temperature operation test	Ta=50°C 50%RH 240
		hours
4	Low Temperature operation test	Ta=0 <sup>0</sup> C 240 hours
5	Vibration Test (Non-Operating)	Waveform: Random
		Vibration level: 1.0G
		RMS
		Bandwidth: 10-500Hz
		Duration: X,Y,Z, 20
		min
		One time in each
		direction
6	Shock Test (Non-Operating)	Shock level: 100G
		Waveform: half sine
		wave, 2mS
		Direction: $\pm X$ , $\pm Y$ , $\pm Z$
		One time in each
		direction

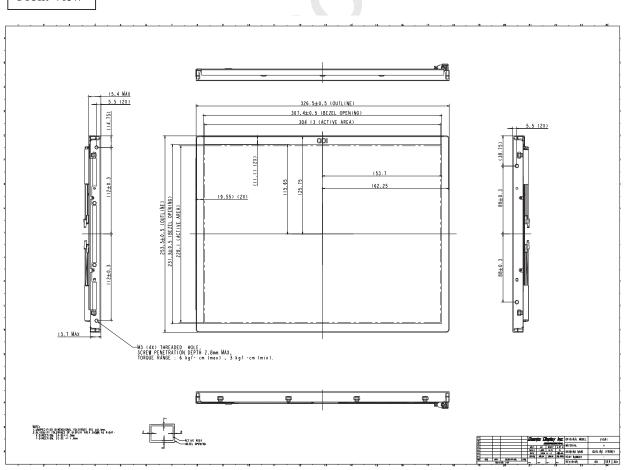
©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



- 1) Adjusting volume has been set optimally before shipment, so do not change any adjusted value.
- If adjusted value is changed, the technical literature may not be satisfied.
- 2) Disassembling the module can cause permanent damage and should be strictly avoided.
- 3) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 4) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.

#### 14. Drawing

Front View



©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed



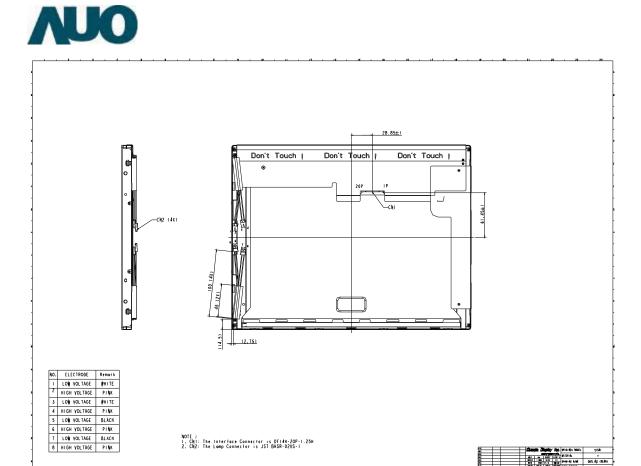


Back View

©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed

Global LCD Panel Exchange Center





©Copyright AU Optronics, Inc. Sep, 2006 All Rights Reserved. No Reproduction and Redistribution Allowed