

# UNISONIC TECHNOLOGIES CO., LTD

# T2096

## NPN SILICON TRANSISTOR

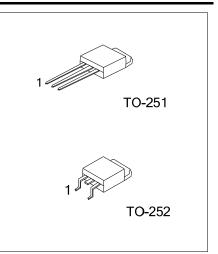
# HIGH VOLTAGE TRANSISTOR

#### DESCRIPTION

The T2096 is a NPN Silicon Planar Transistors in TO-251 package. It is intended for high voltage, switching power supply and industrial applications.

#### FEATURES

- \* Pb-free package is available
- \* Collector-Emitter voltage: V<sub>CEO</sub> = 400V
- \* Pulse collector current to 4A

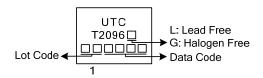


#### ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
T2096L-TM3-T	T2096G-TM3-T	TO-251	В	С	Е	Tube	
T2096L-TN3-R	T2096G-TN3-R	TO-252	В	С	Е	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source							
T2096L-TM3-T (1)Packing Type		(1) T: Tube, R:					

(2)Package Type	(2) TM3: TO-251, TN3: TO-252
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

#### MARKING



#### ■ **ABSOLUATE MAXIUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V <sub>CBO</sub>	800	V
Collector-Emitter Voltage	V <sub>CES</sub>	800	V
Collector-Emitter Voltage	V <sub>CEO</sub>	400	V
Emitter-Base Voltage	V <sub>EBO</sub>	8	V
Base Current	Iв	1	А
DC Collector Current	lc	2	А
Pulse Collector Current (Note 2)	I <sub>CP</sub>	4	А
Collector Discipation T <sub>A</sub> =25°C		n 1	
Collector Dissipation T <sub>c</sub> =25°C	Pc	15	W
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Note:1.Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

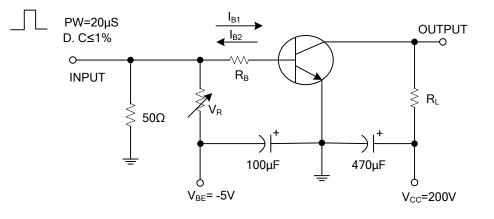
2. Pulse Test: Pulse Width ≤300µS, Duty Cycle≤10%

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =1mA, I <sub>E</sub> =0	800			V
Collector-Emitter Breakdown Voltage	$BV_{CEO}$	I <sub>C</sub> =5mA, R <sub>BE</sub> =∞	400			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =1mA, I <sub>C</sub> =0	8			V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =1A, I <sub>B</sub> =0.2A			0.8	V
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	I <sub>C</sub> =1A, I <sub>B</sub> =0.2A			1.5	V
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =400V, I <sub>E</sub> =0			10	μA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =5V, I <sub>C</sub> =0			10	μA
DC Current Gain	h <sub>FE 1</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =1mA	45			
	h <sub>FE 2</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =0.2A	120		180	
Current Gain-Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =0.2A		20		MHz
Output Capacitance	Cob	V <sub>CB</sub> =10V, f =1MHz		20		рF
Turn-on Time	t <sub>on</sub>	I <sub>C</sub> =1.0A, I <sub>B1</sub> =0.05A			0.5	μs
Storage Time	t <sub>stg</sub>	I <sub>B2</sub> = -0.5A, R <sub>L</sub> =200Ω			2.5	μs
Fall Time	t <sub>F</sub>	V <sub>CC</sub> =200V			0.3	μs

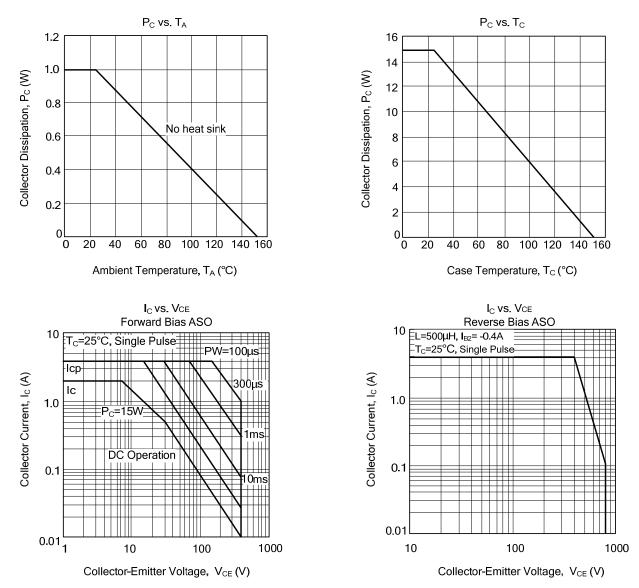


## ■ SWITCHING TIME TEST CIRCUIT





## TYPICAL CHARACTERISTICS



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