



# Model Name: T260XVN01.0

Issue Date: 2011/10/28

(\*)Preliminary Specifications( )Final Specifications

Customer Signature	Date	AUO	Date						
Approved By		Approval By PM Director							
Note		Reviewed By RD Director							
		Reviewed By Project Leader							
		Prepared By PM  Ry Lee							





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# **Record of Revision**

Version	Date	Page	Description
0.1	10/28		First release





# 1. General Description

This specification applies to the 26.0 inch Color TFT-LCD Module T260XVN01.0. This LCD module has a TFT active matrix type liquid crystal panel 1,366x768 pixels, and diagonal size of 26.0 inch. This module supports 1,366x768 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot.

The T260XVN01.0 has been designed to apply the 8-bit 1 channel FFC interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important. The LED Driver is combined into whole module.

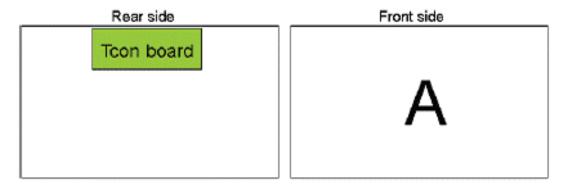
#### \* General Information

Items	Specification	Unit	Note
Active Screen Size	26.00	inch	<b>♦</b>
Display Area	575.769(H)X323.712(V)	mm	<i>y</i>
Outline Dimension	609.8(H) X 357.8 (V)X14.6(D)	mm	D: Front Bezel to T-Con Cover
Driver Element	a-Si TFT active matrix		
Display Colors	8 bit, 16.7M	Colors	
Number of Pixels	1,366x768	Pixel	
Pixel Pitch	0.4215 (H) x 0.4215 (W)	mm	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment Anti-Glare, 3H			Haze=2%

Display Orientation	Signal input with "A"	Note 2	
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Note 1: Rotate Function refers to LCD display could be able to rotate.

Note 2: LCD display as below illustrated when signal input with "A".







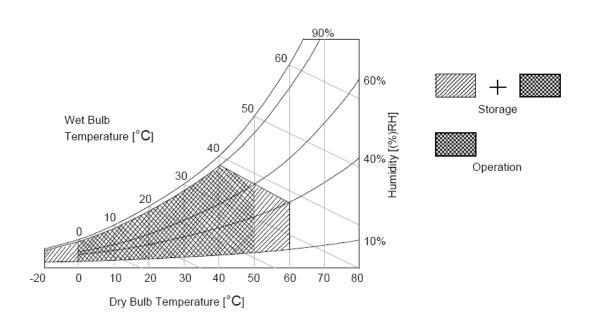
# 2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

Item	Symbol	Symbol Min Max			Conditions
Logic/LCD Drive Voltage	$V_{DD}$	-0.3	14	$V_{DC}$	Note 1
Input Voltage of Signal	Vin	-0.3	4	V <sub>DC</sub>	Note 1
BLU Input Voltage	VDDB	-0.3	28	V <sub>DC</sub>	Note 1
BLU Brightness Control Voltage	Vdim	-0.3	7	V <sub>DC</sub>	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

The relative humidity must not exceed 90% non-condensing at temperatures of  $40^{\circ}$ C or less. At temperatures greater than  $40^{\circ}$ C, the wet bulb temperature must not exceed  $39^{\circ}$ C.







# 3. Electrical Specification

The T260XVN01.0 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input for BLU is to power inverter.

#### 3.1 Electrical Characteristics

	Parameter	Symbol		Value		Unit	Note
	i didilielei	Symbol	Min.	Тур.	Max	O I II	INOLE
LCD							
Power Su	pply Input Voltage	V <sub>DD</sub>	10.8	12	13.2	V <sub>DC</sub>	1
Power Su	pply Input Current	I <sub>DD</sub>		0.22	0.27	Α	2
Inrush Cu	rrent	I <sub>RUSH</sub>			3	Α	3
Interface	Input Differential Voltage	V <sub>ID</sub>	200	400	600	$mV_{DC}$	4
	Differential Input High Threshold Voltage	$V_{TH}$	+100	-	+300	$mV_{DC}$	4
	Differential Input Low Threshold Voltage	V <sub>TL</sub>	-300		-100	$mV_{DC}$	4
	Input Common Mode Voltage	V <sub>ICM</sub>	1.1	1.25	1.4	V <sub>DC</sub>	4
CMOS	Input High Threshold Voltage	V <sub>IH</sub> (High)	2.7		3.3	$V_{DC}$	5
Interface	Input Low Threshold Voltage	V <sub>IL</sub> (Low)	0		0.6	V <sub>DC</sub>	5
Backlight	Power Consumption	P <sub>BL</sub>	17.28	19.008	20.736	Watt	
Life Time	(MTTF)		30000			Hours	9, 10



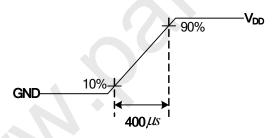


### 3.1.2: AC Characteristics

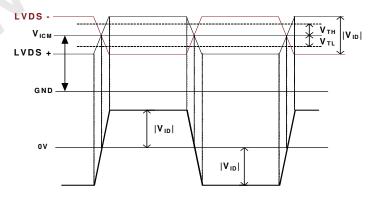
	Parameter	Symbol		Value		Unit	Note	
	Farameter	Symbol	Min.	Тур.	Max	Offic	Note	
	Input Channel Pair Skew Margin	t <sub>SKEW (CP)</sub>	-500		+500	ps	6	
LVDS	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%		Fclk +3%	MHz	7	
Interface	Receiver Clock : Spread Spectrum  Modulation frequency	Fss	30		200	KHz	7	
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	-0.4 -0.5		0.4 0.5	ns	8	

#### Note:

- 1. The ripple voltage should be controlled under 10% of  $\ensuremath{V_{\text{CC}}}$
- 2. Test Condition:
  - (1)  $V_{DD} = 12V$
  - (2) Fv = 60Hz
  - (3)  $F_{CLK} = Max. Freq.$
  - (4) Temperature = 25 °C
  - (5) Test Pattern : White Pattern
- **3.** Measurement condition : Rising time = 400us



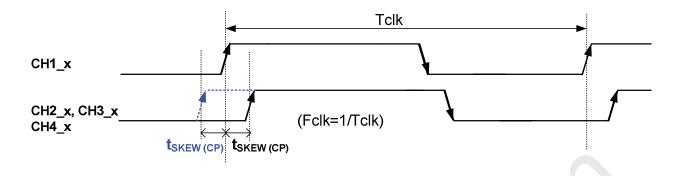
**4.**  $V_{ICM} = 1.25V$ 



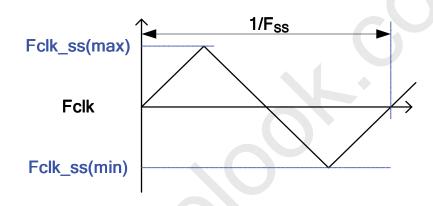
- 5. The measure points of  $V_{IH}$  and  $V_{IL}$  are in LCM side after connecting the System Board and LCM.
- 6. Input Channel Pair Skew Margin







7. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures



### 8. Receiver Data Input Margin

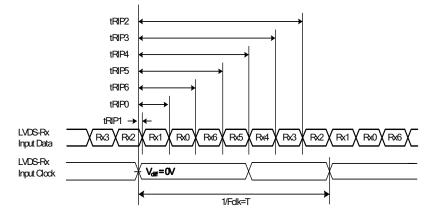
Donomotor	Cymhal		Rating		Unit	Note
Parameter	Symbol	Min	Туре	Max	Unit	Note
Input Clock Frequency	Fclk	Fclk (min)		Fclk (max)	MHz	T=1/Fclk
Input Data Position0	tRIP1	- tRMG	0	tRMG	ns	
Input Data Position1	tRIP0	T/7- tRMG	T/7	T/7+ tRMG	ns	
Input Data Position2	tRIP6	2T/7- tRMG	2T/7	2T/7+ tRMG	ns	
Input Data Position3	tRIP5	3T/7- tRMG	3T/7	3T/7+ tRMG	ns	
Input Data Position4	tRIP4	4T/7- tRMG	4T/7	4T/7+ tRMG	ns	
Input Data Position5	tRIP3	5T/7- tRMG	5T/7	5T/7+ tRMG	ns	
Input Data Position6	tRIP2	6T/7- tRMG	6T/7	6T/7+ tRMG	ns	





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- 9. The relative humidity must not exceed 80% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C. When operate at low temperatures, the brightness of LED will drop and the life time of LED will be reduced.
- 10. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at Ta = 25±2°C]





# Interface Connections (FFC type)

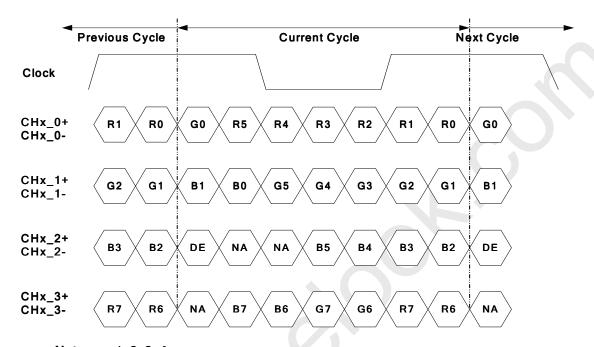
LCD connector: 093G30-B0001A-1 (Starconn, LVDS connector)

		U-BUUU1A-1 (Starconn, LVDS connector)
Customer		STD
Pin No		12V / 30pin
1	V <sub>DD</sub>	Power Supply, +12V DC Regulated
2	V <sub>DD</sub>	Power Supply, +12V DC Regulated
3	V <sub>DD</sub>	Power Supply, +12V DC Regulated
4	V <sub>DD</sub>	Power Supply, +12V DC Regulated
5	GND	Ground
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	LVDS_SEL	Open/High(3.3V) for NS, Low(GND) for JEIDA
10	N.C	AUO Internal Use Only
10	N.C.	(Aging)
11	GND	Ground
12	CH1_0-	LVDS Channel 1, Signal 0-
13	CH1_0+	LVDS Channel 1, Signal 0+
14	GND	Ground
15	CH1_1-	LVDS Channel 1, Signal 1-
16	CH1_1+	LVDS Channel 1, Signal 1+
17	GND	Ground
18	CH1_2-	LVDS Channel 1, Signal 2-
19	CH1_2+	LVDS Channel 1, Signal 2+
20	GND	Ground
21	CH1_CLK-	LVDS Channel 1, Clock -
22	CH1_CLK+	LVDS Channel 1, Clock +
23	GND	Ground
24	CH1_3-	LVDS Channel 1, Signal 3-
25	CH1_3+	LVDS Channel 1, Signal 3+
26	GND	Ground
0.7	N.O.	AUO Internal Use Only
27	N.C.	(SCL - EEPROM Serial Clock)
	NO	AUO Internal Use Only
28	N.C.	(SDA - EEPROM Serial Data)
29	N.C.	No Connection
30	GND	Ground
	l	



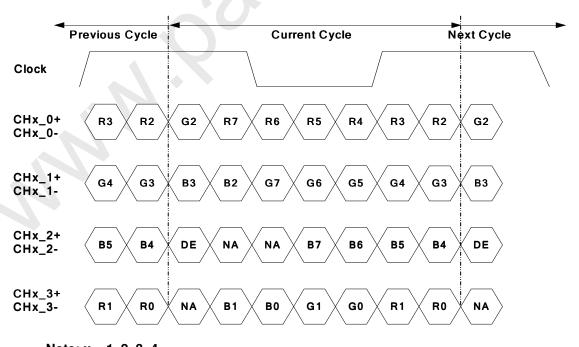
Note: N.C. : please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High).

### FFC Option = High/Open→NS



Note: x = 1, 2, 3, 4...

### LVDS Option = Low→JEIDA



Note: x = 1, 2, 3, 4...



## 3.2 Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

### **Timing Table**

Signal	Item	Symbol	Min.	Тур.	Max	Unit			
	Period	Tv	784	810	1015	Th			
Vertical Section	Active	Tdisp (v)		768					
	Blanking	Tblk (v)	16	42	247	Th			
	Period	Th	1460	1648	2000	Tclk			
Horizontal Section	Active	Tdisp (h)		1366					
	Blanking	Tblk (h)	94	282	634	Tclk			
Clock	Frequency	Fclk=1/Tclk	50	80	86	MHz			
Vertical Frequency	Frequency	Fv	47	60	63	Hz			
Horizontal Frequency	Frequency	Fh	43	48	53	KHz			

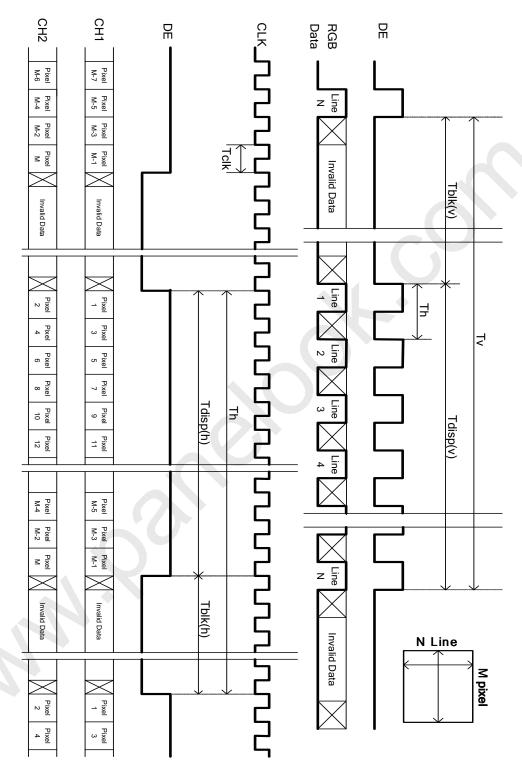
#### Notes:

- (1) Display position is specific by the rise of DE signal only. Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.
- (2) Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1<sup>st</sup> data corresponding to one horizontal line after the rise of 1<sup>st</sup> DE is displayed at the top line of screen.
- (3) If a period of DE "High" is less than 1366 DCLK or less than 768 lines, the rest of the screen displays black.
- (4) The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.





# 3.3 Signal Timing Waveforms







## 3.4 Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

### COLOR DATA REFERENCE

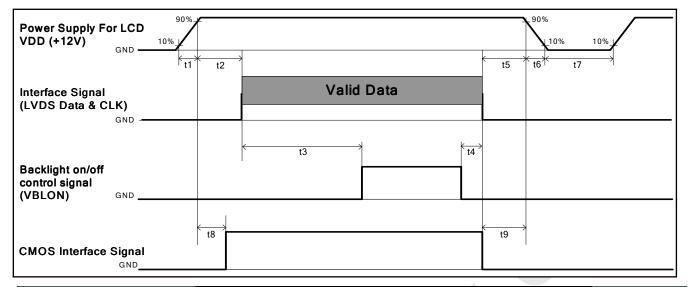
											I	npu	t Co	lor	Data	ì									
	Color	RED								GREEN						BLUE									
	Color	MSB LSB N						MS	MSB LSB					В	MSB LSB										
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	ВЗ	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	10	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R																	•								
	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
G																									
	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
В																									
	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



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## 3.5 Power Sequence for LCD



Developed		l limit			
Parameter	Min. Type.		Max.	Unit	
t1	0.4	0.4		ms	
t2	0.1		150	ms	
t3	450			ms	
t4	0 <sup>*1</sup>			ms	
t5	0			ms	
t6			*2 	ms	
t7	500			ms	
t8	10		50	ms	
t9	0			ms	

#### Note:

- (1) T4=0: concern for residual pattern before BLU turn off.
- (2) T6: voltage of VDD must decay smoothly after power-off. (customer system decide this value)



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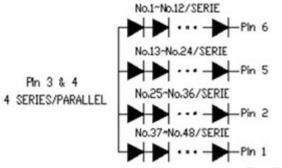
T260XVN01.0 Specification Rev.0.1

### 3.7 Backlight Specification

The backlight unit contains 4pcs light bar.

### 3.7.1 Light bar Driven Condition

Parameter		Symbol	Values			Unit	Note
		Зупио	Min	Тур	Max	Onk	NOIS
Forward Current	Anode	IF (anode)		480		mΑ	
(one light bar)	Cathode	F (cathode)		120	125	mA	
Peak Forward Current		IFP			500	mA	<1msec.
Forward Voltage		VF	36	39.6	43.2	٧	
Forward Voltage Variation		△VF			1.8	V	
Total Power Consumption (4 light bars)		PBL	17.3	19.1	20.8	W	
PWM Operation Frequency		F_PWM	140	180	240	Hz	Note 1&2
PWM Dimming Duty Ratio		D_PWM	10		100	%	



Connector pin assignment						
pin	electrode	Model No.				
Pin 1	. ,					
Pin 2	- 1					
Pin 3	+ 1	CviLux				
Pin 4	(4)	CI1406M1HRE-NH				
Pin 5	Otto Control					
Pin 6	/-					

Note 1: Dimming range



PWM Dimming: include Internal and External PWM Dimming

Note 2: Low dimming ratio operation

When PWM dimming duty ratio is operated lower than recommended value, feedback signal and all protection functions should be confirmed by LIPS design. Display performance should also be confirmed by customer's implement.

Note 3: Low dimming ratio operation

When PWM dimming duty ratio is operated lower than recommended value, feedback signal and all protection functions should be confirmed by LIPS design. Display performance should also be confirmed by customer's implement.

Note 3: Each LED string should be driven by independent current control/feedback circuit.





Note 4: Fuse protection should be added into LIPS circuit to have better LED driving protection.





# 3.7.2 Input Pin Assignment

	P1 -12pin
1	#1-1 Anode 480mA
2	NC
3	#1-1 Cathode(120mA)
4	#1-2 Cathode(120mA)
5	NC
6	NC
7	NC
8	NC
9	#1-3 Cathode 120mA
10	#1-4Cathod 120mA
11	NC
12	#1-1 Anode 480mA

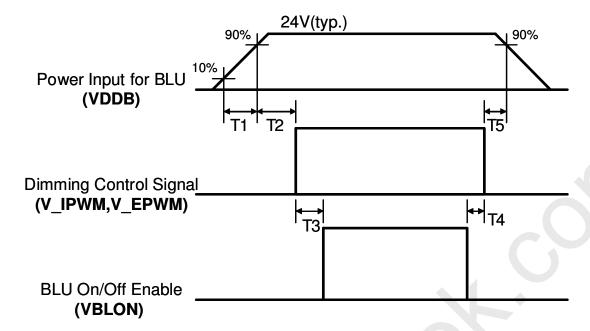




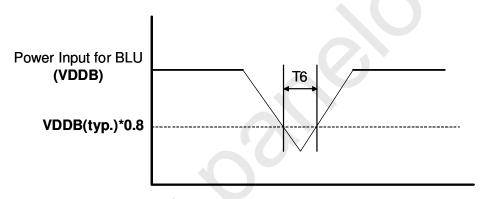
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# 1.7.3 Power Sequence for Backlight (CCFL and LED)



## **Dip condition for Inverter**



Parameter		Units			
Parameter	Min	Тур	Max	Onits	
T1	20	-	-	ms	
T2	500	-	-	ms	
Т3	250	-	-	ms	
T4	0	-	-	ms	
T5	1	-	-	ms	
T6	-	-	10	ms	

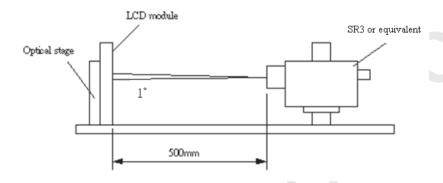




# 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\phi$  and  $\theta$  equal to  $0^{\circ}$ .

Fig.1 presents additional information concerning the measurement equipment and method.



Downston	Values			11.2	Notes	
Parameter	Symbol	Min.	Тур.	Max	Unit	Notes
Contrast Ratio	CR	2400	3000			1
Surface Luminance (White)	L <sub>wH</sub>	240	300		cd/m <sup>2</sup>	2
Luminance Variation	δ <sub>WHITE(9P)</sub>			1.33		3
Response Time (G to G)	Тү		6.5		Ms	4
Color Gamut	NTSC		68		%	
Color Coordinates						
Red	R <sub>X</sub>		0.63			
	R <sub>Y</sub>		0.34			
Green	G <sub>X</sub>		0.34			
	G <sub>Y</sub>	Typ0.03	0.62	Typ.+0.03		
Blue	B <sub>X</sub>	Τ γρυ.υ3	0.15	тур.+0.03		
	B <sub>Y</sub>		0.04			
White	W <sub>X</sub>		0.28			
	W <sub>Y</sub>		0.29			
Viewing Angle						5
x axis, right(φ=0°)	$\theta_{r}$		89		degree	
x axis, left(φ=180°)	θι		89		degree	
y axis, up(φ=90°)	$\theta_{u}$		89		degree	
y axis, down (φ=270°)	$\theta_{\sf d}$		89		degree	
•	•	•		•		





Note:

1. Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio= 
$$\frac{\text{Surface Luminance of L}_{\text{on5}}}{\text{Surface Luminance of L}_{\text{off5}}}$$

- 2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When LED current = 120mA, L<sub>WH</sub>=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.
- 3. The variation in surface luminance,  $\delta WHITE$  is defined (center of Screen) as:

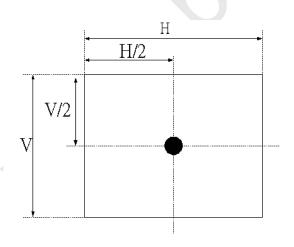
 $\delta_{WHITE(9P)} = Maximum(L_{on1},\,L_{on2},...,L_{on9}) /\, Minimum(L_{on1},\,L_{on2},...L_{on9})$ 

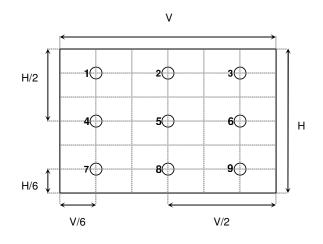
4. Response time  $T_{\gamma}$  is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on  $F_{\nu}$ =60Hz to optimize.

Measured		Target						
Response Time		0%	25%	50%	75%	100%		
	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%		
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%		
Start	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%		
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%		
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%			

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.

#### FIG. 2 Luminance





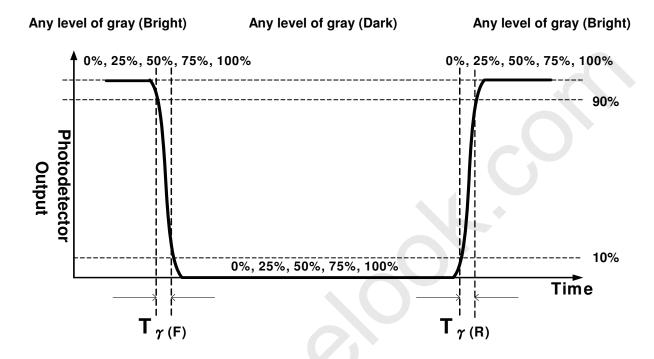


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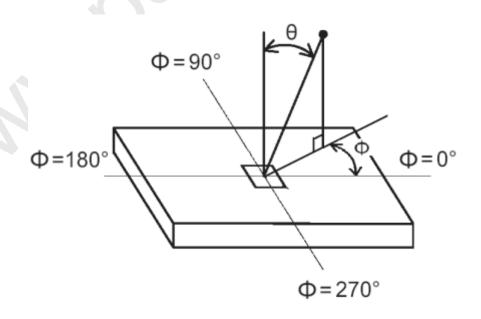
T260XVN01.0 Specification Rev.0.1

### FIG.3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright) " and "any level of gray(dark)".



### FIG.4 Viewing Angle







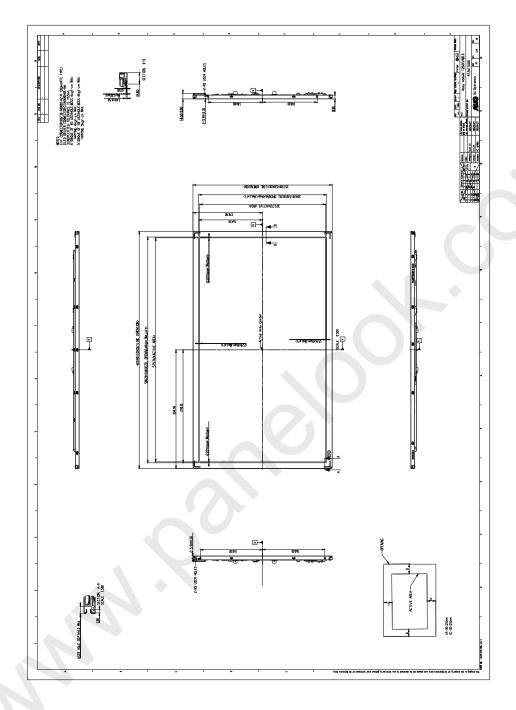
## 5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model T260XVN01.0. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	609.8 mm		
Outline Dimension	Vertical	357.8 mm		
	Depth	14.6 mm (Bezel to T-con cover)		
	Horizontal	580.2		
Bezel Opening	Vertical	328.2		
Active Display Area	Horizontal	575.769		
Active Display Area	Vertical	323.712		
Weight	3100 g (Typ.)			
Surface Treatment	Anti-Glare, 3H			



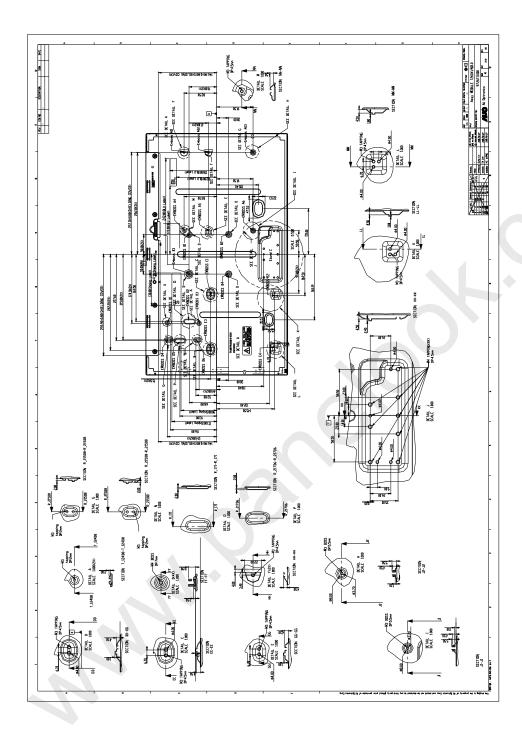
# **Front View**







# **BackView**







# 6. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60°C, 300hrs
2	Low temperature storage test	3	-20℃, 300hrs
3	High temperature operation test	3	50°ℂ, 300hrs
4	Low temperature operation test	3	-5℃, 300hrs
			wave form : random
			Overall average energy level : 1.0Grms
5	Vibration test (non-operation)	3	Level: 10~300Hz
			Duration:X,Y,Z 10min
			one time each direction
			shock level : 50G
6	Shock test (non-operation)	3	wave form :half sine wave 20ms in ±X, ±Y, ±Z axis
			one time each direction
		1CTN/10PCS	
7	Vibration test (With carton)		Random Wave (1.05Grms 10~200Hz) Duration: X,Y,Z10min per axes
		1CTN/10PCS	Height: 38.1 mm
8	Drop test (With carton)	NO.	1 corner, 3 edges , 6 flats (refer ASTM D 5276)





## 7. International Standard

### 7.1 Safety

- (1) UL 60950-1, UL 60065; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.
- (2) IEC 60950-1: 2001, IEC 60065:2001; Standard for Safety of International Electrotechnical Commission
- (3) EN 60950 : 2001+A11, EN 60065:2002+A1:2006; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

#### **7.2 EMC**

- (1) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992
- (2) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.
- (3) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998



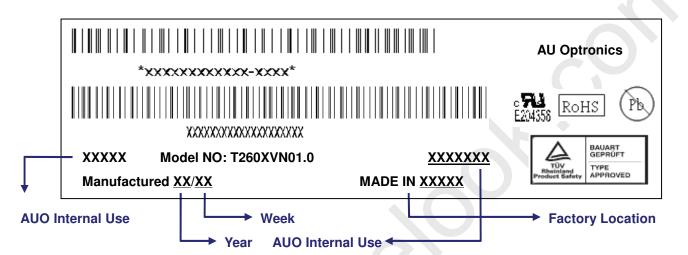


# 8. Packing

### 8-1 DEFINITION OF LABEL:

A. Panel Label:



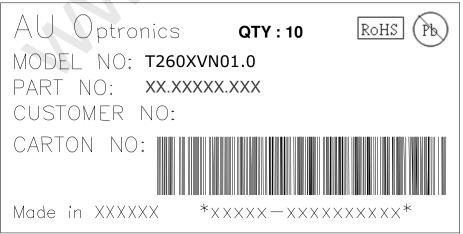


#### **Green mark description**

- (1) For Pb Free Product, AUO will add hor identification.
- (2) For RoHs compatible products, AUO will add RoHS for identification.

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

#### **B. Carton Label:**

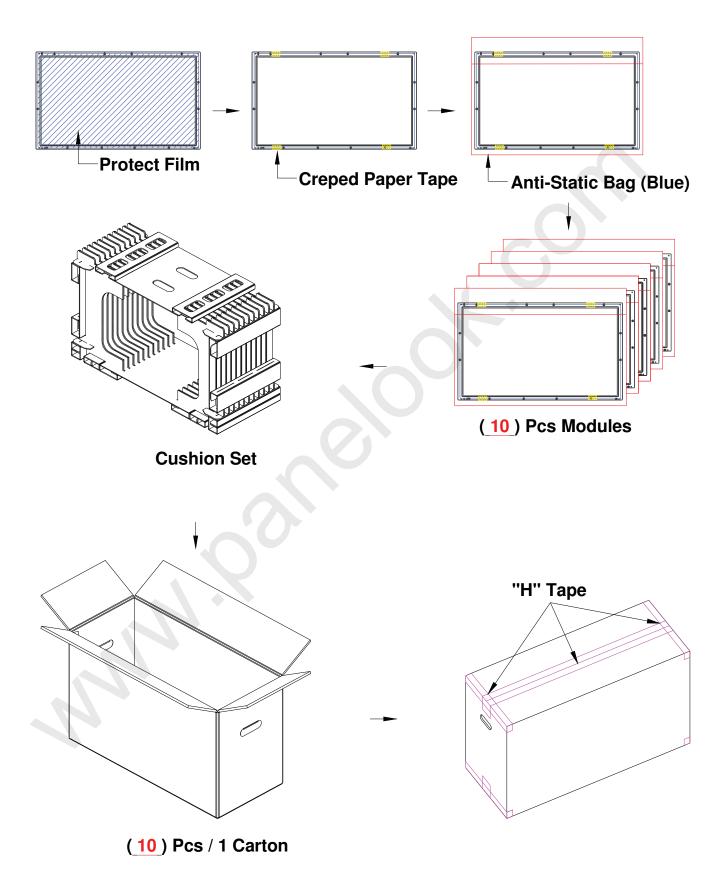


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## 8-2 PACKING METHODS:

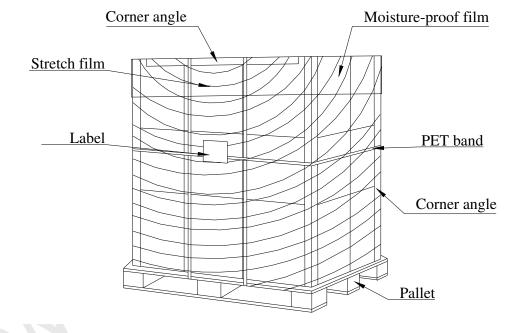






# 8-3 Pallet and Shipment Information

Item		Packing Remark			
item	Qty.	Qty. Dimension Total Weight (kg)		I acking Helliaik	
Packing POV	10pgs/boy	690/L\*279/M\\*450/U\	04.01	Box = 1.34kg	
Packing BOX	10pcs/box	680(L)*378(W)*450(H)	34.8kg	Cushion = 2.29kg	
Pallet	1	1150(L)*690(W)*132(H)	13.5kg		
Boxes per Pallet					
	B. 9 boxes/pallet				
Panels per Pallet					
Pallet after packing	A. 1150(L)*690(W)*1032(H) A. 222.3kg				
(40' container)	00	B. 1150(L)*690(W)*1482(H)	B. 326.7kg		







## 9.PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

### 9-1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2 OPERATING PRECAUTIONS

- (1) The device listed in the product specification sheets was designed and manufactured for TV application
- (2) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (3) Response time depends on the temperature. (In lower temperature, it becomes longer..)
- (4) Brightness of CCFL depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (5) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (6) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (7) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall





be done by system manufacturers. Grounding and shielding methods may be important to minimize the interface.

#### 9-3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

#### 9-4 PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

#### 9-5 STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

#### 9-6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.